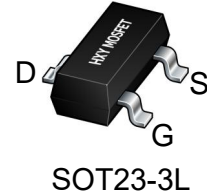




Description

The HXY3401MI uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

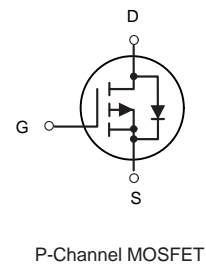


General Features

$V_{DS} = -30V$ $I_D = -4.2A$
 $R_{DS(ON)} < 54m\Omega @ V_{GS}=10V$
 $R_{DS(ON)} < 77m\Omega @ V_{GS}=4.5V$

Application

Battery protection
Load switch
Uninterruptible power supply



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
HXY3401MI	SOT23-3L	X1KX	3000

Absolute Maximum Ratings ($T_A=25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V_{DS}	Drain-Source Voltage	-30	V
V_{GS}	Gate-Source Voltage	± 12	V
I_D	Drain Current-Continuous	-4.2	A
I_{DM}	Drain Current-Pulsed (Note 1)	-30	A
P_D	Maximum Power Dissipation	1.2	W
T_J, T_{STG}	Operating Junction and Storage Temperature Range	-55 To 150	$^{\circ}C$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient (Note 2)	104	$^{\circ}C/W$



Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-24V, V_{GS}=0V$	-	-	-1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 10V, V_{DS}=0V$	-	-	± 100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.7	-1	-1.3	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=-10V, I_D=-4.2A$	-	46	54	m Ω
		$V_{GS}=-4.5V, I_D=-4A$	-	58	77	m Ω
		$V_{GS}=-2.5V, I_D=-1A$		74	130	m Ω
Forward Transconductance	g_{FS}	$V_{DS}=-5V, I_D=-4.2A$	-	10	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C_{ISS}	$V_{DS}=-15V, V_{GS}=0V,$ $F=1.0MHz$	-	880	-	PF
Output Capacitance	C_{OSS}		-	105	-	PF
Reverse Transfer Capacitance	C_{RSS}		-	65	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=-15V, I_D=-4.2A$ $V_{GS}=-10V, R_{GEN}=6\Omega$	-	7	-	nS
Turn-on Rise Time	t_r		-	3	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	30	-	nS
Turn-Off Fall Time	t_f		-	12	-	nS
Total Gate Charge	Q_g	$V_{DS}=-15V, I_D=-4.2A, V_{GS}=-4.5V$	-	8.5	-	nC
Gate-Source Charge	Q_{gs}		-	1.8	-	nC
Gate-Drain Charge	Q_{gd}		-	2.7	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	$V_{GS}=0V, I_S=-4.2A$	-	-	-1.2	V

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production



Typical Electrical and Thermal Characteristics

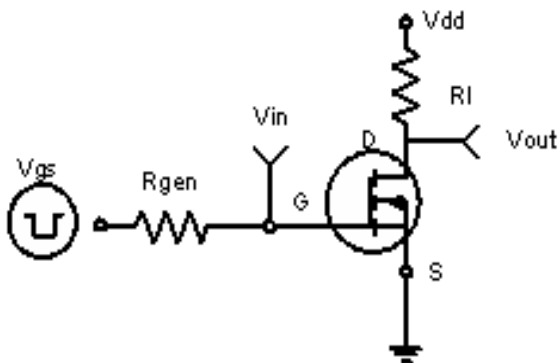


Figure 1: Switching Test Circuit

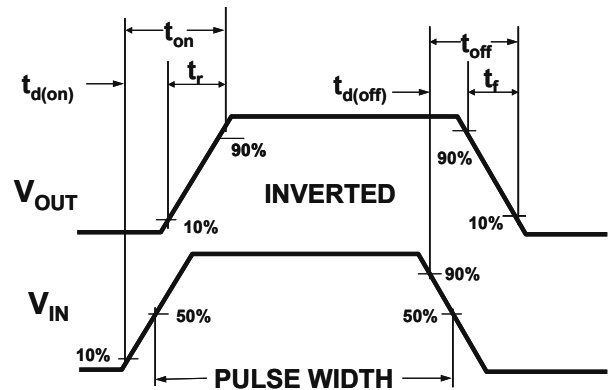


Figure 2: Switching Waveforms

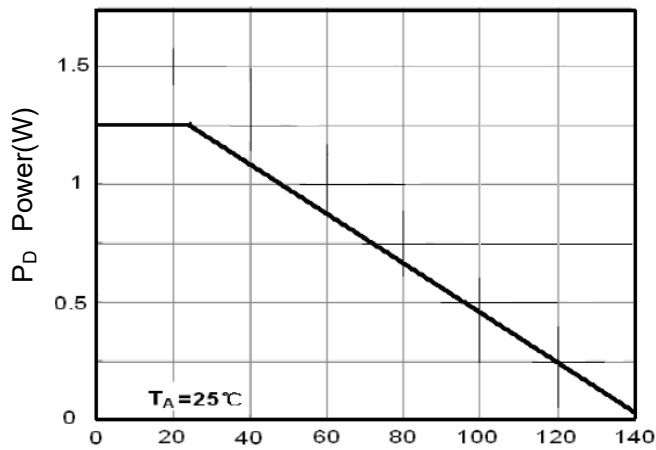


Figure 3 Power Dissipation

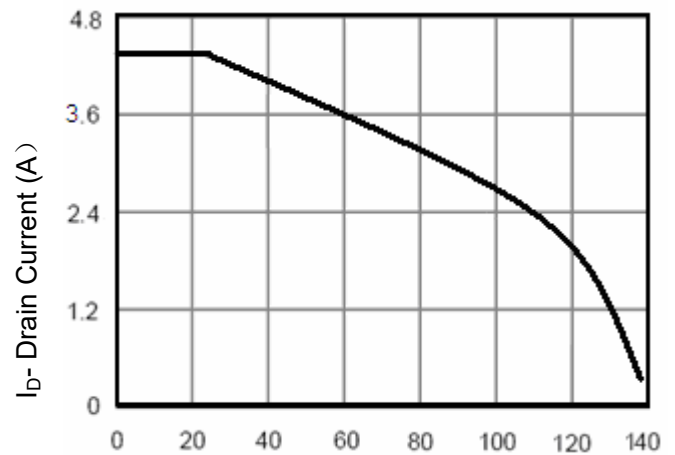


Figure 4 Drain Current

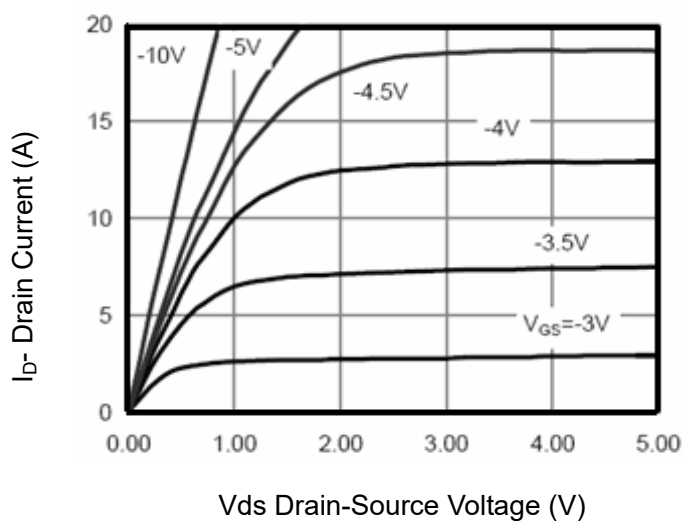


Figure 5 Output Characteristics

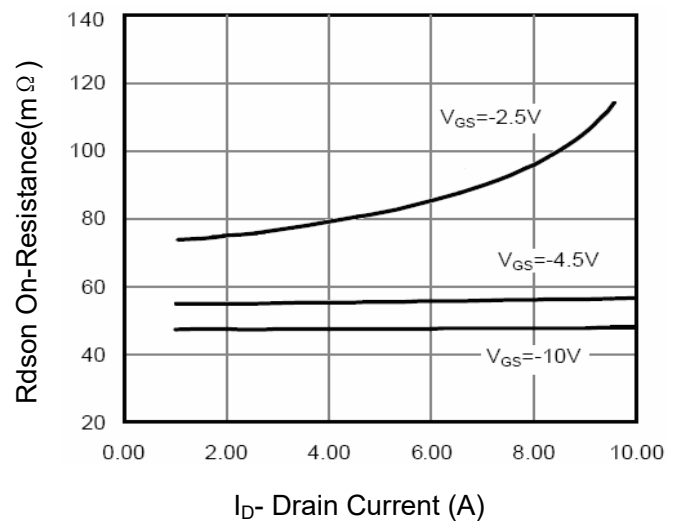


Figure 6 Drain-Source On-Resistance

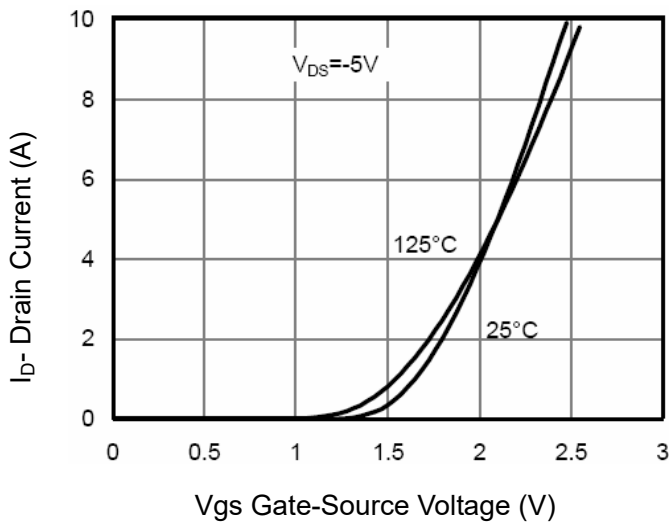


Figure 7 Transfer Characteristics

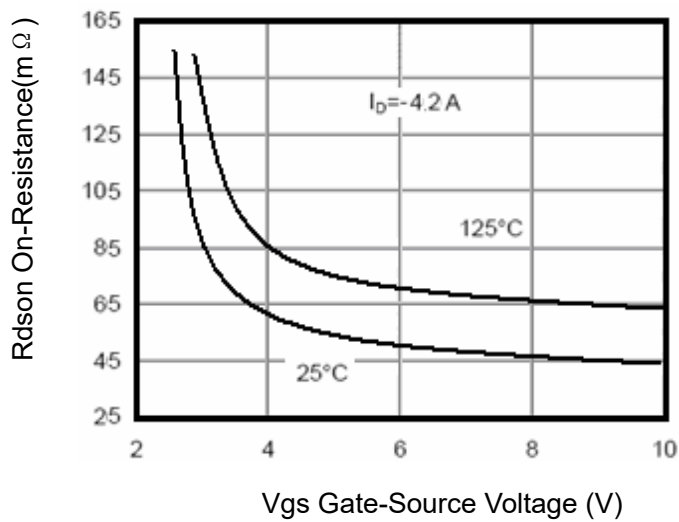


Figure 9 $R_{DS(on)}$ vs V_{GS}

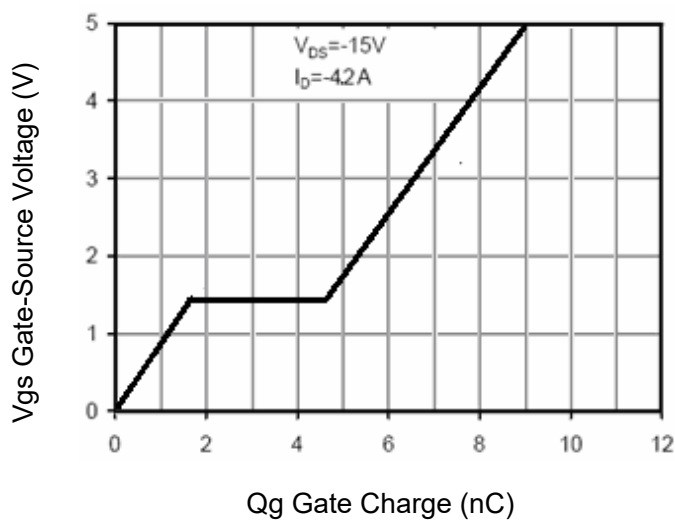


Figure 11 Gate Charge

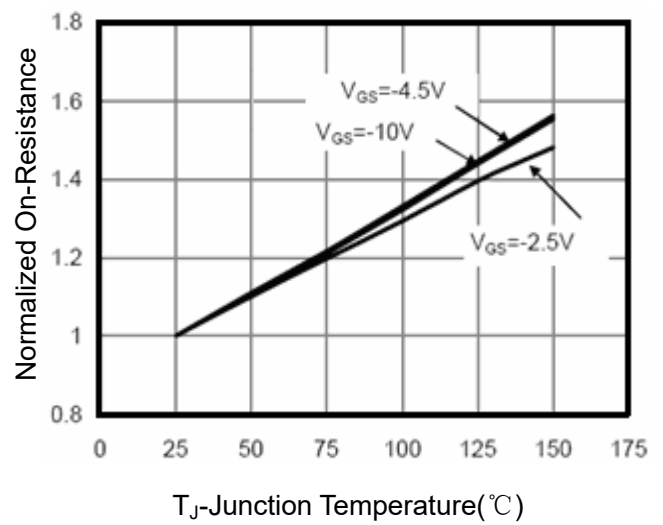


Figure 8 Drain-Source On-Resistance

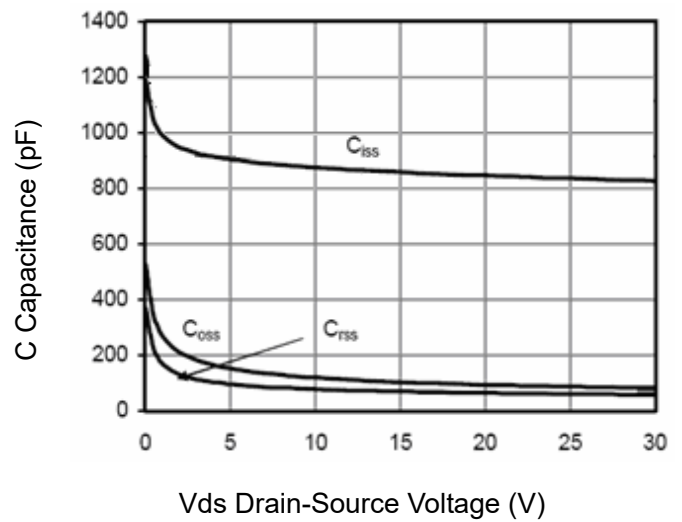


Figure 10 Capacitance vs V_{DS}

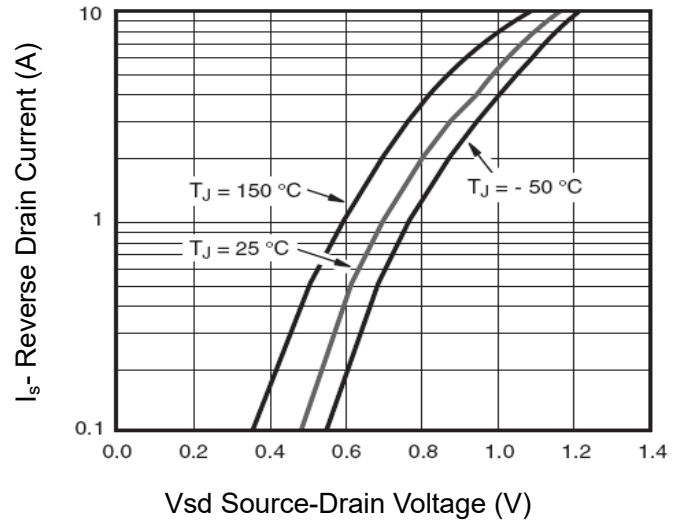


Figure 12 Source- Drain Diode Forward

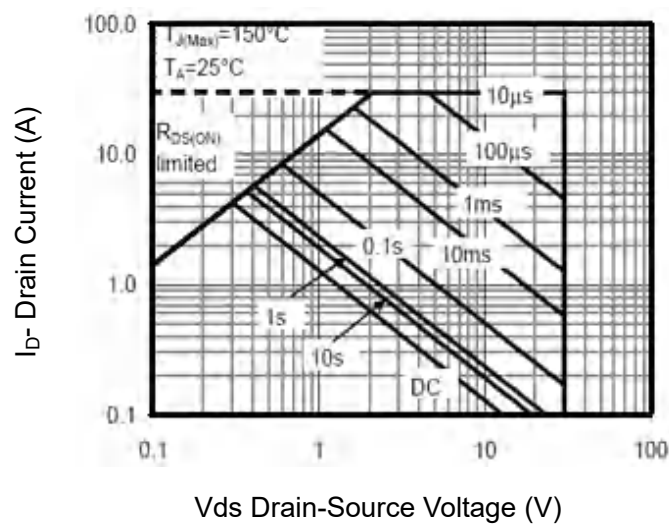


Figure 13 Safe Operation Area

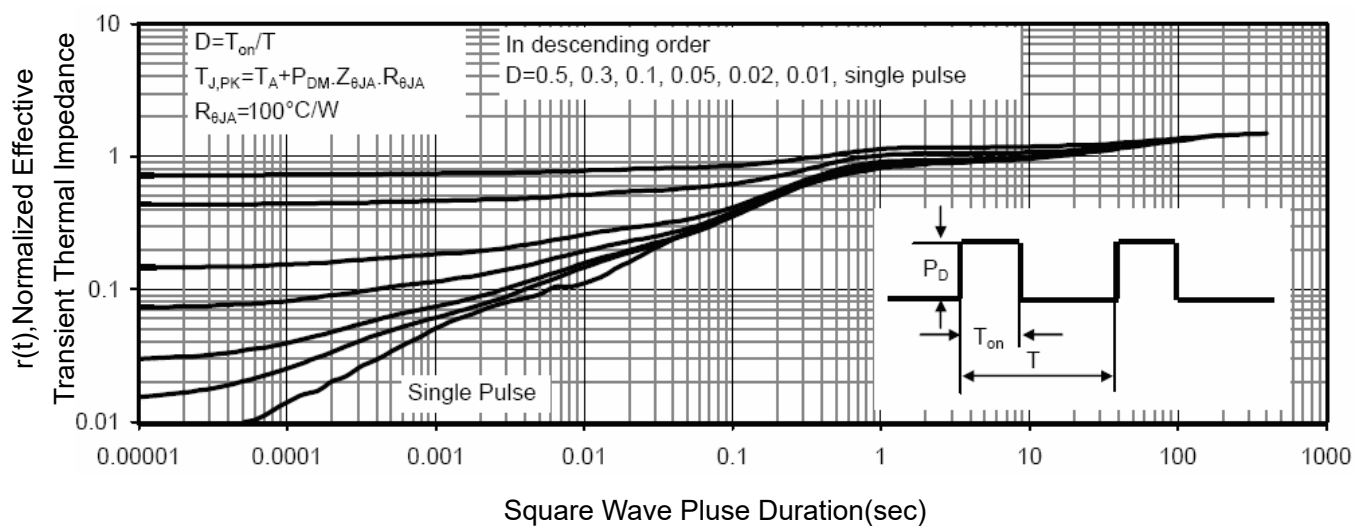
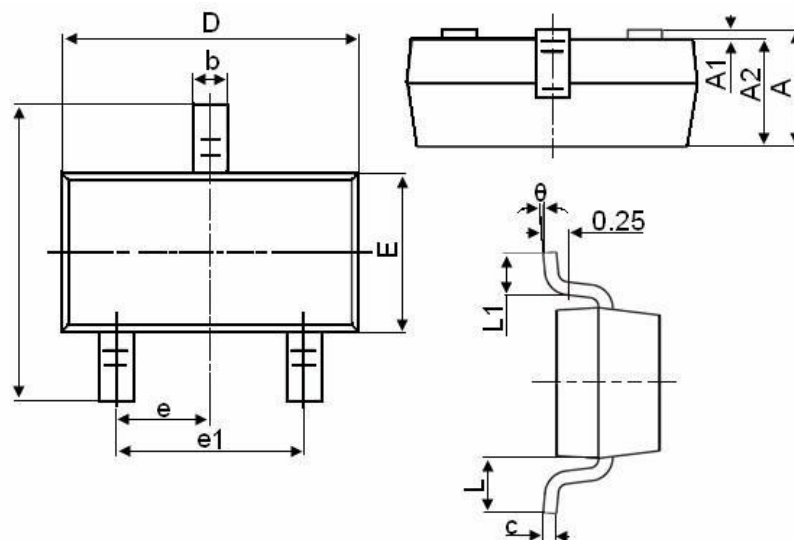


Figure 14 Normalized Maximum Transient Thermal Impedance



SOT23-3L Package Information



Symbol	Dimensions in Millimeters	
	MIN.	MAX.
A	1.050	1.250
A1	0.000	0.100
A2	1.050	1.150
b	0.300	0.500
c	0.100	0.200
D	2.800	3.000
E	1.500	1.700
E1	2.650	2.950
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.600
θ	0°	8°



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