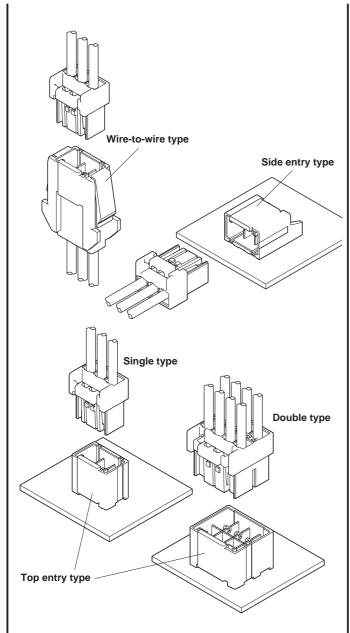


## 4.0mm pitch/Disconnectable Crimp style connectors



This PSI connector is designed for wire-to-board and wire-to-wire 4.0 mm pitch connector. Low insertion force type contact is adopted and it provides excellent operability. This connector has the secure locking device that has the mechanism for preventing the inverse insertion. By adopting key shape and multi colors of housing, prevention function of mis-mating is considered.

- Secure locking structure
- Mis-mating prevention mechanism by keying (2 to 4 circuits/3 kinds of keying)
- Finger-friendly design
- Large electric current was realized.

## Specifications —

• Current rating: 12 A AC, DC (Refer to the following table.)

• Voltage rating: 300 V AC, DC

• Temperature range: -25°C to +85°C

(including temperature rise in applying

electrical current)

• Contact resistance: Initial value/ 10 m $\Omega$  max.

After environmental tests/ 20 m $\Omega$  max.

Insulation resistance: 1,000 MΩ min.
Withstanding voltage: 1,500 VAC/minute
Applicable wire: AWG #26 to #16

\* Refer to "General Instruction and Notice when using Terminals and Connectors" at the end of this catalog.

\* Contact JST for details.

\* Compliant with RoHS.

Note: The current rating differs depending on the number of circuits and the wire size used in each connector. The table below lists the current rating as a function of the number of circuits and the wire size.

			Current unit. A						
	Circuits	Wire size (AWG)							
		# 16	# 18	# 20	# 22	# 24	# 26		
	2	12	10	8	5	4	3		
	3	11	9	7	5	4	3		
	4, 5, 6, 8	10	8	6	5	4	3		
	10, 12	9	7	6	5	4	3		
	14	9	7	6	4.5	4	3		

Note: Do not branch in parallel current which exceeds the rated current (e.g. more than 12 A in the case of 2 circuits with AWG #12). If branched in parallel, current imbalance or other problems may develop. If it is absolutely necessary to branch such a large current in parallel, design the circuits without causing any imbalance and provide an extra margin for each circuit.

#### Standards -

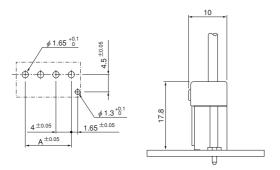
Recognized E 60389

Certified LR 20812

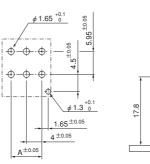
△ R50259465

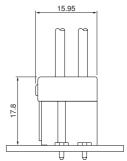
## PC board layout and Assembly layout

# Top entry type ●Single type

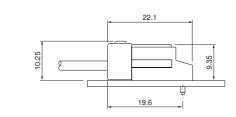


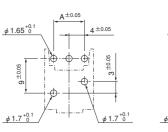
#### Double type



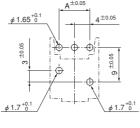


#### Side entry type



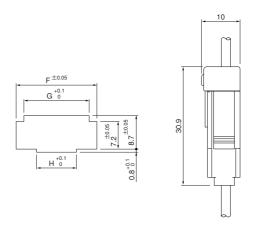


Keying: A, B types



Keying: C type

Wire-to-wire type

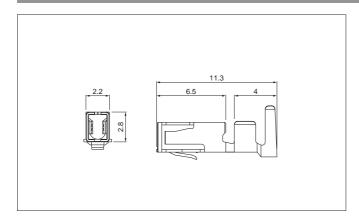


<u> </u>	F	=			Applicable panel
Circuits	0.8≤t≤1.2	1.2 <t≤2.0< td=""><td>G</td><td>  H</td><td>thickness (mm)</td></t≤2.0<>	G	H	thickness (mm)
2	13	13.3	9	6.8	0.8~2.0
3	17	17.3	13	8.4	0.0.92.0

#### Note: 1. The above figure is the figure viewed from the connector mounting side.

- 2. Tolerances are non-cumulative: ±0.05 mm for all centers.
- 3. Hole dimensions differ according to the type of PC board and piercing method. The dimensions above should serve as a guideline. Contact JST for details.

## Socket contact



Ī	Madel No	Applica	ble wire	Inculation O.D. (mm)	O'ty/rool
	Model No.	mm²	AWG#	Insulation O.D. (mm)	Q ty/reer
	SPSI-001T-M1.1	0.13~0.33	26~22	1.3~2.4	2,600
	SPSI-41T-M1.1	0.5 ~1.25	20~16	1.7~3.2	2,600

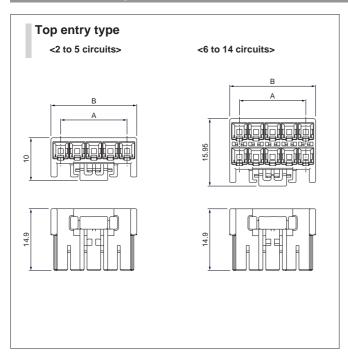
#### Material and Finish

Copper alloy, tin-plated

#### RoHS compliance

<b>2</b>	Crimping		Applicator			
Contact	machine	Crimp applicator	Dies	Crimp applicator with dies		
SPSI-001T-M1.1		MKS-L	MK/SPSI/M-001-11	APLMK SPSI/M001-11		
3F3I-0011-W11.1	AP-K2N	_	_	_		
CDCL 44T M4 4	AFTIZIN	MKS-L	MK/SPSI/M-41-11	APLMK SPSI/M41-11		
SPSI-41T-M1.1		_	_	_		

#### Socket housing



Circuits	Keying	Model No.	Dimension	ons (mm)	Q'ty/
Onouno	rtoying	Wodel 140.	Α	В	box
	Α	PSIP-02V-LE-A	4.0	8.7	500
2	В	PSIP-02V-Y-B	4.0	8.7	500
	C PSIP-02V-R-C	4.0	8.7	500	
	Α	PSIP-03V-LE-A	8.0	12.7	300
3	В	PSIP-03V-Y-B	8.0	12.7	300
	С	PSIP-03V-R-C	8.0	12.7	300
	Α	PSIP-04V-LE-A	12.0	16.7	200
4	В	PSIP-04V-Y-B	12.0	16.7	200
	С	PSIP-04V-R-C	12.0	16.7	200
5	_	PSIP-05V-LE	16.0	20.7	200
6	_	PSIP-06V-LE	8.0	12.7	200
8	_	PSIP-08V-LE	12.0	16.7	200
10	_	PSIP-10V-LE	16.0	20.7	150
12	_	PSIP-12V-LE	20.0	24.7	150
14	_	PSIP-14V-LE	24.0	28.7	100

#### Material

Glass-filled PBT, UL94V-0

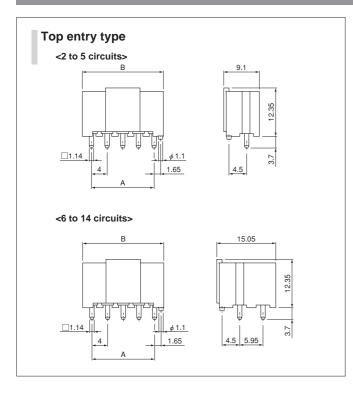
#### RoHS compliance

<For reference> As the color identification,

the following alphabet shall be put in the underlined part. For availability, delivery and minimum order quantity, contact JST.

ex. PSIP-02V-LE-A LE...light blue Y...yellow R...red

### Header



Circu	its Keying	Model No.	Dimension	ons (mm)	Q'ty/
Circu	its reging	Model No.	Α	В	box
	Α	B02B-PSILE-A1	4.0	8.7	500
2	2 B <b>B02B-PSIY-B1</b> C <b>B02B-PSIR-C1</b>	B02B-PSIY-B1	4.0	8.7	500
		4.0	8.7	500	
	Α	B03B-PSILE-A1	8.0	12.7	300
3	В	B03B-PSIY-B1	8.0	12.7	300
	С	B03B-PSIR-C1	8.0	12.7	300
	А	B04B-PSILE-A1	12.0	16.7	200
4	В	B04B-PSIY-B1	12.0	16.7	200
	С	B04B-PSIR-C1	12.0	16.7	200
5	_	B05B-PSILE-1	16.0	20.7	200
6	_	B06B-PSILE-1	8.0	12.7	200
8	_	B08B-PSILE-1	12.0	16.7	200
10	_	B10B-PSILE-1	16.0	20.7	150
12	_	B12B-PSILE-1	20.0	24.7	125
14	_	B14B-PSILE-1	24.0	28.7	100

Material and Finish

Post: Copper alloy, copper-undercoated, tin-plated (reflow treatmnent) Header: Glass-filled PBT, UL94V-0

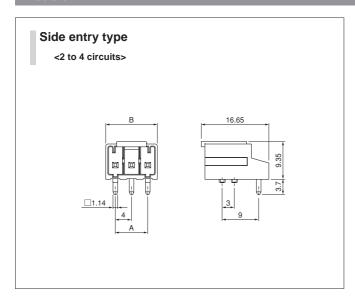
**RoHS compliance** This product displays (LF)(SN) on a label. Note: Other kinds of post-omitted products are available. Contact JST for details.

<For reference> As the color identification,

the following alphabet shall be put in the underlined part. For availability, delivery and minimum order quantity, contact JST.

ex. B02B-PSILE-A1 LE...light blue Y...yellow R...red

## Header



_						
(	Circuits	Keying	Model No.	Dimension	ons (mm)	Q'ty/
	Jiicuits	rtcynig	Wodel No.	Α	В	box
		Α	S02B-PSILE-A1	4.0	8.7	350
	2	В	S02B-PSIY-B1	4.0	8.7	350
		С	S02B-PSIR-C2	4.0	8.7	350
		Α	S03B-PSILE-A1	8.0	12.7	200
	3	В	S03B-PSIY-B1	8.0	12.7	200
		С	S03B-PSIR-C2	8.0	12.7	200
		Α	S04B-PSILE-A1	12.0	16.7	150
	4	В	S04B-PSIY-B1	12.0	16.7	150
		С	S04B-PSIR-C2	12.0	16.7	150

#### Material and Finish

Post: Copper alloy, copper-undercoated, tin-plated (reflow treatmnent) Header: Glass-filled PBT, UL94V-0

RoHS compliance This product displays (LF)(SN) on a label.

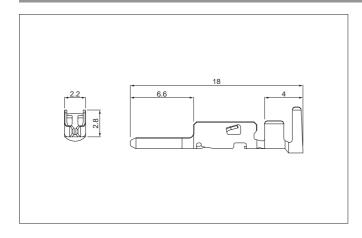
Note: Other kinds of post-omitted products are available. Contact JST for details. <For reference> As the color identification,

the following alphabet shall be put in the underlined part.

For availability, delivery and minimum order quantity, contact JST.

ex. S02B-PSILE-A1 LE...light blue Y...yellow R...red

## Pin contact



Model No.	Applica	ble wire	Insulation O.D. (mm)	O'ty/rool	
Model No.	mm²	AWG#	msulation O.D. (min)	Q ty/leel	
SPSM-001T-M1.1	0.13~0.33	26~22	1.3~2.4	2,600	
SPSM-41T-M1.1	0.5 ~1.25	20~16	1.7~3.2	2,600	

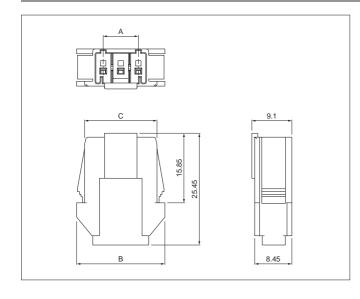
#### Material and Finish

Copper alloy, tin-plated

#### RoHS compliance

	Crimping		Applicator	
Contact	machine	Crimp applicator	Dies	Crimp applicator with dies
SPSM-001T-M1.1		MKS-L	MK/SPSI/M-001-11	APLMK SPSI/M001-11
3P3W-0011-W1.1	AP-K2N	_	_	_
SPSM-41T-M1.1	AF-KZIN	MKS-L	MK/SPSI/M-41-11	APLMK SPSI/M41-11
5P5W-411-W1.1		_	_	_

## Receptacle housing



Circuits	Keying Model No.		Dime	Q'ty/		
Circuits	Reynig	Wodel No.	Α	В	С	bag
	Α	PSIR-02V-LE-A	4.0	16.1	12.5	200
	В	PSIR-02V-Y-B	4.0	16.1	12.5	200
3	Α	PSIR-03V-LE-A	8.0	20.1	16.5	150
3	В	PSIR-03V-Y-B	8.0	20.1	16.5	150

## Material

Glass-filled PBT, UL94V-0

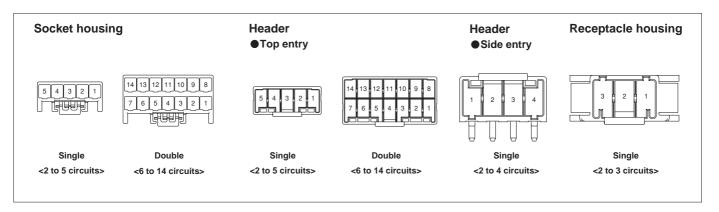
#### RoHS compliance

<For reference> As the color identification, the following alphabet shall be put in the underlined part. For availability, delivery and minimum order quantity, contact JST.

ex. PSIR-03V-LE-A

LE...light blue Y...yellow

## Contact position location numbers



#### Pin-omitted Header

1) When setting two times of pitch in single type header with omitting every other one pin However, pins shall be inserted in No.1-circuit and No. N-circuit.

e.g.

e.g.)

B\*1 (8.0)B-PSI

\*1; No. of circuits (No. of pins)

○; With circuit (pin)

×; Without circuit (pin)

)	Circuit No.	3	2	1
	Circuit (pin)	0	×	0
	Model No.	B2(8.0	)B-PSI	LE-A1

Circuit No.	5	4	3	2	1
Circuit (pin)	0	×	0	×	0
Model No.		B3(8.	0)B-PS	ILE-1	

2) When setting two times of pitch in double type header with omitting every other one pin However, pins shall be inserted in the both ends of circuit.

- \*1; No. of circuits (No. of pins)
- \*2; Circuit No. of used original header
- \*3; Circuit number without pin (Circuit number in which pin was removed)

Circuit No.	6	4					
Circuit (pin)	0	0					
Circuit No.	3	2	1				
Circuit (pin)	0	0					
Model No.	B4(6-2, 5)B-PSILE-1						

3) When setting three times of pitch in double type header with omitting every other two pins However, pins shall be inserted in the both ends of circuit.

e.g.)	Circuit No.	14	13	12	11	10	9	8		
	Circuit (pin)	0	×	×	0	×	×	×		
	Circuit No.	7	6	5	4	3	2	1		
	Circuit (pin)	0	×	×	×					
	Model No.	B6(14-2, 3, 5, 6, 9, 10, 12, 13)B-PSILE-1								

4) When omitting a pin of No.3 circuit in single type herder However, pins shall be inserted in the both ends of circuit.

e.g.)	Circuit No.	5	4	3	2	1
	Circuit (post)	0	0	×	0	0
	Model No.		B4(5-	-3)B-PS	ILE-1	

Note) As for pin-omitted product, there is a setting that cannot be used. Contact JST for details.

## Keying

#### ■ Socket housing

Sha	ape	A type				В	уре	C type			
		<2 circu	uits>	<3 circuits>	<2 circ	uits>	<3 circuits>	<2 circ	uits> <	3 circuits>	
Mating part											
		<4 circuits>		<4 circ	uits>		<4 circuits>				
	2 circuits		Р	SIP-02V-LE-A		PS	IP-02V-Y-B		PSIP-	-02V-R-C	
Model	3 circuits	Blue	Р	SIP-03V-LE-A	Yellow	Yellow PSIP-03V-Y-B		Red	PSIP-	-03V-R-C	
No.	4 circuits	PSIP-04V-LE-A			PS	PSIP-04V-Y-B		PSIP-	-04V-R-C		

From 2 circuit to 4 circuit are applied.

# ■ Header Top entry type

Sha	аре	A type				В	type	C type		
Mating part		<2 circu	its>	<3 circuits>	<2 circ	uits>	<3 circuits>	<2 circ	uits>	<3 circuits>
		<4 circu	اللود تيا		<4 circ	[F <del>5</del> ]		<4 circ	[	
	2 circuits	rcuits B02B-PSILE-A1		B02B-PSIY-B1		02B-PSIY-B1	B02B-PSIR-C1		3-PSIR-C1	
Model	3 circuits	Blue	Е	303B-PSILE-A1	Yellow	bw B03B-PSIY-B1		Red	B03B-PSIR-C1	
No.	4 circuits		E	304B-PSILE-A1		В	04B-PSIY-B1		B04E	3-PSIR-C1

#### Side entry type

From 2 to 4 circuits are applied.

Sha	ape		A typ	9	B type				C type			
		<2 circu			<2 circ	uits>	<3 circuits>	<4 circuits>	<2 circuit		<4 circuits>	
Matin	g part	<2 circu	its> <3 circuits>	<4 circuits>	<2 circ	uits>	<3 circuits>	<4 circuits>	<2 circuit	s> <3 circuits>	<4 circuits>	
		0										
	2 circuits		S02B-PSILE-A1   Yello   S03B-PSILE-A1   Yello   S04B-PSILE-A1			\$02B-PSIY-B1 Yellow \$03B-PSIY-B1			Note)		PSIR-C2	
Model No.	3 circuits	Blue			Yellow				Red	S02B-PSIR-C2 S03B-PSIR-C2		
INO.	4 circuits					S04B-PSIY-B1			S04B-PSIR-C2			
From 2 to 4 circuit are applied												

### ■ Receptacle housing

From 2 to 4 circuit are applied.

Note) Boss: Only C type boss position is reversed.

