TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

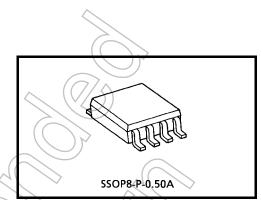
TC7WB125FK

Dual Bus Switch

The TC7WB125FK is a low on-resistance, high-speed CMOS 2-bit bus switch. This bus switch allows the connections or disconnections to be made with minimal propagation delay while maintaining Low power dissipation which is the feature of CMOS.

When output enable (\overline{OE}) is at low level, the switch is on; when at high level, the switch is off.

All inputs are equipped with protector circuits to protect the device from static discharge.

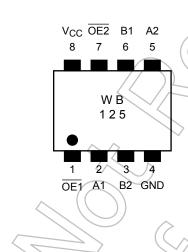


Weight: 0.01 g (typ.)

Features

- Operating voltage: $V_{CC} = 4.5$ to 5.5 V
- High speed operation: $t_{pd} = 0.25 \text{ ns (max)}$
- Ultra-low on resistance: $R_{ON} = 5 \Omega$ (typ.)
- ESD performance: Machine model $\geq \pm 200 \text{ V}$ Human body model $\geq \pm 2000 \text{ V}$
- TTL level input (control input)
- Package: US8

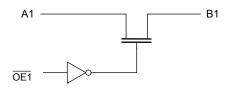
Pin Assignment (top view)

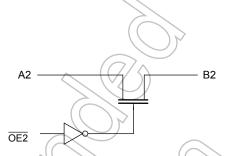


Truth Table

Inputs	Function			
ŌE	Tunction			
L	A port = B port			
Н	Disconnect			

System Diagram





Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	-0.5 to 7.0	У
Control pin input voltage	V_{IN}	-0.5/to 7.0	> v
Switch terminal I/O voltage	VS	-0.5 to 7.0	٧
Clump diode current	I _{IK}	-50	mA
Switch I/O current	I _S	128	mA
Power dissipation	PD	200	mW
DC V _{CC} /GND current	I _{CC} /I _{GND}	±100	mA
Storage temperature	T _{stg}	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Power supply voltage	Vec	4.5 to 5.5	V
Control pin input voltage	V _{IN}	0 to 5.5	٧
Switch I/O voltage	Vs	0 to 5.5	V
Operating temperature	T _{opr}	-40 to 85	°C
Control pin input rise/fall time	dt/dv	0 to 10	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

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Electrical Characteristics

DC Characteristics ($Ta = -40 \sim 85$ °C)

Character	istics	Symbol	Test Condition Vcc (V		V _{CC} (V)	Min	Typ. (Note 1)	Max	Unit
Control pin input	"H" level	V_{IH}	_		4.5 to 5.5	2.0	_	_	V
voltage	"L" level	V _{IL}	_		4.5 to 5.5	6	//	0.8	V
Input leakage cur	rent	I _{IN}	V _{IN} = 0 to 5.5 V 4.5		4.5 to 5.5	+/) Y	±1.0	μΑ
Power off leakage	current	l _{OFF}	A, B, $\overline{OE} = 0 \text{ to } 5.5 \text{ V}$		0	771		±1.0	μΑ
Off-state leakage (switch		I _{SZ}	A, B = 0 to 5.5 V, OE = V _{CC}		4.5 to 5.5		_	±1.0	μА
			V _{IS} = 0 V	I _{IS} = 64 mA		1)/—	5	7	
ON resistance (Note 2)	RON	VIS = U V	I _{IS} = 30 mA	4.5		5	7	Ω	
			$V_{IS} = 2.4 \text{ V}, I_{IS} = 15 \text{ m/s}$	4	4.5		10	15	
Quiescent supply current		Icc	$V_{IN} = V_{CC}$ or GND $I_{OUT} = 0$		5.5	<u> </u>	9	10	μА
		Δlcc	V _{IN} = 3.4 V (one input)		5.5	+	74/	2.5	mA

Note1: The typical values are at $V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}\text{C}$.

Note2: Apply the specified current to the switch, then measure the voltages on pins A and B. The on-resistance is the lower of the two.

AC Characteristics ($Ta = -40 \sim 85$ °C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time (bus to bus)	t _{pLH}	Figure 1, Figure 2 (Note)	4.5	_	0.25	ns
Output enable time	t _{pZL}	Figure 1, Figure 3	4.5	_	4.0	ns
Output disable time	t _{pLZ}	Figure 1, Figure 3	4.5	_	5.0	ns

Note: The propagation delay time is calculated by the RC (on-resistance and load capacitance) time constant.

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Тур.	Unit
Control pin input capacitance	CIN		(Note)	5.0	3	pF
Switch terminal capacitance	C _{I/O}	OE = V _{CC}	(Note)	5.0	10	pF

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Note: This item is guaranteed by design.

AC Test Circuit

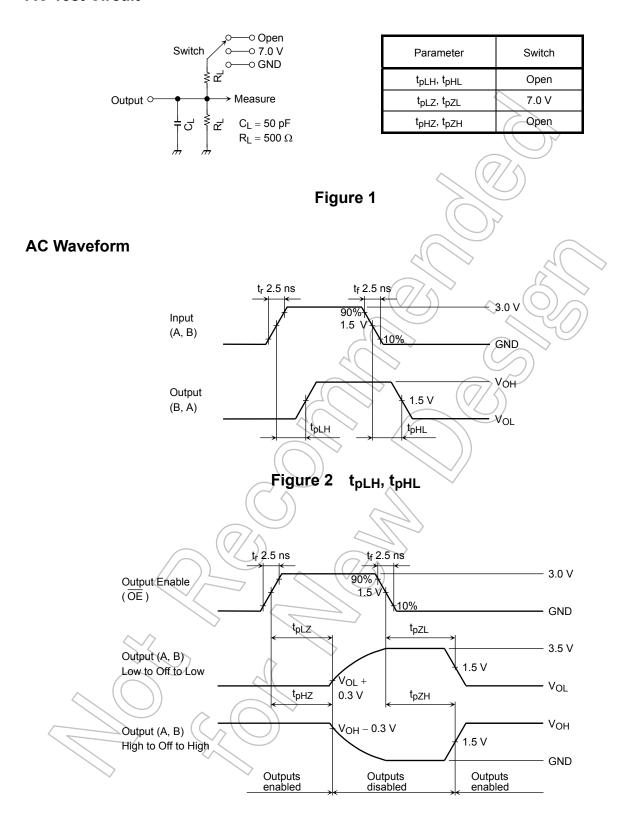
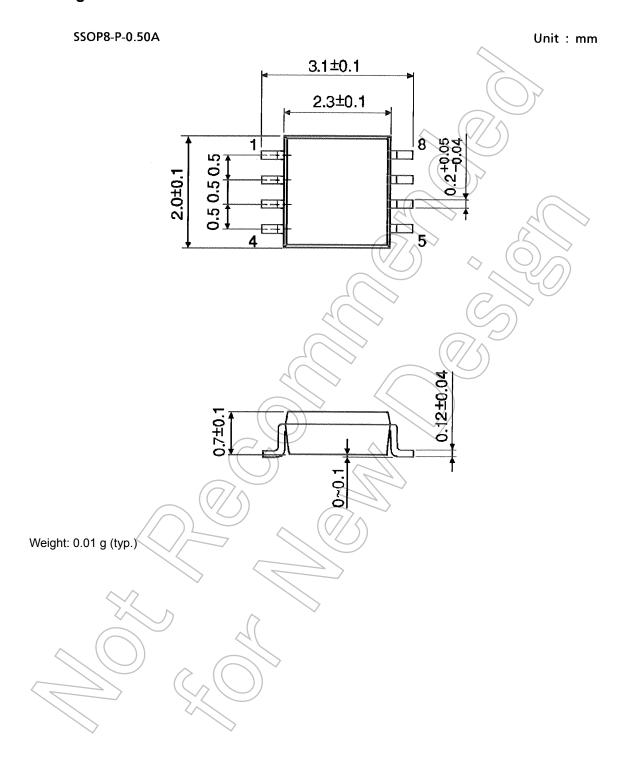


Figure 3 $t_{\text{pLZ}},\,t_{\text{pHZ}},\,t_{\text{pZL}},\,t_{\text{pZH}}$

Package Dimensions



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