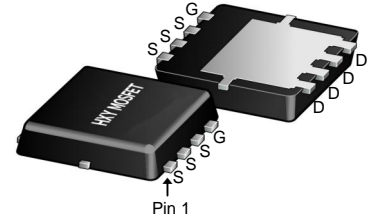




General Description

The HXYG125N06NF use advanced SGT MOSFET technology to provide low RDS(ON), low gate charge, fast switching and excellent avalanche characteristics.

This device is specially designed to get better ruggedness and suitable to use in



DFN5X6-8L

General Features

$V_{DS} = 60V$ $I_D = 125A$

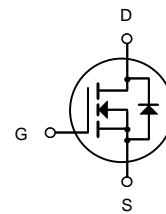
$R_{DS(ON)} < 2.9m\Omega @ V_{GS} = 10V$

Applications

Consumer electronic power supply Motor control

Synchronous-rectification Isolated DC

Synchronous-rectification applications



N-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
HXYG125N06NF	DFN5X6-8L	125N06 XXXX	5000

Absolute Maximum Ratings ($T_C = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	125	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	101	A
I_{DM}	Pulsed Drain Current ²	641	A
EAS	Single Pulse Avalanche Energy ³	189	mJ
$P_D @ T_C = 25^\circ C$	Total Power Dissipation ⁴	113	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$
$R_{\theta JC}$	Thermal Resistance from Junction-to-Ambient ³	1.11	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	39.4	$^\circ C/W$



Electrical Characteristics (T_J = 25°C, unless otherwise noted)

Static Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
V _{DSS}	Drain to Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	60	--	--	V
I _{DSS}	Drain to Source Leakage Current	V _{DS} = 60V, V _{GS} = 0V	--	--	1	μA
I _{GSS(F)}	Gate to Source Forward Leakage	V _{GS} =+20V	--	--	100	nA
I _{GSS(R)}	Gate to Source Reverse Leakage	V _{GS} =-20V	--	--	-100	nA
V _{GS(TH)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D = 250μA	1.2	--	2.2	V
R _{DSON}	Drain-to-Source On-Resistance	V _{GS} =10V, I _D =20A	--	2.4	2.9	mΩ

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
C _{iss}	Input Capacitance	V _{GS} = 0V V _{DS} = 30V f = 1.0MHz	--	4610	6915	pF
C _{oss}	Output Capacitance		--	2188	3282	
C _{rss}	Reverse Transfer Capacitance		--	66	132	
R _g	Gate resistance		--	0.93	18.8	

Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
t _{d(ON)}	Turn-on Delay Time	I _D =40A V _{DS} = 30V V _{GS} = 10V R _G = 2.7Ω	--	14.13	--	ns
t _r	Rise Time		--	63.73	--	
t _{d(OFF)}	Turn-Off Delay Time		--	46.8	--	
t _f	Fall Time		--	105.07	--	
Q _g	Total Gate Charge	V _{GS} =10V	--	74.37	111.56	nC
Q _{gs}	Gate Source Charge	V _{DS} = 30V	--	17.26	--	
Q _{gd}	Gate Drain Charge	I _D =40A	--	9.44	18.88	

Source-Drain Diode Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
I _S	Diode Forward Current	T _C =25 °C	--	--	160	A
V _{SD}	Diode Forward Voltage	I _S =40A, V _{GS} =0V	--	0.83	1.2	V
t _{rr}	Reverse Recovery time	I _S =40A,	--	52.78	105.56	ns
Q _{rr}	Reverse Recovery Charge	dI/dt=300A/μs	--	56.31	112.62	nC

a¹ : Repetitive rating; pulse width limited by maximum junction temperature a² :
VDD=30V, L=0.3mH, Rg=25Ω, Starting T_J=25 °C



Typical Characteristics

Fig 1: Output Characteristics

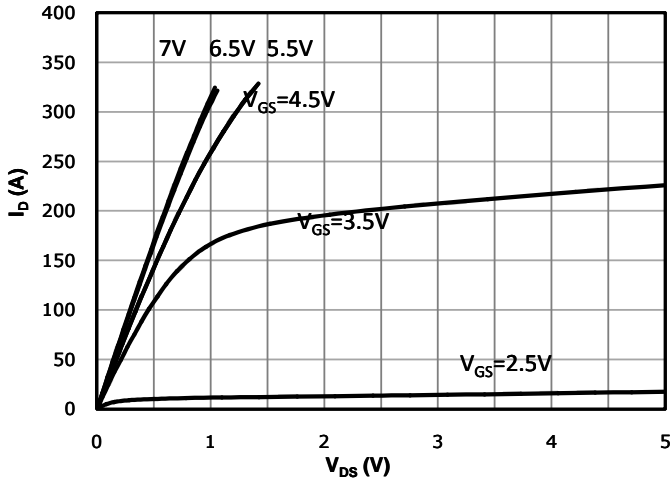


Fig 2: Transfer Characteristics

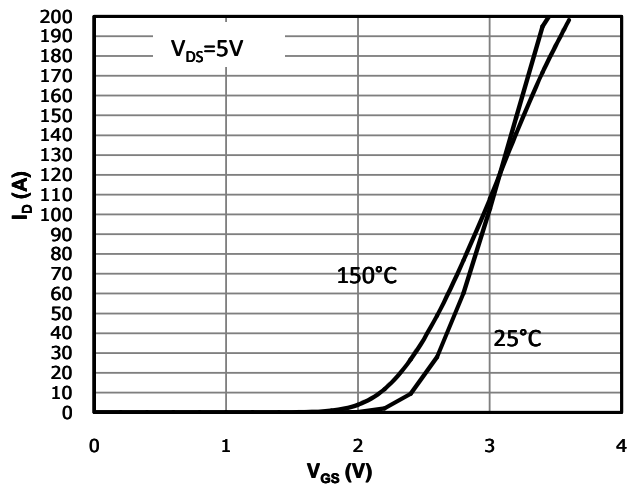


Fig 3: $R_{DS(on)}$ vs Drain Current and Gate Voltage

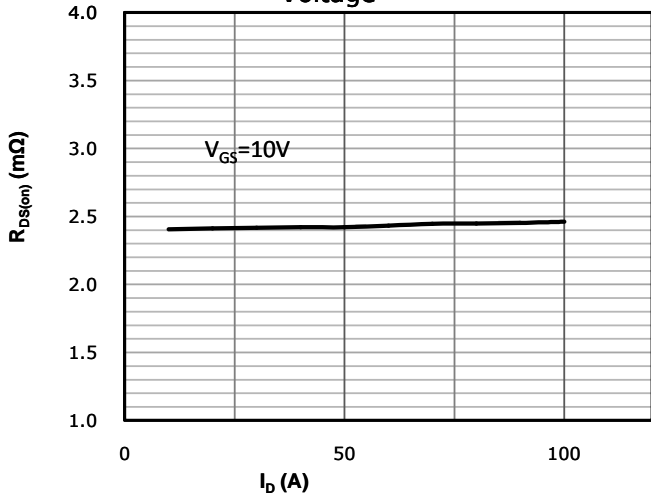


Fig 4: $R_{DS(on)}$ vs Gate Voltage

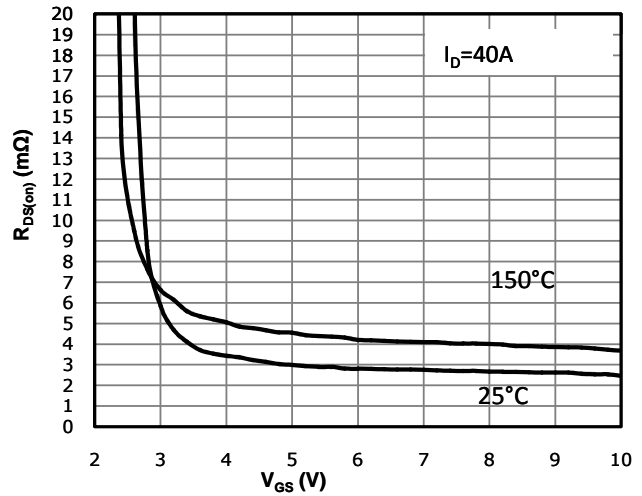


Fig 5: $R_{DS(on)}$ vs. Temperature

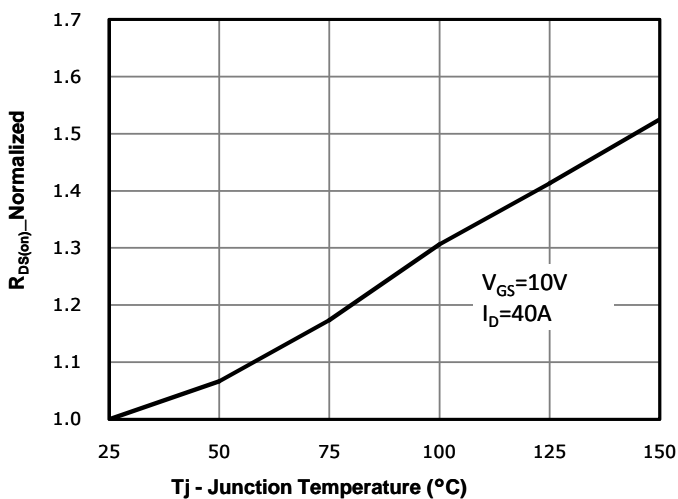


Fig 6: Capacitance Characteristics

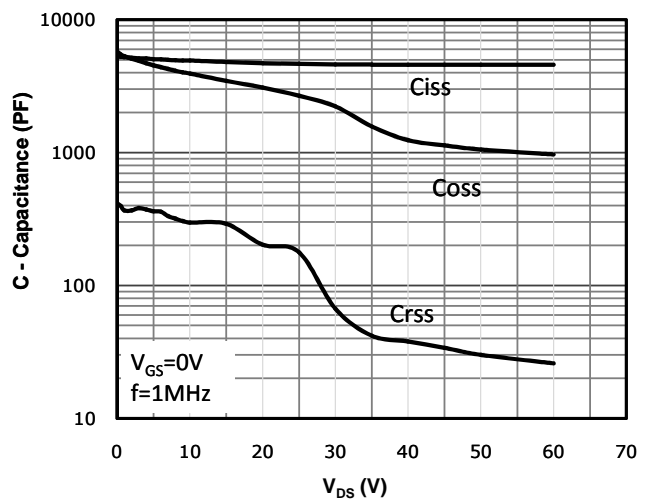




Fig 7: Gate Charge Characteristics

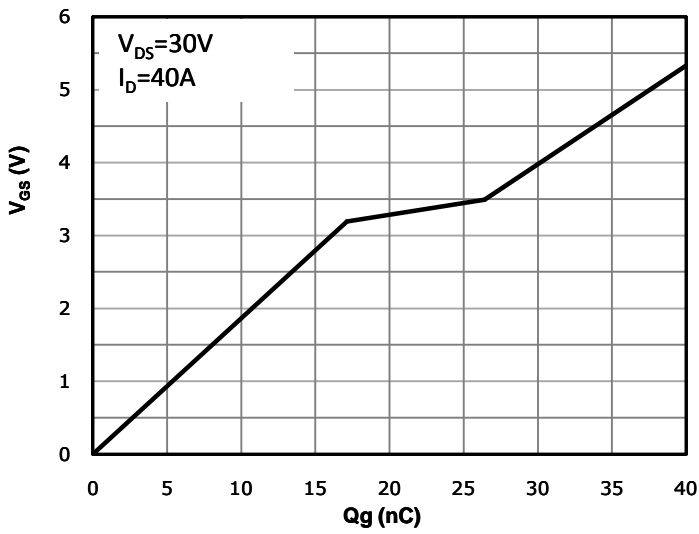


Fig 8: Body-diode Forward Characteristics

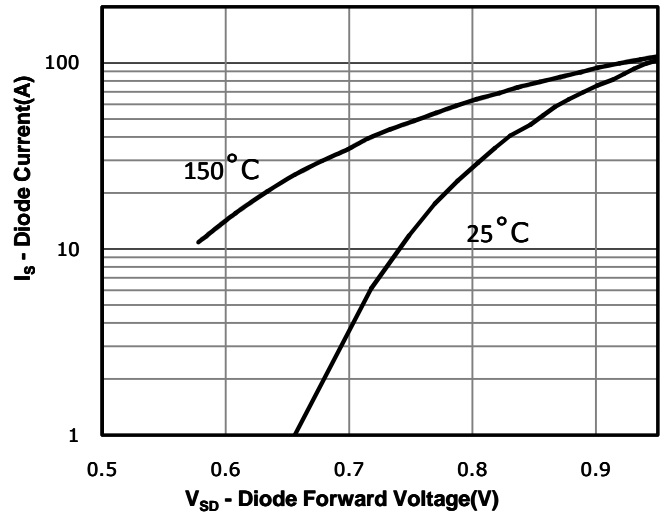


Fig 9: Power Dissipation

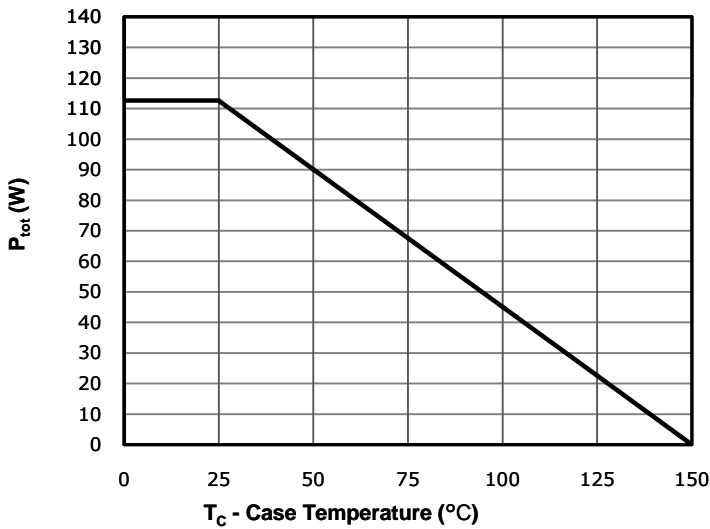


Fig 10: Drain Current Derating

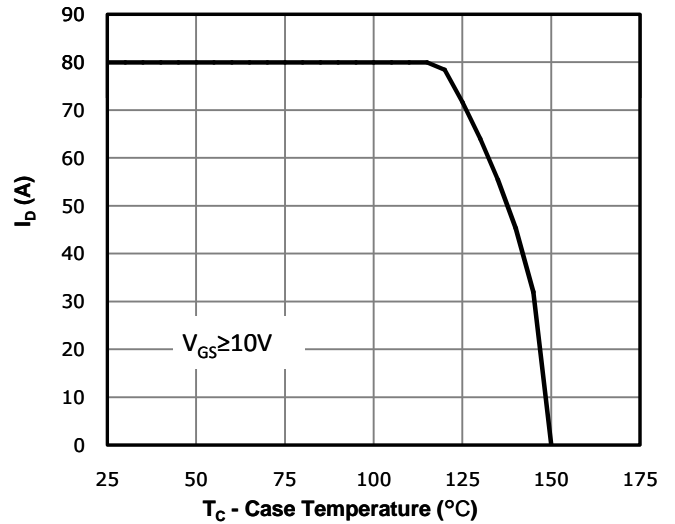


Fig 11: Safe Operating Area

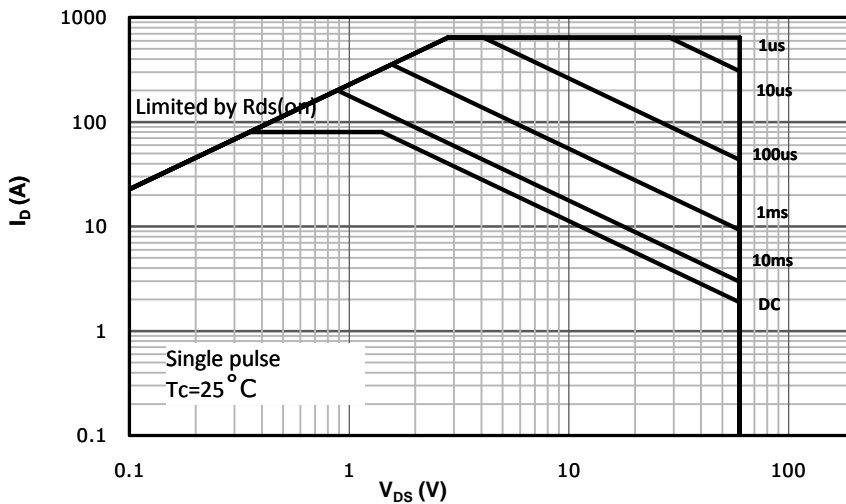
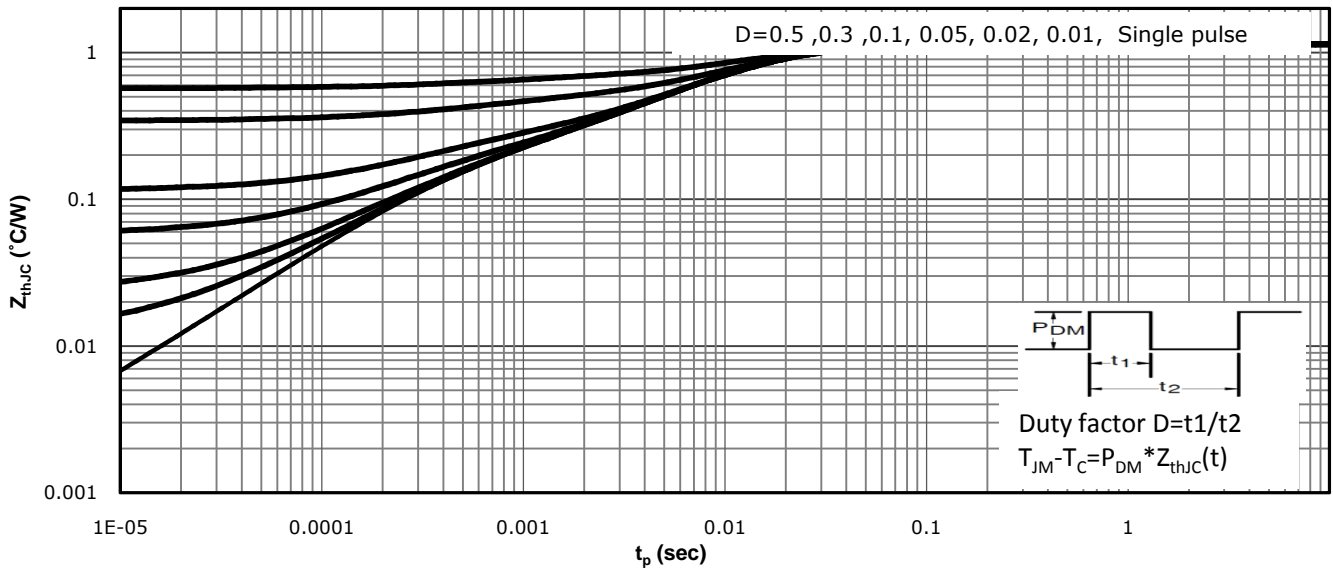


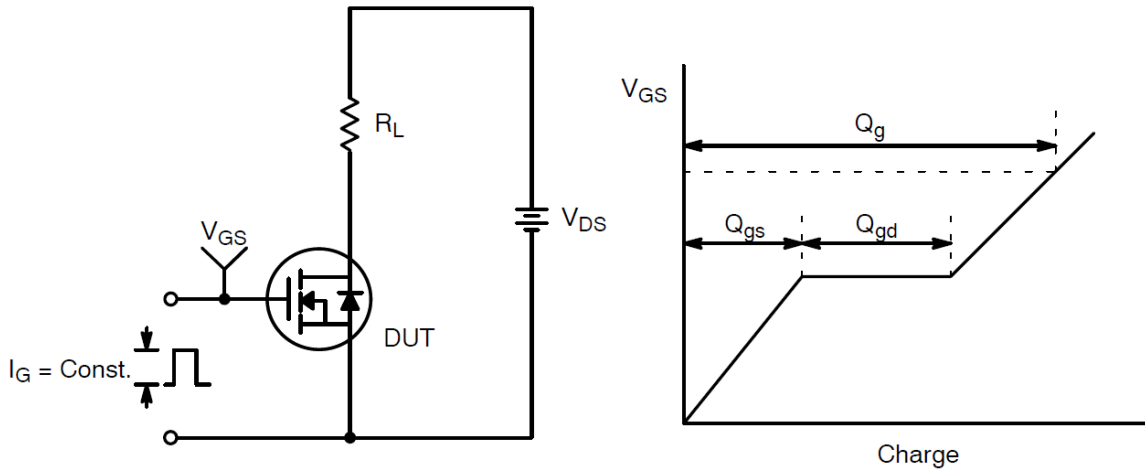


Fig 12: Max. Transient Thermal Impedance

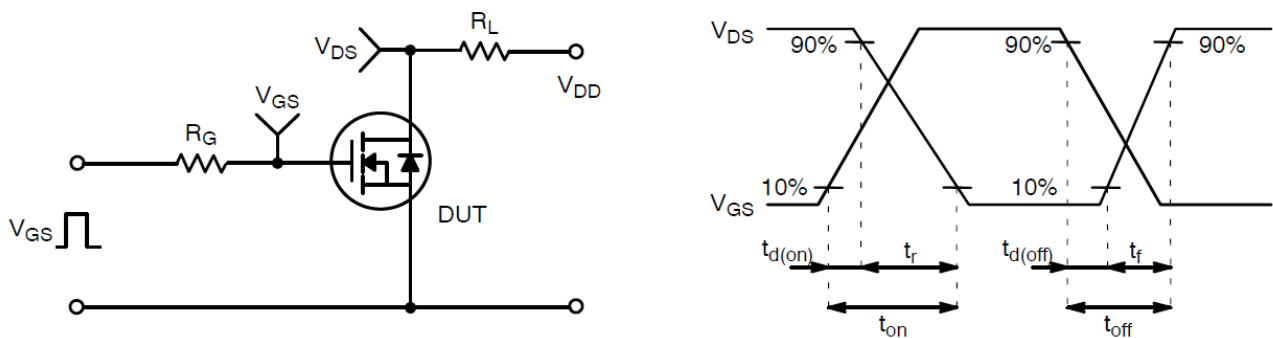




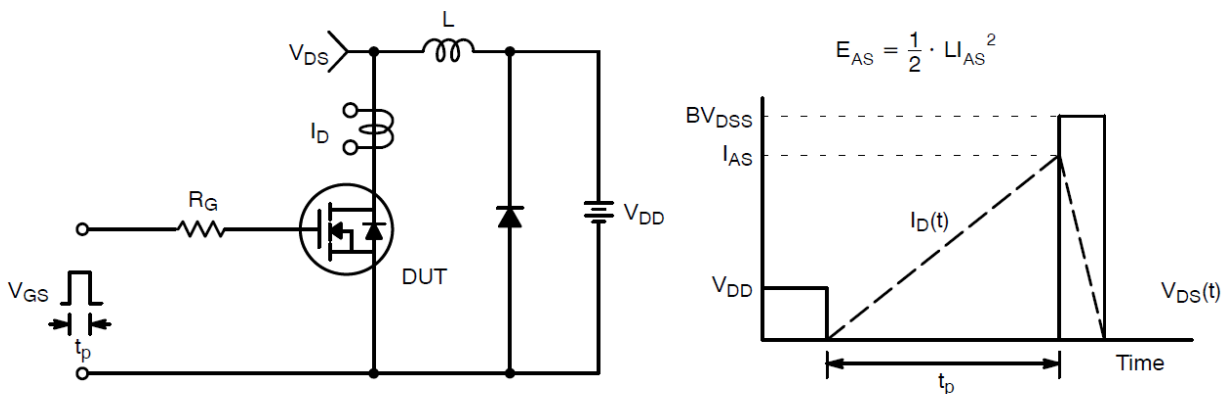
Test Circuit and Waveform:



Gate Charge Test Circuit & Waveform



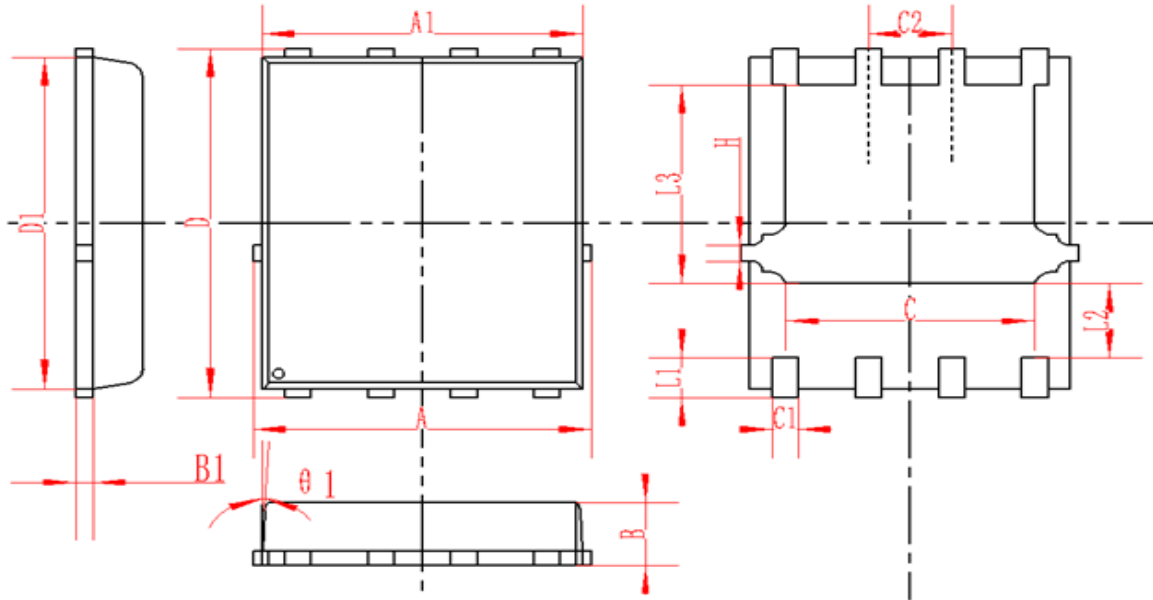
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms



DFN5X6-8L Package Information



SYMBOL	MM			INCH		
	MIN	NOM	MAX	MIN	NOM	MAX
A	4.95	5	5.05	0.195	0.197	0.199
A1	4.82	4.9	4.98	0.190	0.193	0.196
D	5.98	6	6.02	0.235	0.236	0.237
D1	5.67	5.75	5.83	0.223	0.226	0.230
B	0.9	0.95	1	0.035	0.037	0.039
B1	0.254REF			0.010REF		
C	3.95	4	4.05	0.156	0.157	0.159
C1	0.35	0.4	0.45	0.014	0.016	0.018
C2	1.27TYP			0.5TYP		
$\theta 1$	8°	10°	12°	8°	10°	12°
L1	0.63	0.64	0.65	0.025	0.025	0.026
L2	1.2	1.3	1.4	0.047	0.051	0.055
L3	3.415	3.42	3.425	0.134	0.135	0.135
H	0.24	0.25	0.26	0.009	0.010	0.010



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