



Description

JMT P-channel MOSFET

Features

- $V_{DS}=-20V$, $I_D=-55A$
- $R_{DS(ON)}<8.5m\Omega$ @ $V_{GS} = -4.5V$
- $R_{DS(ON)}<12m\Omega$ @ $V_{GS} = -2.5V$
- High Power and Current Handling Capability
- Lead Free Product is Acquired
- Surface Mount Package

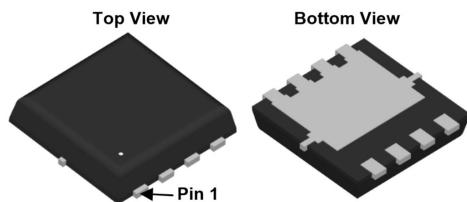
Application

- PWM Applications
- Load Switch

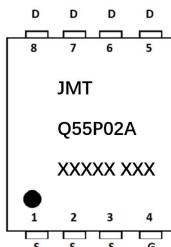


100% UIS TESTED!

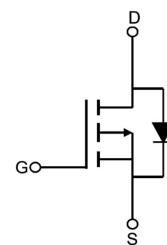
100% ΔV_{ds} TESTED!



PDFN3.3X3.3-8L



Marking and pin Assignment



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
JMTQ55P02A	JMTQ55P02A	TAPING	PDFN3.3X3.3-8L	13inch	5000	50,000

Absolute Maximum Ratings ($T_C=25^\circ C$ unless otherwise specified)

Symbol	Parameter		Max.	Units
V_{DSS}	Drain-Source Voltage		-20	V
V_{GSS}	Gate-Source Voltage		± 12	V
I_D	Continuous Drain Current	$T_C = 25^\circ C$	-55	A
		$T_C = 100^\circ C$	-35	
I_{DM}	Pulsed Drain Current ^{note1}		-220	A
P_D	Power Dissipation	$T_C = 25^\circ C$	38	W
R_{eJC}	Thermal Resistance, Junction to Ambient		3.2	$^\circ C / W$
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150	$^\circ C$

**Electrical Characteristics** ($T_J=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D = -250\mu\text{A}$	-20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -20V, V_{GS} = 0V,$	-	-	-1	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 12V$	-	-	± 100	nA
On Characteristics						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$	-0.35	-0.65	-1.0	V
$R_{DS(\text{on})}$ note3	Static Drain-Source on-Resistance	$V_{GS} = -4.5V, I_D = -15A$	-	6.6	8.5	$\text{m}\Omega$
		$V_{GS} = -2.5V, I_D = -12A$	-	8	12	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS} = -10V, V_{GS} = 0V,$ $f = 1.0\text{MHz}$	-	4600	-	pF
C_{oss}	Output Capacitance		-	460	-	pF
C_{rss}	Reverse Transfer Capacitance		-	459	-	pF
Q_g	Total Gate Charge	$V_{DS} = -10V, I_D = -15A,$ $V_{GS} = -4.5V$	-	46	-	nC
Q_{gs}	Gate-Source Charge		-	7.3	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	10	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD} = -10V, I_D = -14A,$ $R_{GEN} = 2.7\Omega,$ $V_{GS} = -10V$	-	8	-	ns
t_r	Turn-on Rise Time		-	59	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	111	-	ns
t_f	Turn-off Fall Time		-	43	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current	-	-	-55	-	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current	-	-	-220	-	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_S = -20A$	-	-	-1.2	V
trr	Reverse Recovery Time	$T_J = 25^\circ\text{C}, I_{SD} = -15A,$ $V_{GS} = 0V$ $di/dt = -100A/\mu\text{s}$	-	18	-	ns
Qrr	Reverse Recovery Charge		-	7.7	-	Nc

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition: $T_J = 25^\circ\text{C}, V_{DD} = -10V, V_G = -10V, R_G = 5.9\Omega, L = 0.5\text{mh}, I_{AS} = -13.2A$ 3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$

Typical Performance Characteristics

Figure 1: Output Characteristics

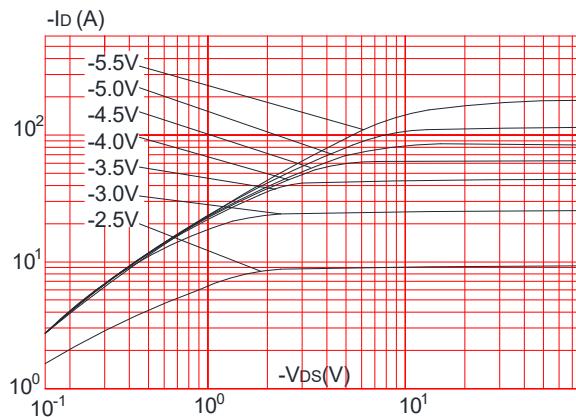


Figure 3: On-resistance vs. Drain Current

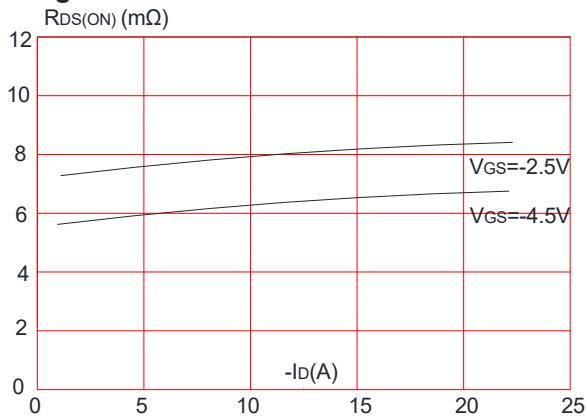


Figure 5: Gate Charge Characteristics

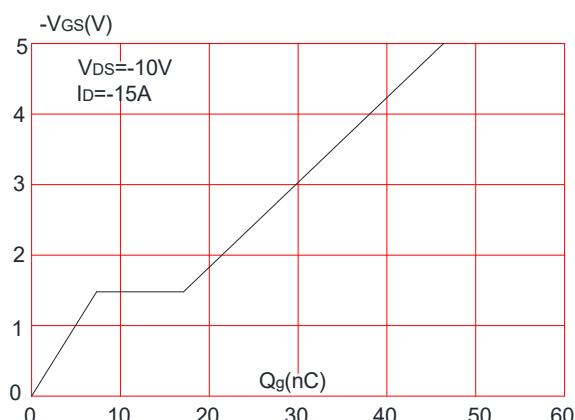


Figure 2: Typical Transfer Characteristics

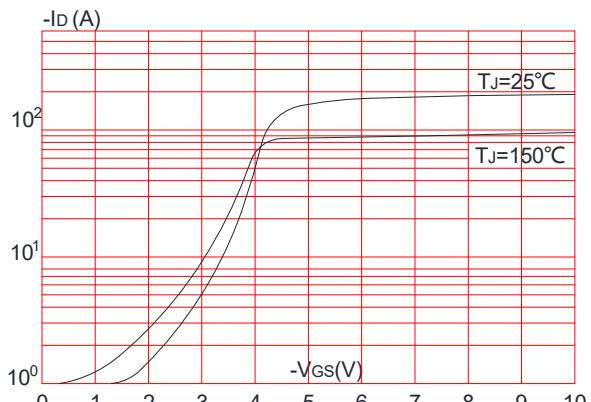


Figure 4: Body Diode Characteristics

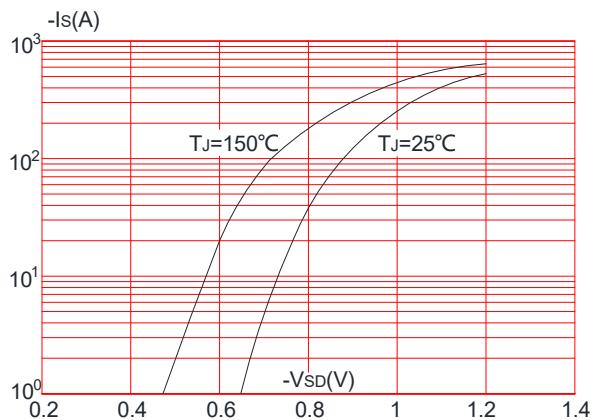


Figure 6: Capacitance Characteristics

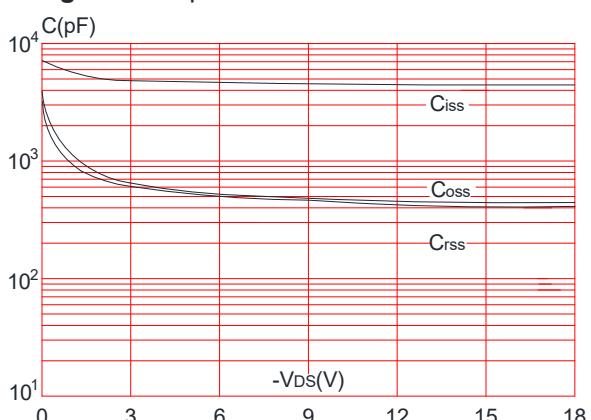


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

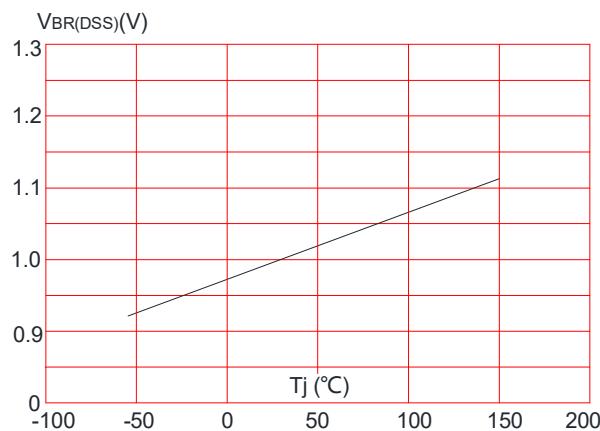


Figure 9: Maximum Safe Operating Area

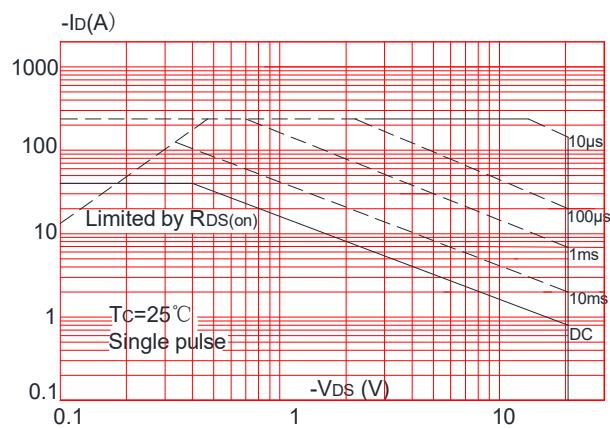


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Case

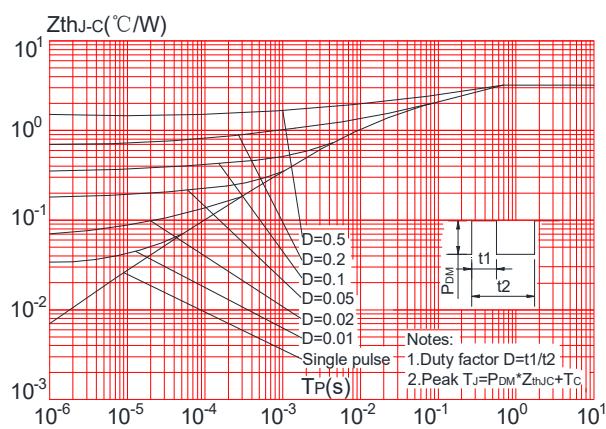


Figure 8: Normalized on Resistance vs. Junction Temperature

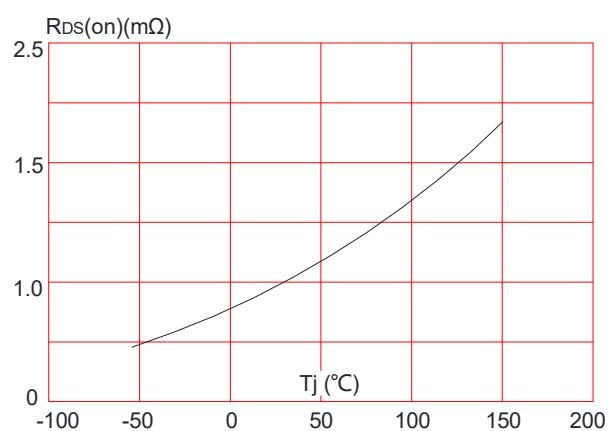
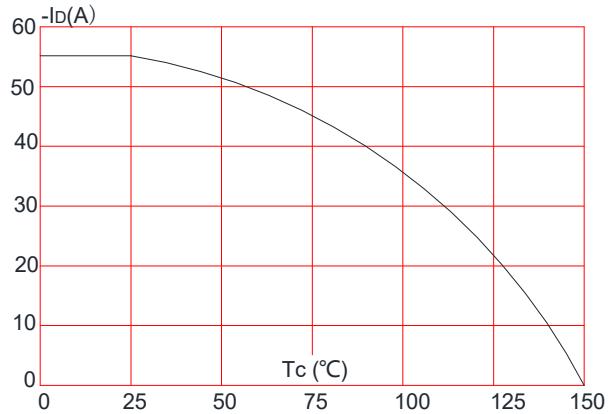
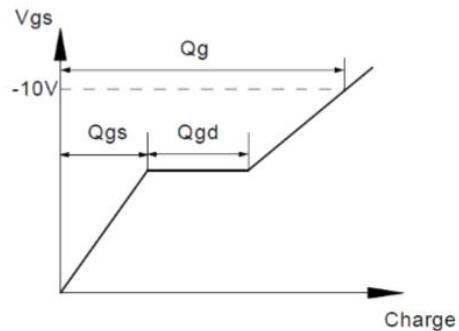
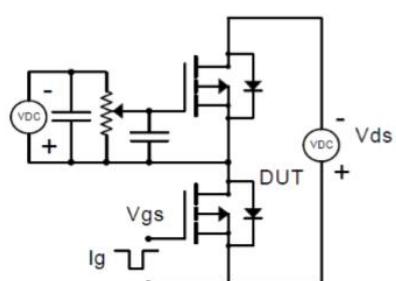


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

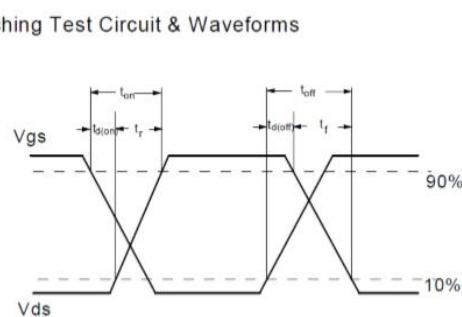
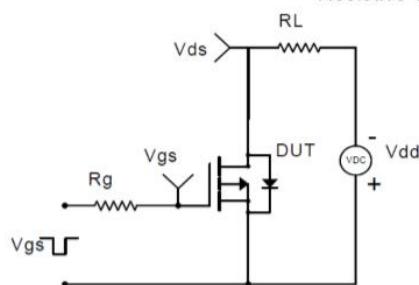


Test Circuit

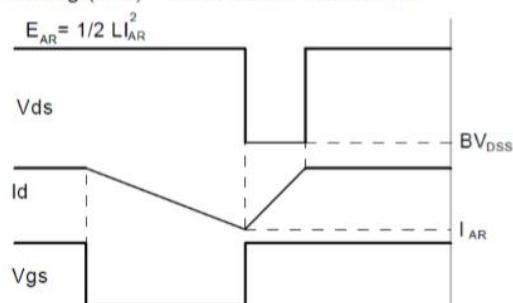
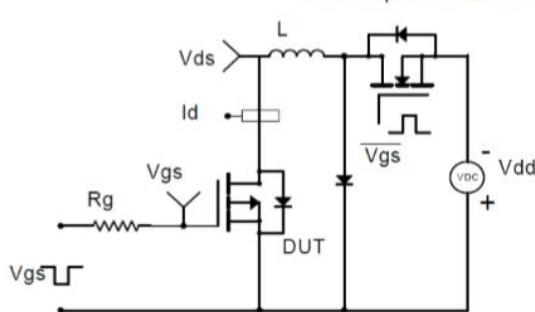
Gate Charge Test Circuit & Waveform



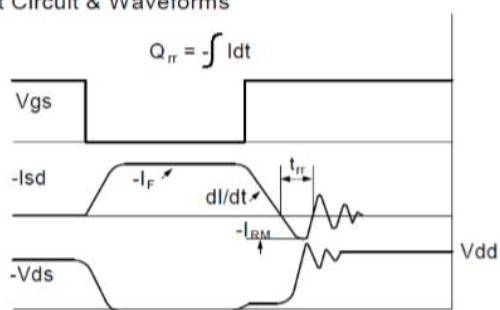
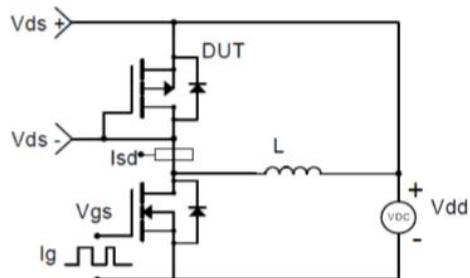
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

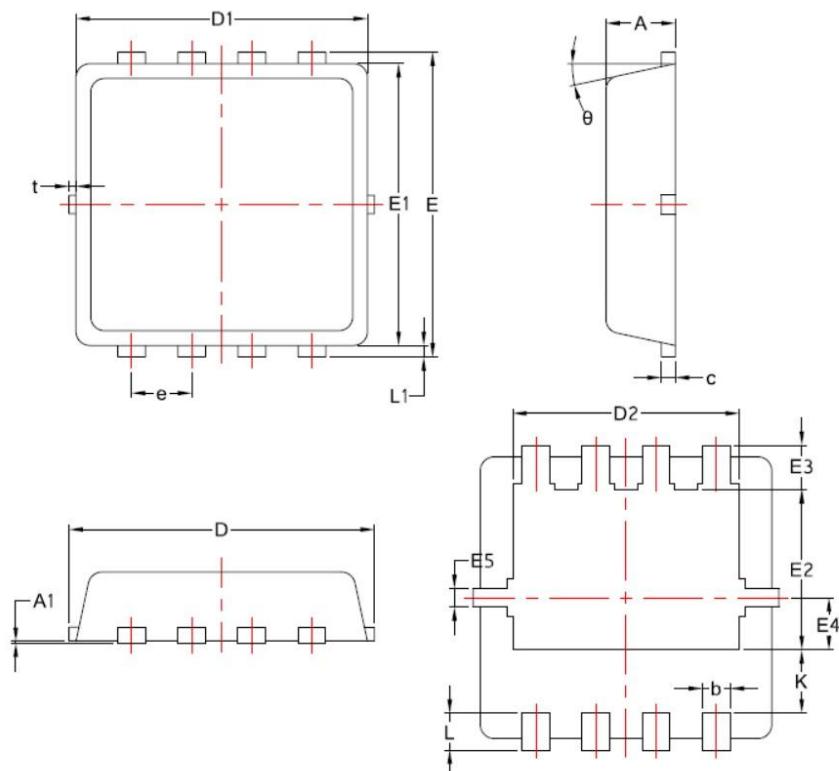


Diode Recovery Test Circuit & Waveforms





Package Mechanical Data



SYMBOL	COMMON		
	MM		
	MIN	NOM	MAX
A	0.70	0.75	0.85
A1	/	/	0.05
b	0.20	0.30	0.40
c	0.10	0.152	0.25
D	3.15	3.30	3.45
D1	3.00	3.15	3.25
D2	2.29	2.45	2.65
E	3.15	3.30	3.45
E1	2.90	3.05	3.20
E2	1.54	1.74	1.94
E3	0.28	0.48	0.65
E4	0.37	0.57	0.77
E5	0.10	0.20	0.30
e	0.60	0.65	0.70
K	0.59	0.69	0.89
L	0.30	0.40	0.50
L1	0.06	0.125	0.20
t	0	0.075	0.13
θ	10°	12°	14°

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