

#### **General Description**

The IRFR120ZTRPBF use advanced SGT MOSFET technology

to provide low RDS(ON), low gate charge, fast switching

and excellent avalanche characteristics.

This device is specially designed to get better ruggedness

and suitable to use in.

#### **General Features**

V<sub>DS</sub> = 100V I<sub>D</sub> =12 A

 $R_{DS(ON)} < 120 m\Omega @ V_{GS} = 10V$ 

### Applications

Consumer electronic power supply

Motor control

Synchronous-rectification

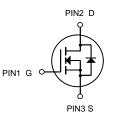
### **Package Marking and Ordering Information**

Product ID	Pack	Marking	Qty(PCS)
IRFR120ZTRPBF	TO-252-2L(DPAK)	12N10 XXXXX	2500

### **Absolute Maximum Ratings** at T<sub>j</sub>=25°C unless otherwise noted

Symbol	Parameter	Value	Unit
VDS	Drain source voltage	100	v
VGS	Gate source voltage	±20	v
ID	Continuous drain current <sup>1)</sup>	12	A
ID, pulse	Pulsed drain current <sup>2)</sup>	24	А
P <sub>D</sub>	Power dissipation <sup>3)</sup>	17	w
EAS	Single pulsed avalanche energy <sup>4)</sup>	1.2	mJ
Tstg, Tj	Operation and storage temperature	-55 to 150	°C
RθJC	Thermal resistance, junction-case	6.6	°C/W
RÐJA	Thermal resistance, junction-ambient <sup>5)</sup>	62	°C/W





N-Channel MOSFET



## Electrical Characteristics Tc=25°C unless otherwise specified

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
Off Chara	cteristic					
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA	100	110	-	V
IDSS	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 100V, V <sub>GS</sub> = 0V	-	-	1	μA
lgss	Gate to Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	cteristics note3			•		
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS}$ = $V_{GS}$ , $I_D$ = 250 $\mu$ A	1.0	1.8	3.0	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance note2	V <sub>GS</sub> = 10V, I <sub>D</sub> = 3A	-	95	120	mΩ
Dynamic (	Characteristics note4					
Ciss	Input Capacitance	V <sub>DS</sub> = 50V, V <sub>GS</sub> = 0V, f = 1.0MHz	-	196	-	pF
Coss	Output Capacitance		-	25.9	-	pF
Crss	Reverse Transfer Capacitance		-	21.4	-	pF
Qg	Total Gate Charge	V <sub>DS</sub> = 50V, I <sub>D</sub> = 3A,	-	4.3	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	3.5	-	nC
Q <sub>gd</sub>	Gate-Drain("Miller") Charge	V <sub>GS</sub> = 10V	-	3.1	-	nC
Switching	Characteristics <sup>note4</sup>	•				
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> = 50V, I <sub>DS</sub> =3A	-	14.7	-	ns
tr	Turn-On Rise Time		-	3.5	-	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	$R_G = 2\Omega, V_{GEN} = 10V$	-	20.9	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	2.7	-	ns
Drain-Sou	irce Diode Characteristics and Maximum Rati	ngs	•		•	•
ls	Maximum Continuous Drain to Source Diode Forward Current note2		-	-	4.5	А
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forwa	ard Current	-	-	12	А
V <sub>SD</sub>	Drain to Source Diode Forward Voltage note3	V <sub>GS</sub> = 0V, I <sub>S</sub> =3A	-	-	1.3	V
t <sub>rr</sub>	Body Diode Reverse Recovery Time		-	32.1	-	ns
Qrr	Body Diode Reverse Recovery Time Charge	$V_{GS} = 0V, I_F = 3A,$	-	39.4	-	nC
I <sub>rrm</sub>	Peak Reverse Recovery Current	di/dt =100A/µs	-	2.1	-	А

Notes:

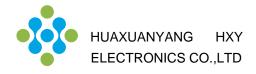
1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, t  $\leq$  10 sec.

3. Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$ 2%.

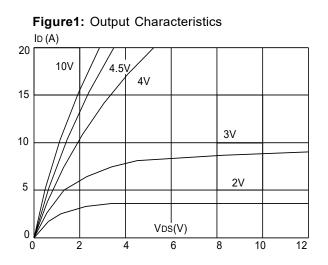
4. Guaranteed by design, not subject to production

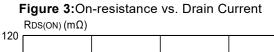
5. V<sub>DD</sub>=50 V, R<sub>G</sub>=50  $\Omega$ , L=0.3 mH, starting T<sub>j</sub>=25 °C

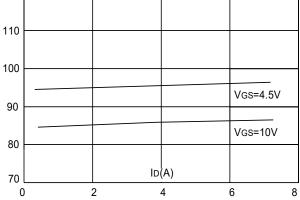


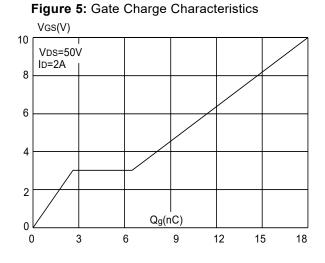
## **IRFR120ZTRPBF** N-SGT Enhancement Mode MOSFET

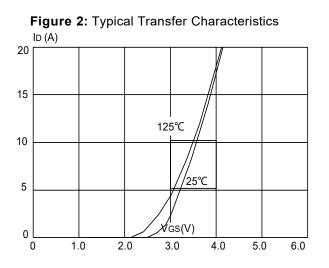
# **Typical Performance Characteristics**

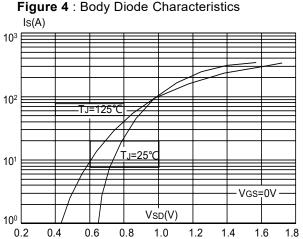


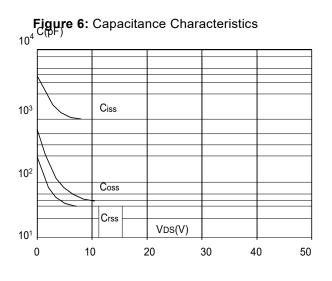






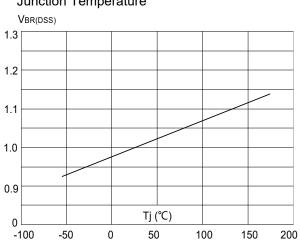






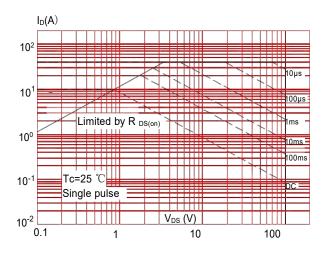
# Figure 4 : Body Diode Characteristics



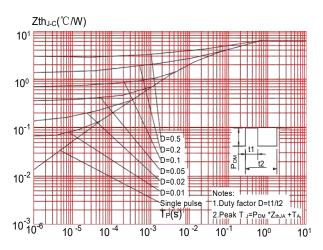


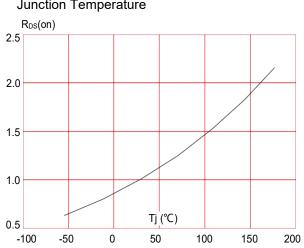
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature

Figure 9: Maximum Safe Operating Area

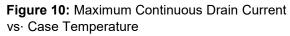


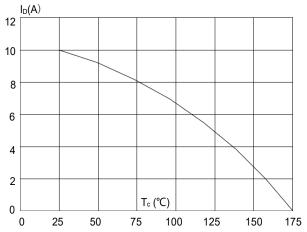
**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case

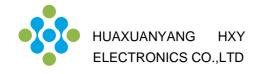




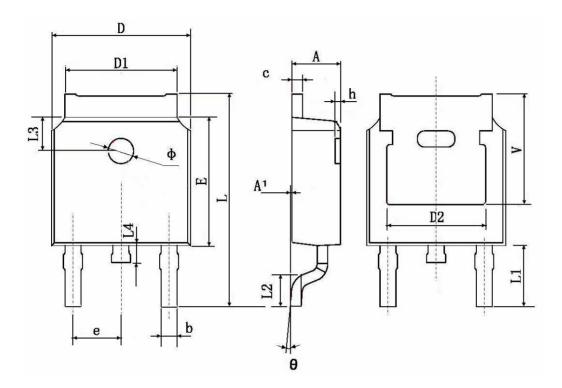
**Figure 8:** Normalized on Resistance vs. Junction Temperature







# TO-252-2L(DPAK) Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
A	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
с	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	0.483 TYP.		0.190 TYP.		
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067	
L3	1.600	0.063 TYP.		3 TYP.	
L4	0.600	1.000	0.024	0.039	
Φ	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350	) TYP.	0.211 TYP.		



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