



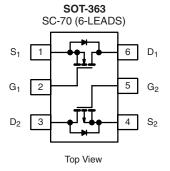
# Complementary 20 V (D-S) MOSFET

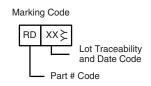
PRODUCT SUMMARY							
	V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)			
N-Channel	20	1.9 at $V_{GS} = 4.5 \text{ V}$	0.30				
		3.7 at $V_{GS} = 2.7 \text{ V}$	0.22	0.72			
		4.2 at V <sub>GS</sub> = 2.5 V	0.21				
P-Channel	- 20	$0.995$ at $V_{GS} = -4.5$ V	- 0.44				
		1.600 at $V_{GS} = -2.7 \text{ V}$	- 0.34	0.52			
		1.800 at $V_{GS} = -2.5 \text{ V}$	- 0.32				

#### **FEATURES**

- Halogen-free According to IEC 61249-2-21
- TrenchFET® Power MOSFET: 2.5 V Rated
- Compliant to RoHS Directive 2002/95/EC







Ordering Information: Si1551DL-T1-E3 (Lead (Pb)-free) Si1551DL-T1-GE3 (Lead (Pb)-free and Halogen-free)

<b>ABSOLUTE MAXIMUM RATINGS</b> T <sub>A</sub> = 25 °C, unless otherwise noted								
Parameter			N-Channel		P-Channel			
		Symbol	5 s	Steady State	5 s	Steady State	Unit	
Drain-Source Voltage		$V_{DS}$	20			- 20		
Gate-Source Voltage		$V_{GS}$	± 12				V	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C	I <sub>D</sub>	0.30	0.29	- 0.44	- 0.41		
	T <sub>A</sub> = 85 °C		0.22	0.21	- 0.31	- 0.30		
Pulsed Drain Current		I <sub>DM</sub>	0.6 - 1.0		- 1.0	Α		
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	0.25	0.23	- 0.25	- 0.23		
	T <sub>A</sub> = 25 °C	В	0.30	0.27	0.30	0.27	W	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 85 °C	$P_{D}$	0.16	0.14	0.16	0.14		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150				°C	

THERMAL RESISTANCE RATINGS								
Parameter		Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 5 s	R <sub>thJA</sub>	360	415				
	Steady State		400	460	°C/W			
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	300	350				

a. Surface mounted on 1" x 1" FR4 board.

## **Si1551DL**

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Static	Syllibol	rest conditions		IVIIII.	Typ.	IVIAX.	Offic	
- Clusto		V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	N-Ch	0.6		1.5		
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = - 250 μA	P-Ch	- 0.6		- 1.5	V	
			N-Ch			± 100		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$	P-Ch			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V	N-Ch			1		
		V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V P-Ch				- 1		
		$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$	N-Ch			5	μΑ	
		V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 85 °C	P-Ch			- 5		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	N-Ch	0.6			۸	
		$V_{DS} \le$ - 5 V, $V_{GS} =$ - 4.5 V	P-Ch	- 1.0			A	
		$V_{GS} = 4.5 \text{ V}, I_D = 0.29 \text{ A}$	N-Ch	1.55	1.9			
	R <sub>DS(on)</sub>	$V_{GS} = -4.5 \text{ V}, I_D = -0.41 \text{ A}$	P-Ch		0.850	0.995	Ω	
Drain Course On State Resistance		$V_{GS} = 2.7 \text{ V}, I_D = 0.1 \text{ A}$	N-Ch		2.8	3.7		
Drain-Source On-State Resistance <sup>a</sup>		$V_{GS} = -2.7 \text{ V}, I_D = -0.25 \text{ A}$	P-Ch		1.23	1.600		
		$V_{GS} = 2.5 \text{ V}, I_D = 0.1 \text{ A}$	N-Ch		3.0	4.2		
		$V_{GS} = -2.5 \text{ V}, I_D = -0.25 \text{ A}$	P-Ch		1.4	1.800		
Famous districtions of	9 <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.29 A	N-Ch		0.3		S	
Forward Transconductance <sup>a</sup>		V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 0.41 A	P-Ch		0.8			
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = 0.23 A, V <sub>GS</sub> = 0 V	N-Ch		0.8	1.2	V	
blode Forward Voltage		I <sub>S</sub> = - 0.23 A, V <sub>GS</sub> = 0 V	P-Ch		- 0.8	- 1.2		
Dynamic <sup>b</sup>								
Total Gate Charge	Qg	N-Channel	N-Ch		0.72	1.5		
		$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 0.29 \text{ A}$	P-Ch		0.52	1.8	-	
Gate-Source Charge	Q <sub>gs</sub>		N-Ch P-Ch		0.22 0.11		nC	
		P-Channel	N-Ch		0.11		-	
Gate-Drain Charge		$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -0.41 \text{ A}$	P-Ch		0.14			
Tives On Dalay Time	t <sub>d(on)</sub>		N-Ch		23	40		
Turn-On Delay Time		N-Channel	P-Ch		7.5	15	-	
Rise Time		$V_{DD} = 10 \text{ V}, R_L = 20 \Omega$ $I_D \cong 0.5 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 6 \Omega$	N-Ch		30	60		
11100 111110		1D = 0.0 /1, VGEN = 4.0 V, Fig = 0.32	P-Ch		20	40		
Turn-Off Delay Time	t <sub>d(off)</sub>	P-Channel	N-Ch		10	20	ns	
		$V_{DD} = -10 \text{ V}, R_L = 20 \Omega$	P-Ch N-Ch		8.5 15	17 30		
Fall Time		$I_D \cong$ - 0.5 A, $V_{GEN}$ = - 4.5 V, $R_g$ = 6 $\Omega$	P-Ch		12	24		
		I <sub>F</sub> = 0.23 A, dl/dt = 100 A/μs	N-Ch		20	40	1	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	$I_F = -0.23 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}$			25	40		

#### Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

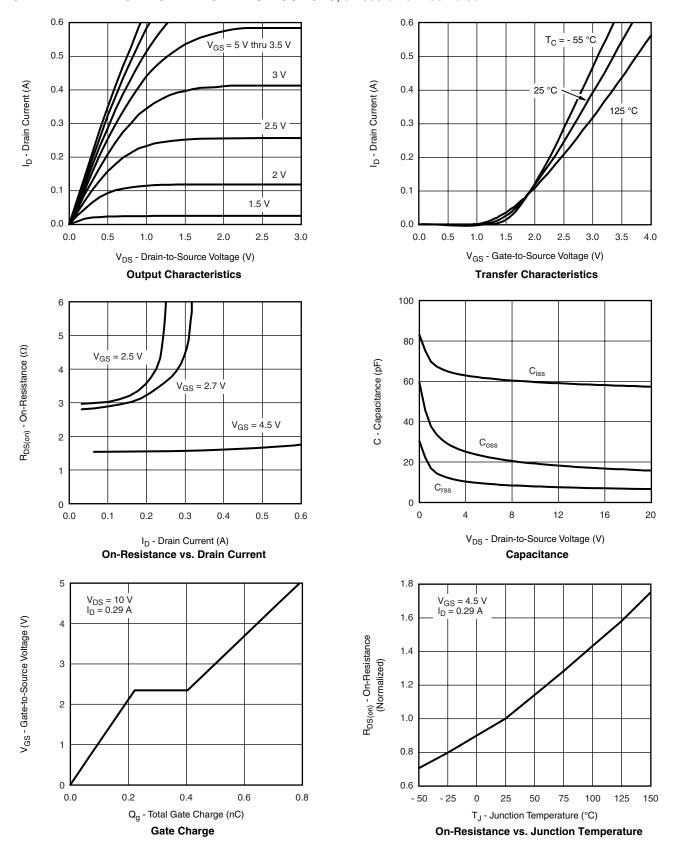
b. Guaranteed by design, not subject to production testing.







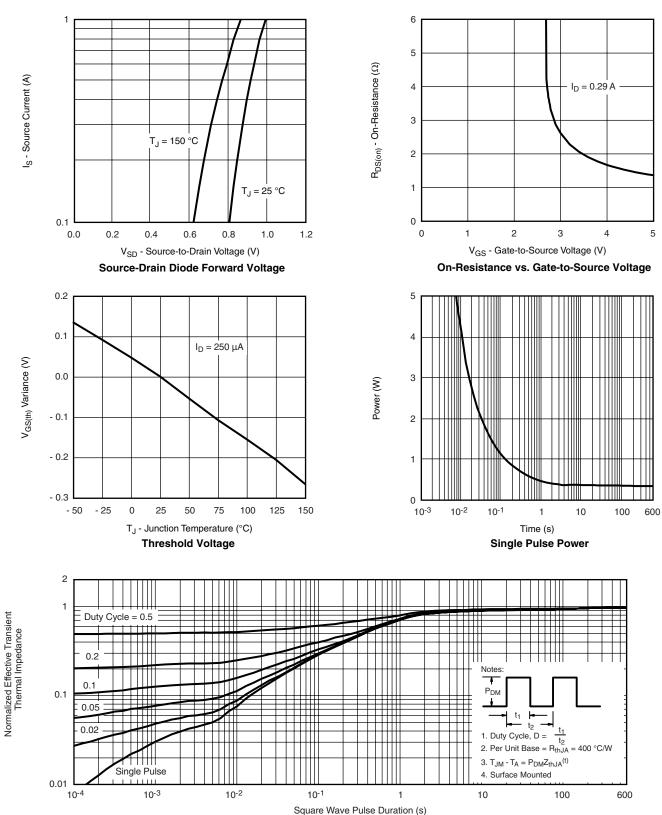
### N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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#### N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

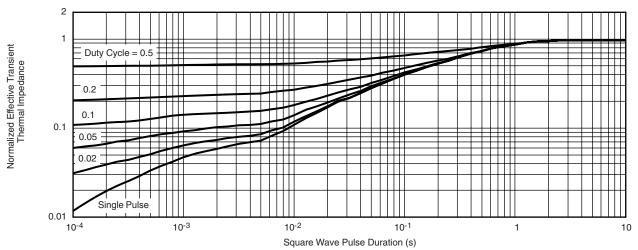


Normalized Thermal Transient Impedance, Junction-to-Ambient



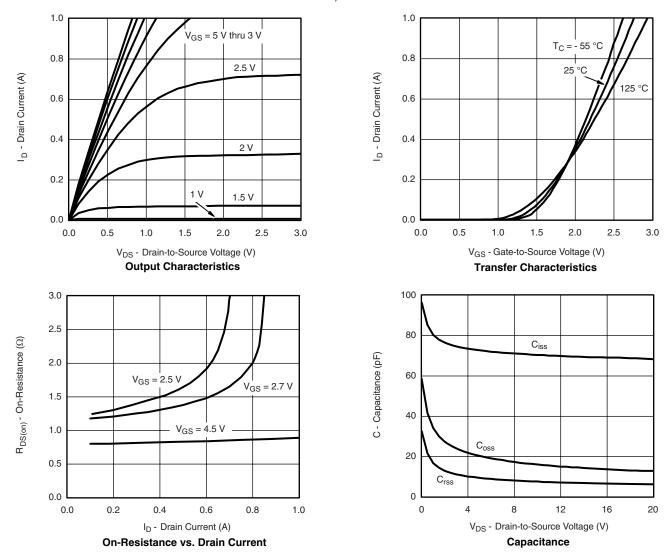


#### N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

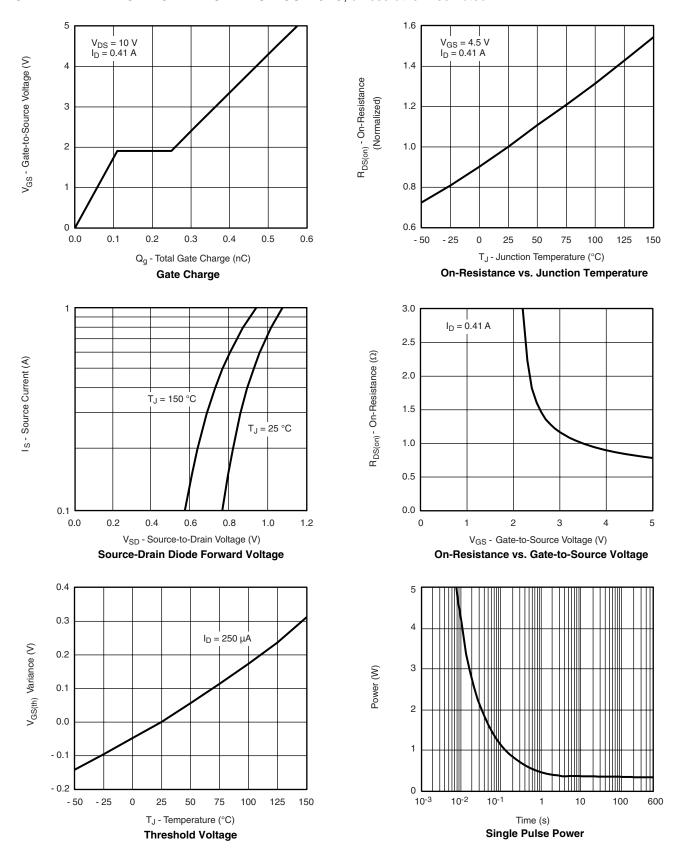
#### P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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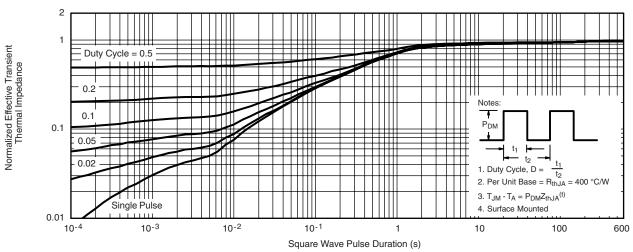


#### P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

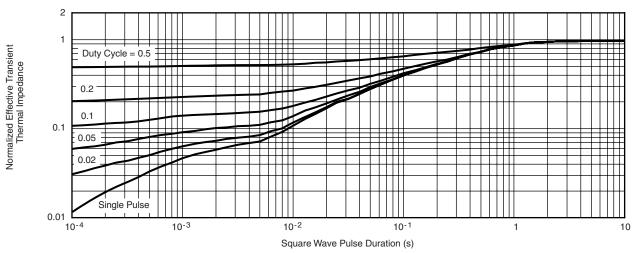




#### P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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