

## N-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY							
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)				
<u></u>	0.076 at V <sub>GS</sub> = 10 V	5.5	29 nC				
60	0.088 at V <sub>GS</sub> = 4.5 V	4.5	29110				

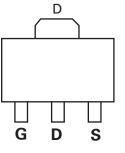
#### **FEATURES**

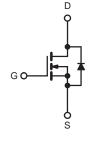
- Halogen-free
- TrenchFET<sup>®</sup> Power MOSFET

#### APPLICATIONS

· Load Switches for Portable Devices







N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> $T_A = 25 \text{ °C}$ , unless otherwise noted							
Parameter		Symbol	Limit	Unit			
Drain-Source Voltage		V <sub>DS</sub>	60	V			
Gate-Source Voltage		V <sub>GS</sub>	± 20	- v			
Continuous Drain Current (T <sub>J</sub> = 150 °C)	$T_{C} = 25 °C$ $T_{C} = 70 °C$ $T_{A} = 25 °C$ $T_{A} = 70 °C$	I <sub>D</sub>	5.5 <sup>a</sup> 4 <sup>a</sup> 4.7 <sup>a, b, c</sup> 4 <sup>a, b, c</sup>	А			
Pulsed Drain Current		I <sub>DM</sub>	20				
Continuous Source-Drain Diode Current $T_{C} = 25 \text{ °C}$ $T_{A} = 25 \text{ °C}$		I <sub>S</sub>	5.2 5.1 <sup>b, c</sup>				
Maximum Power Dissipation		P <sub>D</sub>	5.3 4 2.5 <sup>b, c</sup> 1.6 <sup>b, c</sup>	w			
Operating Junction and Storage Temperatur	e Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C			
Soldering Recommendations (Peak Temperations	ature) <sup>e, f</sup>	•	260				

#### THERMAL RESISTANCE BATINGS

Parameter	Symbol Typical		Maximum	Unit					
Maximum Junction-to-Ambient <sup>a, c, d</sup>	t ≤ 5 s	R <sub>thJA</sub>	40	50	°C/W				
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	15	20	0/11				

Notes:

a. Package limited, T<sub>C</sub> = 25 °C.
b. Surface Mounted on 1" x 1" FR4 board.

c. t = 10 s.

d. Maximum under Steady State conditions is 95 °C/W.

e. See Reliability Manual for profile. The ChipFET is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.

f. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.

SPECIFICATIONS T <sub>J</sub> = 25 °C, unless otherwise noted         Parameter       Symbol       Test Conditions       Min.       Typ.       Max.       Unit									
Symbol	Test Conditions	Min.	Тур.	Max.	Unit				
		[	T	[	1				
	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	60			V				
	l <sub>D</sub> = 250 µA		25						
. ,			- 4.0		mV/°C				
V <sub>GS(th)</sub>		1.5		3.0	V				
I <sub>GSS</sub>				± 100	nA				
lace				1					
USS	$V_{DS}$ = 60 V, $V_{GS}$ = 0 V, $T_{J}$ = 55 °C			10	μA				
I <sub>D(on)</sub>	$V_{DS} \!\geq\! 5$ V, $V_{GS}$ = 4.5 V	25			Α				
D	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 3.3 \text{ A}$		0.088						
RDS(on)	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 4.5 \text{ A}$		0.076		Ω				
9 <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 4.3 A		45		S				
		Ι			•				
C <sub>iss</sub>			800						
C <sub>oss</sub>	$V_{DS}$ = 10 V, $V_{GS}$ = 0 V, f = 1 MHz		120		pF				
C <sub>rss</sub>			100						
0	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 6.3 A		22	33	nC				
Qg	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 6.3 A		10	15					
Q <sub>gs</sub>			2.5						
			1.7						
R <sub>g</sub>	f = 1 MHz		2.4		Ω				
t <sub>d(on)</sub>			15	25					
t <sub>r</sub>	$V_{DD} = 10 \text{ V}, \text{ R}_1 = 1.5 \Omega$		10	15	-				
t <sub>d(off)</sub>	$I_D \cong 6.7 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		35	55					
t <sub>f</sub>			12	20					
t <sub>d(on)</sub>			10	15	ns				
	$V_{DD} = 10 \text{ V}, \text{ R}_1 = 1.5 \Omega$		12	20					
-	$I_D \cong 6.7 \text{ A}, V_{GEN} = 10 \text{ V}, \text{ R}_g = 1 \Omega$		25	40	-				
t <sub>f</sub>			10	15					
		I	I	I					
I <sub>S</sub>	T <sub>C</sub> = 25 °C			7.2	A				
				25					
	I <sub>S</sub> = 6.7 A, V <sub>GS</sub> = 0 V		0.8	1.2	V				
			20	40	ns				
			10	20	nC				
	I <sub>F</sub> = 6.7 A, dl/dt = 100 A/μs, T <sub>J</sub> = 25 °C			-					
t <sub>b</sub>			10		ns				
	$\begin{tabular}{ c c c c } \hline I_{DSS} \\ \hline I_{D(on)} \\ \hline R_{DS(on)} \\ \hline g_{fs} \\ \hline C_{iss} \\ \hline C_{oss} \\ \hline C_{oss} \\ \hline C_{rss} \\ \hline C_{rss} \\ \hline Q_g \\ \hline Q_g \\ \hline Q_{gd} \\ \hline R_g \hline$	$\begin{array}{c c c c c c c c c } V_{DS} & V_{GS} = 0 \ V, \ I_D = 250 \ \mu A \\ \hline \Delta V_{DS} / T_J & I_D = 250 \ \mu A \\ \hline \Delta V_{GS(th)} / T_J & V_{DS} = V_{GS}, \ I_D = 250 \ \mu A \\ \hline V_{GS(th)} & V_{DS} = 0 \ V, \ V_{GS} = 12 \ V \\ \hline V_{DS} = 60 \ V, \ V_{GS} = 0 \ V, \ V_{GS} = 0 \ V \\ \hline V_{DS} = 60 \ V, \ V_{GS} = 0 \ V, \ T_J = 55 \ ^{\circ}C \\ \hline I_{D(on)} & V_{DS} \ge 5 \ V, \ V_{GS} = 4.5 \ V \\ \hline V_{DS} \ge 60 \ V, \ I_D = 4.5 \ A \\ \hline V_{DS} = 10 \ V, \ I_D = 4.5 \ A \\ \hline V_{DS} = 10 \ V, \ I_D = 4.3 \ A \\ \hline \hline C_{iss} & V_{DS} = 10 \ V, \ V_{GS} = 10 \ V, \ I_D = 6.3 \ A \\ \hline Q_{gd} & V_{DS} = 10 \ V, \ V_{GS} = 4.5 \ V, \ I_D = 6.3 \ A \\ \hline Q_{gd} & V_{DS} = 10 \ V, \ V_{GS} = 4.5 \ V, \ I_D = 6.3 \ A \\ \hline Q_{gd} & V_{DS} = 10 \ V, \ V_{GS} = 4.5 \ V, \ I_D = 6.3 \ A \\ \hline Q_{gd} & I_D = 10 \ V, \ V_{GS} = 4.5 \ V, \ I_D = 6.3 \ A \\ \hline Q_{DD} = 10 \ V, \ R_L = 1.5 \ \Omega \\ I_D \cong 6.7 \ A, \ V_{GEN} = 4.5 \ V, \ R_g = 1 \ \Omega \\ \hline I_d(off) & I_T \\ \hline I_D \cong 6.7 \ A, \ V_{GEN} = 4.5 \ V, \ R_g = 1 \ \Omega \\ \hline I_D \cong 6.7 \ A, \ V_{GEN} = 10 \ V, \ R_g = 1 \ \Omega \\ \hline I_D \cong 6.7 \ A, \ V_{GEN} = 10 \ V, \ R_g = 1 \ \Omega \\ \hline I_D \cong 6.7 \ A, \ V_{GEN} = 10 \ V, \ R_g = 1 \ \Omega \\ \hline I_D \cong 6.7 \ A, \ V_{GEN} = 10 \ V, \ R_g = 1 \ \Omega \\ \hline I_D \cong 6.7 \ A, \ V_{GEN} = 10 \ V, \ R_g = 1 \ \Omega \\ \hline I_D \cong 6.7 \ A, \ V_{GEN} = 10 \ V, \ R_g = 1 \ \Omega \\ \hline I_D \cong 6.7 \ A, \ V_{GEN} = 10 \ V, \ R_g = 1 \ \Omega \\ \hline I_D \cong 6.7 \ A, \ V_{GEN} = 10 \ V, \ R_g = 1 \ \Omega \\ \hline I_D \cong 6.7 \ A, \ V_{GEN} = 10 \ V, \ R_g = 1 \ \Omega \\ \hline I_D \cong 6.7 \ A, \ V_{GEN} = 10 \ V, \ R_g = 1 \ \Omega \\ \hline I_D \cong 6.7 \ A, \ V_{GEN} = 0 \ V \\ \hline I_T \ I$	$\begin{tabular}{ c c c c } \hline V_{DS} & V_{GS} = 0 \ V, \ I_{D} = 250 \ \mu A & 60 \\ \hline \Delta V_{DS} \ / T_J & I_D = 250 \ \mu A & 1.5 \\ \hline I_D = 250 \ \mu A & 1.5 \\ \hline V_{GS}(h) & V_{DS} = V_{GS}, \ I_D = 250 \ \mu A & 1.5 \\ \hline I_{GSS} & V_{DS} = 0 \ V, \ V_{GS} = 12 \ V & 10 \\ \hline V_{DS} = 60 \ V, \ V_{GS} = 0 \ V, \ T_J = 55 \ ^{\circ}C & 10 \\ \hline V_{DS} = 60 \ V, \ V_{GS} = 4.5 \ V & 25 \\ \hline V_{DS} = 60 \ V, \ V_{GS} = 4.5 \ V & 25 \\ \hline V_{DS} = 10 \ V, \ I_D = 4.5 \ A & 10 \\ \hline V_{DS} = 10 \ V, \ I_D = 4.3 \ A & 10 \\ \hline V_{DS} = 10 \ V, \ V_{GS} = 10 \ V, \ I_D = 6.3 \ A & 10 \\ \hline V_{DS} = 10 \ V, \ V_{GS} = 10 \ V, \ I_D = 6.3 \ A & 10 \\ \hline V_{DS} = 10 \ V, \ V_{GS} = 4.5 \ V, \ I_D = 6.3 \ A & 10 \\ \hline V_{DS} = 10 \ V, \ V_{GS} = 4.5 \ V, \ I_D = 6.3 \ A & 10 \\ \hline V_{DS} = 10 \ V, \ V_{GS} = 4.5 \ V, \ I_D = 6.3 \ A & 10 \\ \hline V_{DS} = 10 \ V, \ V_{GS} = 4.5 \ V, \ I_D = 10 \ V$	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $				

Notes:

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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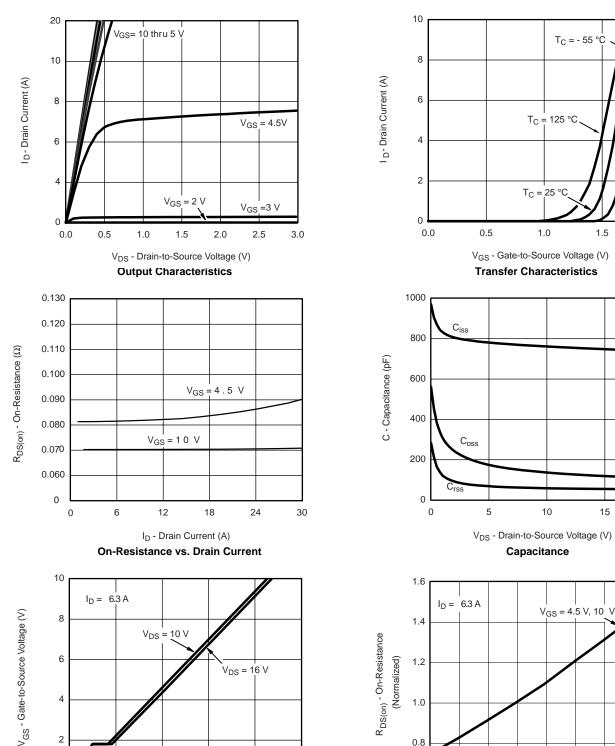
T<sub>C</sub> = - 55

1.5

15

20

2.0



1.0

0.8

0.6

- 50

- 25

0

25

50

 $T_J$  - Junction Temperature (°C)

**On-Resistance vs. Junction Temperature** 

75

100

125

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



4

2

0

0

5

10

15

Qg - Total Gate Charge (nC)

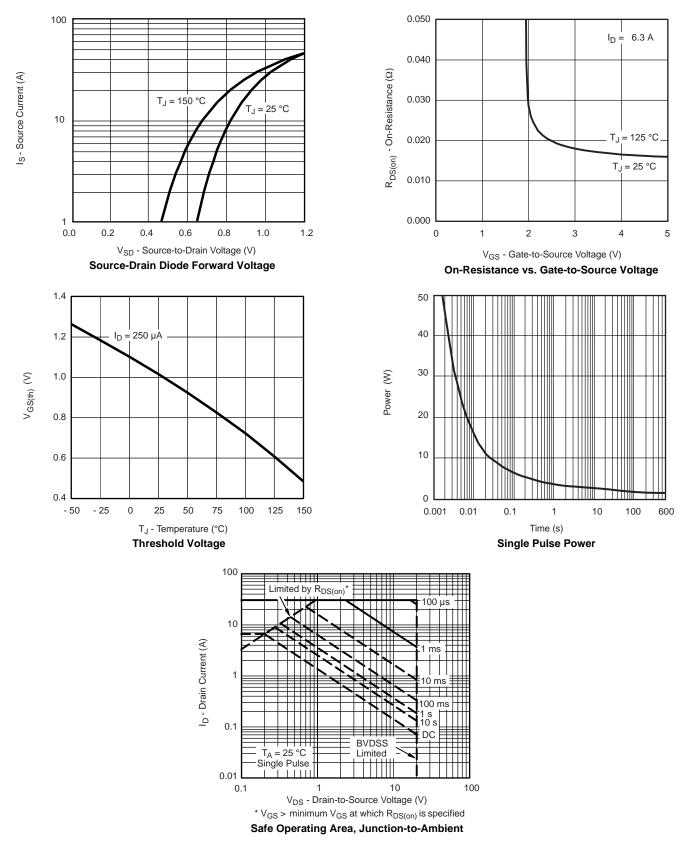
Gate Charge

20

25

150

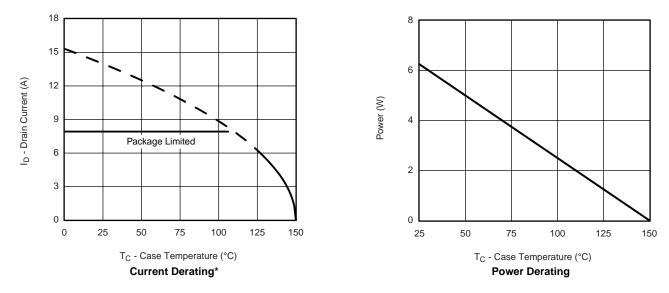




#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



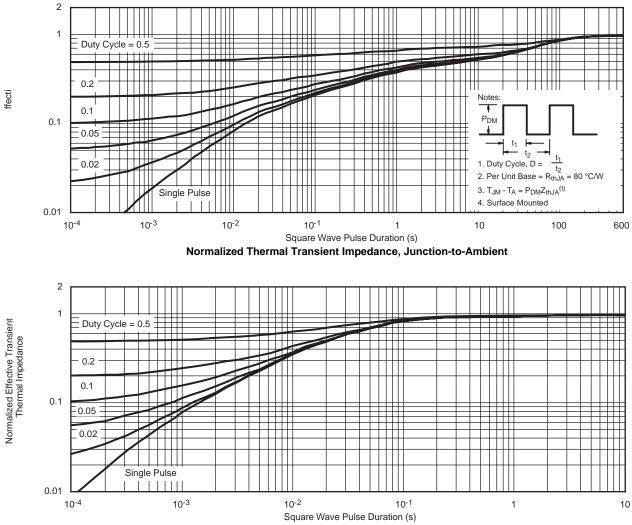
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



\* The power dissipation  $P_D$  is based on  $T_{J(max)}$  = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





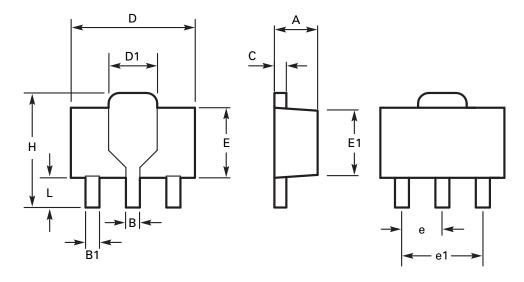


Normalized Thermal Transient Impedance, Junction-to-Foot

## 2SK3293



### Package outline - SOT89



DIM	Millim	neters	Inc	hes	DIM	Millimeters		limeters Inches	
	Min	Max	Min	Max		Min	Max	Min	Max
Α	1.40	1.60	0.550	0.630	E	2.29	2.60	0.090	0.102
В	0.44	0.56	0.017	0.022	E1	2.13	2.29	0.084	0.090
B1	0.36	0.48	0.014	0.019	е	1.50 BSC		0.059 BSC	
С	0.35	0.44	0.014	0.017	e1	3.00 BSC		0.118 BSC	
D	4.40	4.60	0.173	0.181	Н	3.94	4.25	0.155	0.167
D1	1.62	1.83	0.064	0.072	L	0.89	1.20	0.035	0.047

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches



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