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COUNT	DESCRIPTION OF REVISIONS	BY	CHKD	DATE	COUNT	DESCRIPTION OF REVISIONS	BY	CHKD	DATE	
△					△					
△					△					
APPLICABLE STANDARD										
RATING	VOLTAGE	250 V AC			OPERATING TEMPERATURE RANGE	-30 °C TO +85 °C(NOTE1)				
	CURRENT	3A			STORAGE TEMPERATURE RANGE	-10 °C TO +60 °C(NOTE2)				
	APPLICABLE CABLE	UL1061,UL1007:AWG24~28			APPLICABLE CONNECTOR	DF3A-*P-2DSA(**)				
SPECIFICATIONS										
ITEM		TEST METHOD			REQUIREMENTS				QT	AT
CONSTRUCTION										
GENERAL EXAMINATION		VISUALLY AND BY MEASURING INSTRUMENT.			ACCORDING TO DRAWING.				×	×
MARKING		CONFIRMED VISUALLY.							×	×
ELECTRIC CHARACTERISTICS										
CONTACT RESISTANCE		100 mA (DC OR 1000 Hz).			30 mΩ MAX.				×	—
INSULATION RESISTANCE		500V DC			1000 MΩ MIN				×	—
VOLTAGE PROOF		650V AC FOR 1 min			NO RLASHOVER OR BREAKDOWN				×	—
MECHANICAL CHARACTERISTICS										
MECHANICAL OPERATION		30TIMES INSERTIONS AND EXTRACTIONS.			① CONTACT RESISTANCE: 30 mΩ MAX. ② NO DAMAGE, CRACK AND LOSENESS OF PARTS.				×	—
VIBRATION		FREQUENCY 10 TO 55 Hz, SINGLE AMPLITUDE 0.75 mm, AT 2 h, FOR 3 DIRECTIONS.			① NO ELECTRICAL DISCONTINUITY OF 1 μs. ② NO DAMAGE, CRACK AND LOSENESS OF PARTS.				×	—
SHOCK		490 m/s ² DURATION OF PULSE 11 ms AT 3 TIMES FOR 3 DIRECTIONS.			① NO ELECTRICAL DISCONTINUITY OF 1 μs. ② NO DAMAGE, CRACK AND LOSENESS OF PARTS.				×	—
ENVIRONMENTAL CHARACTERISTICS										
DAMP HEAT (STEADY STATE)		EXPOSED AT 40±2 °C, 90 ~ 95 %, 96 h.			① CONTACT RESISTANCE: 30 mΩ MAX. ② INSULATION RESISTANCE:1000MΩ MIN. NO DAMAGE, CRACK AND LOSENESS OF PARTS.				×	—
RAPID CHANGE OF TEMPERATURE		TEMPERATURE -55 →5 TO 35 →+85 →5 TO 35 °C TIME 30 →10 TO 15 →30 →10 TO15min UNDER 5 CYCLES.			① CONTACT RESISTANCE: 30 mΩ MAX. ② INSULATION RESISTANCE:1000MΩ. ③ NO DAMAGE, CRACK AND LOSENESS OF PARTS.				×	—
RESISTANCE TO SOLDERING HEAT		SOLDER TEMPERATURE, 260±5°C FOR IMMERSION,DURATION,5S.			NO DEFORMATION OF CASE OF EXCESSIVE LOOSENESS OF THE TERMINALS.				×	—
SOLDERABILITY		SOLDERED AT SOLDER TEMPERATURE, 230±5°C FOR IMMERSION DURATION, 3S.			A NEW UNIFORM COATING OF SOLDER SHALL COVER A MINIMUM OF 95% OF THE SURFACE BEING IMMERSED .				×	—
REMARKS NOTE1:INCLUDING THE TEMPERATURE RISE BY CURRENT. NOTE2:APPLY TO THE CONDITION OF LONG TERM STORAGE FOR UNUSED PRODUCTS BEFORE PCB ON BOARD, AFTER PCB BOARD,OPERATING TEMPERATURE AND HUMIDITY RANGE IS APPLIED FOR INTERIM STORAGE DURING TRANSPORTATION.					DRAWN	DESIGNED	CHECKED	APPROVED	RELEASED	
					M.Nakamoto	<i>H. Umehara</i>	<i>T. Miyazaki</i>	<i>J. Oma</i>		
					04.03.22	04.03.22	04.03.22	04.03.22		
Unless otherwise specified, refer to JIS C 5402.										
Note QT:Qualification Test AT:Assurance Test ×:Applicable Test										
HRS HIROSE ELECTRIC CO., LTD.					SPECIFICATION SHEET			PART NO.		
								DF3-*S-2DSA (25)		
CODE NO.(OLD)		DRAWING NO.			PART NO.			1/1		
CL		ELC4-162329-10			CL543-					

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