D2TO35

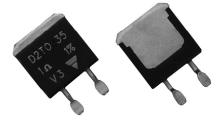
RoHS

COMPLIANT

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Surface Mount Power Resistor Thick Film Technology

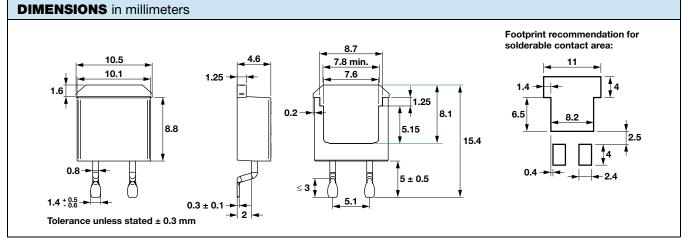


LINKS TO ADDITIONAL RESOURCES



FEATURES

- AEC-Q200 qualified
- 35 W at 25 °C case temperature
- Surface mounted resistor TO-263 (D²PAK) style package
- Wide resistance range from 0.01 Ω to 550 k Ω
- Non inductive
- Resistor isolated from metal tab
- Solder reflow secure at 270 °C/10 s
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



Notes

- Planarity measurement according to JEDEC TO-263D
- For the assembly on board, we recommend the lead (Pb)-free thermal profile as per J-STD-020C
- Power dissipation is 3.5 W at an ambient temperature of 25 °C when mounted on a double sided copper board using FR4 HTG, 70 μm of copper, 39 mm x 30 mm x 1.6 mm, with thermal vias

| STANDARD ELECTRICAL SPECIFICATIONS | | | | | | | |
|------------------------------------|--------|--------------|---|------------------|---|---------------------|-------|
| MODEL | | | LIMITING ELEMENT VOLTAGE U _L V | TOLERANCE ± % | TEMPERATURE COEFFICIENT ± ppm/°C CRITIC RESISTA Ω | | |
| D2TO35 | TO-263 | 0.01 to 550K | 35 | 500 | 1, 2, 5, 10 | 150, 250, 700, 1100 | 7.14K |

| MECHANICAL SPECIFICATIONS | | | | |
|---------------------------|---------------|--|--|--|
| Mechanical Protection | Molded | | | |
| Resistive Element | Thick film | | | |
| Substrate | Alumina | | | |
| Connections | Tinned copper | | | |
| Weight | 2.2 g max. | | | |

| ENVIRONMENTAL SPECIFICATIONS | | | | |
|------------------------------|--|--|--|--|
| Temperature Range | -55 °C to +175 °C | | | |
| | IEC 60695-11-5 | | | |
| Flammability | Application time: $t_a = 10 s$ Burning duration: $t_b < 30 s$ | | | |

| Power Rating and Thermal Resistance of the Component | 35 W at 25 °C | | | | |
|--|---|--|--|--|--|
| or the component | 35 W at 25 °C (case temperature) R _{TH (j - c}): 4.28 °C/W | | | | |
| Temperature Coefficient | See Special Feature table | | | | |
| Standard | ± 150 ppm/°C | | | | |
| Dielectric Strength IEC 60115-1 | 2000 V _{RMS} - 1 min - 10 mA max. (between terminals and board) | | | | |
| Insulation Resistance | $\geq 10^4 \text{ M}\Omega$ | | | | |
| Inductance | ≤ 0.1 µH | | | | |
| DIMENSIONS | | | | | |
| Standard Package | TO-263 style (D ² PAK) | | | | |

Revision: 26-Feb-2024

1 For technical questions, contact: <u>sferfixedresistors@vishay.com</u> Document Number: 51058

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| SPECIAL FEATURES | | | | | |
|--|-----------------|--------------|--------------|--------------|--|
| Resistance Values | ≥ 0.010 | ≥ 0.045 | ≥ 0.1 | ≥ 0.5 | |
| Tolerances | ± 1 % at ± 10 % | | | | |
| Requirement Temperature Coefficient (TCR) (-55 °C +150 °C) IEC 60115-1 | ± 1100 ppm/°C | ± 700 ppm/°C | ± 250 ppm/°C | ± 150 ppm/°C | |

| PERFORMANCE | | | | | |
|---------------------------|--|----------------------|--|--|--|
| TESTS | CONDITIONS | REQUIREMENTS | | | |
| Momentary Overload | IEC 60115-1 §4.13 1.7 Pr 5 s for $R < 2 \Omega$ 1.4 Pr 5 s for $R \ge 2 \Omega$ US < 1.5 UL | ± (0.25 % + 0.005 Ω) | | | |
| Load Life | IEC 60115-1 1000 h, 90/30 Pr at +25 °C | ± (0.5 % + 0.005 Ω) | | | |
| High Temperature Exposure | AEC-Q200 rev. D conditions: MIL-STD-202 method 108 1000 h, +175 °C, unpowered | ± (0.25 % + 0.005 Ω) | | | |
| Temperature Cycling | AEC-Q200 rev. D conditions: pre-conditioning 3 reflows according JESTD020D JESD22 method JA-104 1000 cycles, (-55 °C to +125 °C) dwell time 15 min | ± (0.5 % + 0.005 Ω) | | | |
| Moisture Resistance | AEC-Q200 rev. D conditions: MIL-STD-202 method 106 10 cycles, 24 h, unpowered | ± (0.5 % + 0.005 Ω) | | | |
| Biased Humidity | AEC-Q200 rev. D conditions: MIL-STD-202 method 103 1000 h, 85 °C, 85 % RH | ± (0.5 % + 0.005 Ω) | | | |
| Operational Life | AEC-Q200 rev. D conditions: Pre-conditioning 3 reflows according JESTD020D MIL-STD-202 method 108 2000 h, 90/30, powered, +125 °C | ± (0.5 % + 0.005 Ω) | | | |
| ESD Human Body Model | AEC-Q200 rev. D conditions: AEC-Q200-002 25 kV _{AD} | ± (0.5 % + 0.005 Ω) | | | |
| Vibration | AEC-Q200 rev. D conditions: MIL-STD-202 method 204 5 g's for 20 min, 12 cycles test from 10 Hz to 2000 Hz | ± (0.2 % + 0.005 Ω) | | | |
| Mechanical Shock | AEC-Q200 rev. D conditions: MIL-STD-202 method 213 100 g's, 6 ms, 3.75 m/s 3 shocks/direction | ± (0.2 % + 0.005 Ω) | | | |
| Board Flex | AEC-Q200 rev. D conditions: AEC-Q200-005 bending 2 mm, 60 s | ± (0.25 % + 0.01 Ω) | | | |
| Terminal Strength | AEC-Q200 rev. D conditions: AEC-Q200-006 1.8 kgf, 60 s | ± (0.25 % + 0.01 Ω) | | | |

| ASSEMBLY SPECIFICATIONS | | | | | |
|--|--|--|--|--|--|
| For the assembly on board, we recommend the lead (Pb)-free thermal profile as per J-STD-020C | | | | | |
| TESTS | CONDITIONS | REQUIREMENTS | | | |
| Resistance to Soldering Heat | IEC 60115-1 IEC 60068-2-58 Solder bath method: 270 °C/10 s | ± (0.5 % + 0.005 Ω) | | | |
| Moisture Sensitivity Level (MSL) | IPC/JEDEC [®] J-STD-020C 85 ℃ / 85 % RH / 168 h | Level: 1 + pass requirements of TCR overload and dielectric strength after MSL | | | |

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CHOICE OF THE BOARD

The user must choose the board according to the working conditions of the component (power, room temperature). Maximum working temperature must not exceed 175 °C. The dissipated power is simply calculated by the following ratio:

$$P = \frac{\Delta T}{R_{TH (j - c)} + R_{TH (c - h)} + R_{TH (h - a)}} (1)$$

P: expressed in W

- ΔT: difference between maximum working temperature and room temperature or fluid cooling temperature
- R_{TH (j c)}: thermal resistance value measured between resistive layer and outer side of the resistor. It is the thermal resistance of the component: 4.28 °C/W.
- R_{TH (c h}): thermal resistance value measured between outer side of the resistor and upper side of the board. This is the thermal resistance of the solder layer.

 $R_{TH (h-a)}$: thermal resistance of the board.

Example:

 $R_{TH (c - h)} + R_{TH (h - a)}$ for D2TO35 power rating 3.5 W at ambient temperature +25 °C. Thermal resistance $R_{TH (j - c)}$: 4.28 °C/W Considering equation ⁽¹⁾ we have:

$$\begin{split} \Delta T &= 175 \ ^{\circ}\text{C} - 25 \ ^{\circ}\text{C} = 150 \ ^{\circ}\text{C} \\ \text{R}_{\text{TH (j - c)}} + \text{R}_{\text{TH (c - h)}} + \text{R}_{\text{TH (h - a)}} = \Delta T/\text{P} = 150/3.5 = 42.8 \ ^{\circ}\text{C/W} \\ \text{R}_{\text{TH (c - h)}} + \text{R}_{\text{TH (h - a)}} = 42.8 \ ^{\circ}\text{C/W} - 4.28 \ ^{\circ}\text{C/W} = 38.52 \ ^{\circ}\text{C/W} \end{split}$$

Single Pulse:

These informations are for a single pulse on a cold resistor at 25 °C (not already used for a dissipation) and for pulses of 100 ms maximum duration.

The formula used to calculate E is:

$$E = P \times t = \frac{U^2}{R} \times t$$

with:

E (J): pulse energy

P (W): pulse power

t (s): pulse duration

U (V): pulse voltage

 $R(\Omega)$: resistor

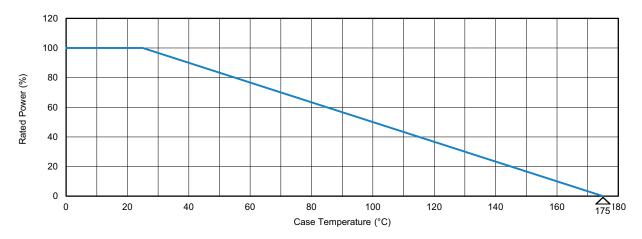
The energy calculated must be less: than that allowed by the graph.



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POWER RATING

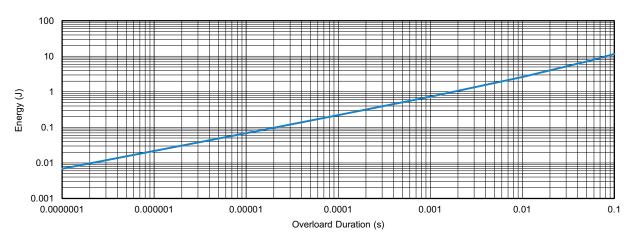
The temperature of the case should be maintained within the limits specified.



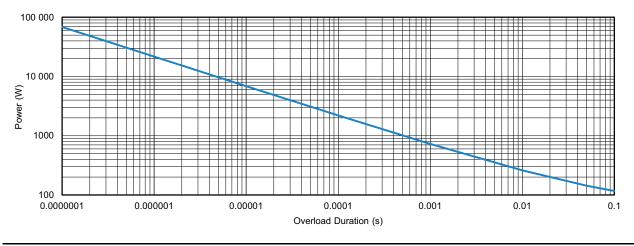
OVERLOADS

In any case the applied voltage must be lower than the maximum overload voltage of 750 V. The values indicated on the graph below are applicable to resistors in air or mounted onto a board.

ENERGY CURVE



POWER CURVE



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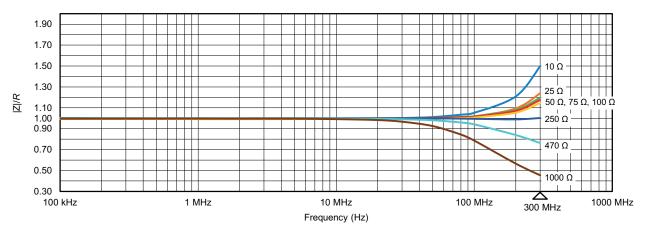
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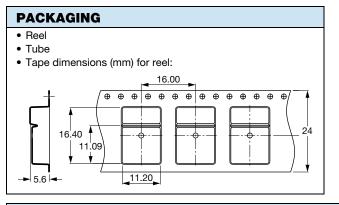


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IMPEDANCE CURVE 10 Ω to 1 k Ω from 100 kHz to 300 MHz

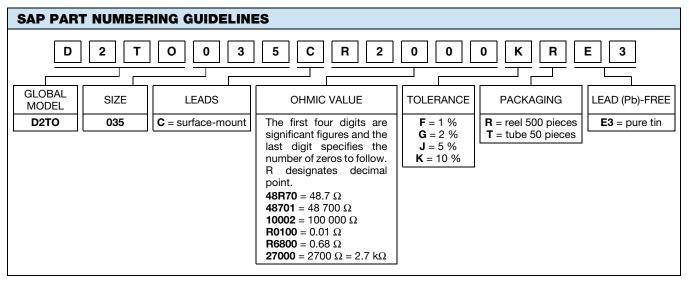




MARKING

Model, style, resistance value (in Ω), tolerance (in %), manufacturing date, Vishay Sfernice trademark

| ORDERIN | g inform | ATION | | | | |
|---------|----------|-------------|---------------------|--|-------------------------------------|----------------|
| D2TO | 35 | С | 100 k Ω | ±1% | XXX | e3 |
| MODEL | STYLE | CONNECTIONS | RESISTANCE VALUE | TOLERANCE | CUSTOM DESIGN | LEAD (Pb)-FREE |
| | | | | $F = \pm 1 \% G = \pm 2 \% J = \pm 5 \% K = \pm 10 \%$ | Optional on request: shape, etc. | |





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