

## N-Channel 30 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	30					
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 V$	0.0014					
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 V$	0.0016					
I <sub>D</sub> (A)	260					
Configuration	Single					

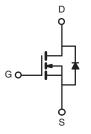
#### FEATURES

- Halogen-free According to IEC 61249-2-21
  Definition
- TrenchFET<sup>®</sup> Power MOSFET
- Package with Low Thermal Resistance
- 100 % R<sub>g</sub> and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



FREE





N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub>	= 25 °C, unles	ss otherwise noted)	)			
PARAMETER		SYMBOL	LIMIT	UNIT		
Drain-Source Voltage		V <sub>DS</sub>	30	V		
Gate-Source Voltage		V <sub>GS</sub>	± 20	V		
Continuous Drain Current	T <sub>C</sub> = 25 °C		260			
	T <sub>C</sub> = 125 °C	ID	120 <sup>a</sup>			
Continuous Source Current (Diode Conduction) <sup>a</sup>	I <sub>S</sub>	120	A			
Pulsed Drain Current <sup>b</sup>	I <sub>DM</sub>	680				
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	82			
Single Pulse Avalanche Energy		E <sub>AS</sub>	336	mJ		
Maximum Power Dissipation <sup>b</sup>	T <sub>C</sub> = 25 °C	Р	375	W		
Waximum Fower Dissipation	T <sub>C</sub> = 125 °C	P <sub>D</sub>	125			
Operating Junction and Storage Temperature Rang	e	T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 175	°C		

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount <sup>c</sup>	R <sub>thJA</sub>	40	°C/W
Junction-to-Case (Drain)		R <sub>thJC</sub>	0.4	C/ VV

#### Notes

a. Package limited.

- b. Pulse test; pulse width  $\leq 300~\mu s,~duty~cycle \leq 2~\%.$
- c. When mounted on 1" square PCB (FR-4 material).
- d. Parametric verification ongoing.

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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static						I	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> =	= 0 V, I <sub>D</sub> = 250 μA	30	-	-	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	= V <sub>GS</sub> , I <sub>D</sub> = 250 μΑ	1.5	2.0	2.5	V
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> =	0 V, $V_{GS} = \pm 20 V$	-	-	± 100	nA
		$V_{GS} = 0 V$	V <sub>DS</sub> = 30 V	-	-	1	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{GS} = 0 V$	$V_{DS} = 30 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$	-	-	50	μA
		$V_{GS} = 0 V$	V <sub>DS</sub> = 30 V, T <sub>J</sub> = 175 °C	-	-	250	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = 10 V	$V_{DS} \ge 5 V$	120	-	-	Α
		$V_{GS} = 10 V$	I <sub>D</sub> = 30 A	-	0.0014	-	
Ducia Course On Otata Decistance?	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 30 A, T <sub>J</sub> = 125 °C	-	0.0023	-	Ω
Drain-Source On-State Resistance <sup>a</sup>		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 30 A, T <sub>J</sub> = 175 °C	-	0.0028	-	
		$V_{GS} = 4.5 V$	I <sub>D</sub> = 20 A	-	0.0016	-	
Forward Transconductanceb	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A		-	190	-	S
Dynamic <sup>b</sup>	·						•
Input Capacitance	C <sub>iss</sub>			-	12 484	15 605	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$	V <sub>GS</sub> = 0 V V <sub>DS</sub> = 15 V, f = 1 MHz		2204	2755	pF
Reverse Transfer Capacitance	C <sub>rss</sub>	1		-	860	1075	
Total Gate Charge <sup>c</sup>	Qg			-	179	270	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{GS} = 10 \text{ V}$ $V_{DS} = 10 \text{ V}, I_D = 120 \text{ A}$		-	34	-	nC
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			-	21	-	1
Gate Resistance	R <sub>g</sub>	f = 1 MHz		0.59	1.19	1.79	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			-	18	27	
Rise Time <sup>c</sup>	t <sub>r</sub>	$\label{eq:VDD} \begin{array}{l} V_{\text{DD}} = 15 \; V, \; R_{\text{L}} = 0.3 \; \Omega \\ I_{\text{D}} \cong 50 \; A, \; V_{\text{GEN}} = 10 \; V, \; R_{\text{g}} = 1 \; \Omega \end{array}$		-	11	17	- ns
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			-	64	96	
Fall Time <sup>c</sup>	t <sub>f</sub>	]	-	11	17		
Source-Drain Diode Ratings and Char	acteristics <sup>b</sup>			·	·		·
Pulsed Current <sup>a</sup>	I <sub>SM</sub>			-	-	480	Α
Forward Voltage	V <sub>SD</sub>	I <sub>F</sub> =	60 A, V <sub>GS</sub> = 0 V	-	0.81	1.5	V

Notes

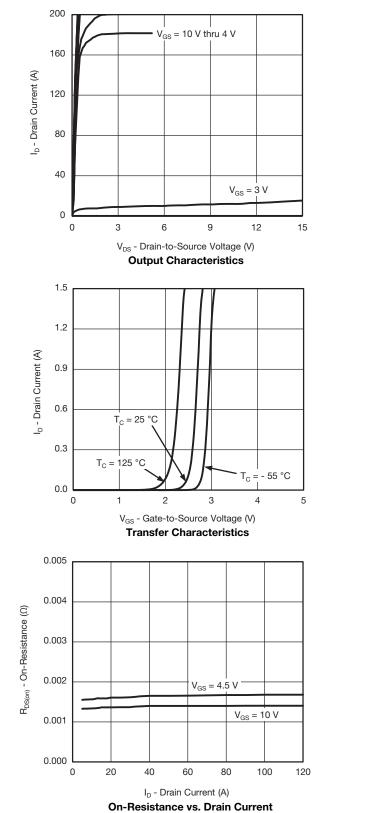
a. Pulse test; pulse width  $\leq 300~\mu\text{s},$  duty cycle  $\leq 2~\%.$ 

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





#### 150 120 I<sub>D</sub> - Drain Current (A) 90 T<sub>C</sub> = 25 °C 60 30 T<sub>C</sub> = 125 °C T<sub>C</sub> = - 55 °C 0 0 1 2 3 4 5 V<sub>GS</sub> - Gate-to-Source Voltage (V) Transfer Characteristics 350 - 55 °C $T_{C} =$ 280 25 °C g<sub>fs</sub> - Transconductance (S) 210 T<sub>C</sub> = 125 °C 140 70 0 14 28 42 56 70 0 I<sub>D</sub> - Drain Current (A) Transconductance 15 000 $C_{iss}$ 12 000 C - Capacitance (pF) 9000 6000 3000 C C<sub>rss</sub>

0

0

6

12

18

V<sub>DS</sub> - Drain-to-Source Voltage (V)

Capacitance

24

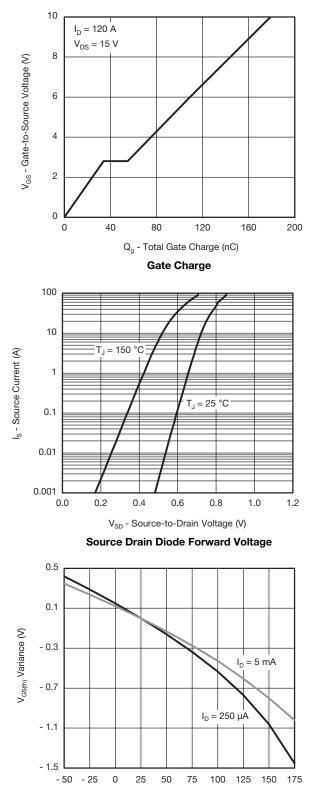
#### **TYPICAL CHARACTERISTICS** ( $T_A = 25$ °C, unless otherwise noted)

30



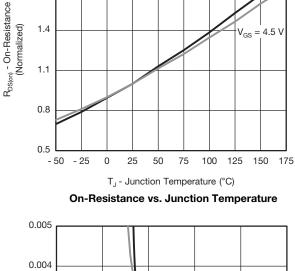
V<sub>GS</sub> = 10 V

 $V_{GS} = 4.5 V$ 



T<sub>J</sub> - Temperature (°C) Threshold Voltage

#### TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise noted)



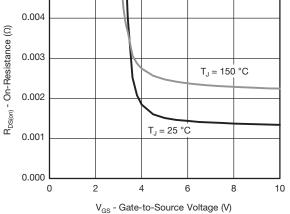
2.0

1.7

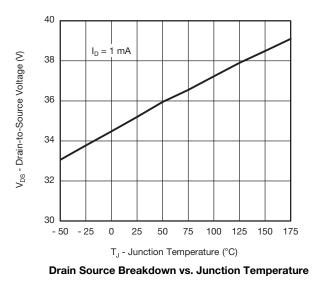
1.4

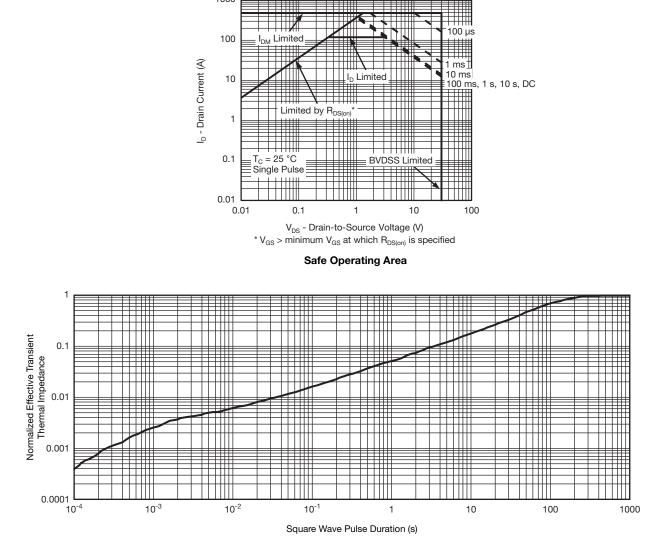
1.1

I<sub>D</sub> = 30 Å









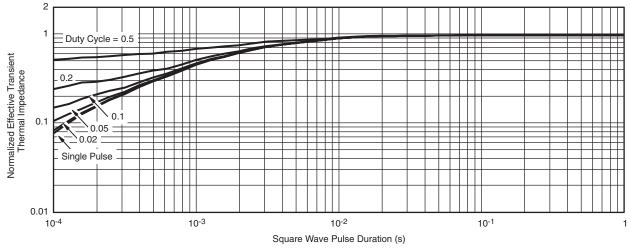
#### **THERMAL RATINGS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)

1000

Normalized Thermal Transient Impedance, Junction-to-Ambient



#### **THERMAL RATINGS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

#### Note

• The characteristics shown in the two graphs

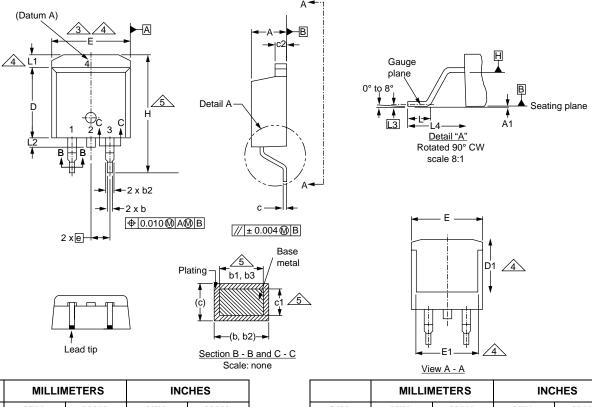
- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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#### **TO-263AB (HIGH VOLTAGE)**



	MILLIMETERS		INCHES			MILLIMETERS		INCHES	
DIM.	MIN.	MAX.	MIN.	MAX.	DIM.	MIN.	MAX.	MIN.	MAX.
А	4.06	4.83	0.160	0.190	D1	6.86	-	0.270	-
A1	0.00	0.25	0.000	0.010	E	9.65	10.67	0.380	0.420
b	0.51	0.99	0.020	0.039	E1	6.22	-	0.245	-
b1	0.51	0.89	0.020	0.035	е	2.54 BSC		0.100 BSC	
b2	1.14	1.78	0.045	0.070	Н	14.61	15.88	0.575	0.625
b3	1.14	1.73	0.045	0.068	L	1.78	2.79	0.070	0.110
С	0.38	0.74	0.015	0.029	L1	-	1.65	-	0.066
c1	0.38	0.58	0.015	0.023	L2	-	1.78	-	0.070
c2	1.14	1.65	0.045	0.065	L3	0.25 BSC		0.010 BSC	
D	8.38	9.65	0.330	0.380	L4	4.78	5.28	0.188	0.208

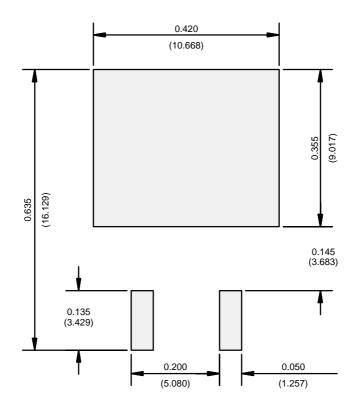
#### Notes

1. Dimensioning and tolerancing per ASME Y14.5M-1994.

- 2. Dimensions are shown in millimeters (inches).
- 3. Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body at datum A.
- 4. Thermal PAD contour optional within dimension E, L1, D1 and E1.
- 5. Dimension b1 and c1 apply to base metal only.
- 6. Datum A and B to be determined at datum plane H.
- 7. Outline conforms to JEDEC outline to TO-263AB.



# RECOMMENDED MINIMUM PADS FOR D PAK: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)



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