



# NEO-F10N

Standard precision GNSS module

Professional grade

Data sheet



## Abstract

This data sheet describes the NEO-F10N module, an L1/L5 dual-band GNSS receiver for meter-level accuracy in urban environment.

**Note!** GPS L5 signals are pre-operational and not used by default. Refer to the Overview section for more information.

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# Document information

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<b>Initial production</b>	Early production information	Data from product verification. Revised and supplementary data may be published later.
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This document applies to the following products:

<b>Product name</b>	<b>Type number</b>	<b>FW version</b>	<b>IN/PCN reference</b>	<b>Product status</b>
NEO-F10N	NEO-F10N-00B-00	EXT SPG 6.00	UBXDOC-963802114-12575	Initial production

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# 1 Functional description

## 1.1 Overview

NEO-F10N is built on the u-blox F10 dual-band GNSS technology using L1 and L5 band signals. The proprietary dual-band multipath mitigation technology enables the u-blox F10 to use the best signals from the L1 and L5 bands providing a solid meter-level position accuracy in urban environment.

 At the time of writing, the GPS L5 signals remain pre-operational and are set as unhealthy until sufficient monitoring capability is established. This is an operational issue concerning the satellites / space segment and not a limitation of u-blox products.

 Due to the pre-operational status, the GPS L5 signals are not used for the navigation solution by default. However, it is possible to evaluate the GPS L5 signals before they become fully operational by changing the receiver configuration to override the GPS L5 health status. Refer to the Integration manual [1] for details.

## 1.2 Performance

Parameter	Specification	Value
Receiver type		u-blox F10 dual-band receiver
Accuracy of time pulse signal	RMS	30 ns
	99%	60 ns
Frequency of time pulse signal		Default 1PPS (0.25 Hz to 10 MHz configurable)
Operational limits <sup>1</sup>	Dynamics	≤ 4 g
	Altitude	80,000 m
	Velocity	500 m/s
Velocity accuracy <sup>2</sup>		0.05 m/s
Dynamic heading accuracy <sup>2</sup>		0.3 deg

**Table 1: NEO-F10N specifications**

Table 2 shows typical performance values in multi-GNSS configurations<sup>3</sup>. SBAS is enabled in all measurements.

Parameter	GPS+GAL +BDS (Default)	GPS+BDS	GPS+GAL	GPS+NavIC	Unit	
Max navigation update rate <sup>4</sup>	10	10	10	10	Hz	
Position accuracy (CEP) <sup>5</sup>	1	1	1	1	m	
Time To First Fix (TTFF) <sup>6</sup>	Cold start	28	28	27	28	s
	Hot start	2	2	2	2	s
	AssistNow Online <sup>7</sup>	2	2	2	2	s

<sup>1</sup> Assuming Airborne 4 g platform.

<sup>2</sup> 50% at 30 m/s for dynamic operation.

<sup>3</sup> The GPS L5 signal health status is ignored. Configuration required.

<sup>4</sup> Minimum 98% fix rate under typical conditions.

<sup>5</sup> CEP, 50%, 24 hours static, -130 dBm, > 6 SVs for each GNSS system.

<sup>6</sup> Commanded starts. All satellites signals at -130 dBm. Measured at room temperature.

<sup>7</sup> Depends on the speed and latency of the aiding data connection, commanded starts.

Parameter		GPS+GAL +BDS (Default)	GPS+BDS	GPS+GAL	GPS+NavIC	Unit
	AssistNow Offline <sup>8</sup>	3	3	3	3	s
	AssistNow Autonomous <sup>9</sup>	4	4	4	4	s
Sensitivity <sup>10</sup>	Tracking and navigation	-167	-167	-167	-167	dBm
	Reacquisition	-159	-159	-159	-159	dBm
	Cold Start	-148	-148	-148	-148	dBm
	Hot start	-159	-159	-159	-159	dBm

**Table 2: NEO-F10N typical performance in multi-GNSS configurations**

Table 3 shows typical performance values in single-GNSS configurations<sup>3</sup>. SBAS is enabled in all measurements.

Parameter		GPS	BDS	Unit
Max navigation update rate <sup>4</sup>		20	20	Hz
Position accuracy (CEP) <sup>5</sup>		1.5	1	m
Time To First Fix (TTFF) <sup>6</sup>	Cold start	29	42	s
	Hot start	2	2	s
	AssistNow Online <sup>7</sup>	2	N/A	s
Sensitivity <sup>10</sup>	Tracking and navigation	-167	-163	dBm
	Reacquisition	-159	-156	dBm
	Cold Start	-148	-137	dBm
	Hot start	-159	-157	dBm

**Table 3: NEO-F10N typical performance in single-GNSS configurations**

## 1.3 Supported GNSS constellations

NEO-F10N is a concurrent GNSS receiver that can receive and track multiple GNSS systems. The dual-band RF front-end architecture enables concurrent reception of multiple dual frequency GNSS constellations. The receiver can be configured for a subset of GNSS constellations to achieve lower power consumption.

The default configuration on NEO-F10N is concurrent reception of GPS, Galileo and BeiDou with SBAS enabled.

The following GNSS and their signals are supported:

System	Signals
GPS / QZSS	L1C/A (1575.42 MHz), L5 (1176.450 MHz)
Galileo	E1-B/C (1575.42 MHz), E5a (1176.450 MHz)
BeiDou	B1C (1575.42 MHz), B2a (1176.450 MHz)
NavIC	SPS-L5 (1176.450 MHz)

**Table 4: Supported GNSS and signals on NEO-F10N**

The following GNSS assistance services are supported:

<sup>8</sup> Using seven days old AssistNow Offline data. External memory may be required.

<sup>9</sup> Using two days old orbital predicted data. External memory may be required.

<sup>10</sup> Demonstrated with a good external LNA. Measured at room temperature.

Service	Support
AssistNow™ Online	GPS L1C/A, Galileo E1, QZSS L1C/A
AssistNow™ Offline	GPS L1C/A, Galileo E1, QZSS L1C/A
AssistNow™ Autonomous	GPS L1C/A, Galileo E1

**Table 5: Supported Assisted GNSS (A-GNSS) services**

The following augmentation systems are supported:

System	Support
SBAS <sup>11</sup>	EGNOS, GAGAN, MSAS and WAAS
QZSS	L1S (SLAS), L1Sb (SBAS)

**Table 6: Supported augmentation systems**

The augmentation system QZSS can be enabled only if GPS operation is also enabled.

## 1.4 Supported protocols

NEO-F10N supports the following interface protocols:

Protocol	Type
UBX	Input/output, binary, u-blox proprietary
NMEA versions 2.1, 2.3, 4.0, 4.10 and 4.11 (default).	Input/output, ASCII

**Table 7: Supported protocols**

## 1.5 Firmware features

Feature	Description
Assisted GNSS	AssistNow Online, AssistNow Offline and AssistNow Autonomous
Backup modes	Hardware backup mode and software standby mode
Super-S	Improved dynamic position accuracy with small antennas
Protection level	Real-time position accuracy estimate with 95% confidence level <sup>12</sup>
Galileo return link messages	Galileo search and rescue (SAR) return link messages (RLM) via Galileo satellite signal
Odometer	Measure traveled distance with support for different user profiles

**Table 8: Firmware features**

Feature	Description
Anti-jamming	RF interference and jamming detection and reporting
Anti-spoofing	Spoofing detection and reporting
Configuration lockdown	Receiver configuration can be locked by command
Message integrity	All messages can be cryptographically signed
Secure boot	Only signed firmware images executed

**Table 9: Security features**

<sup>11</sup> Ionospheric correction service is the only SBAS service supported by NEO-F10N

<sup>12</sup> Verified for automotive environment only.

## 2 System description

### 2.1 Block diagram

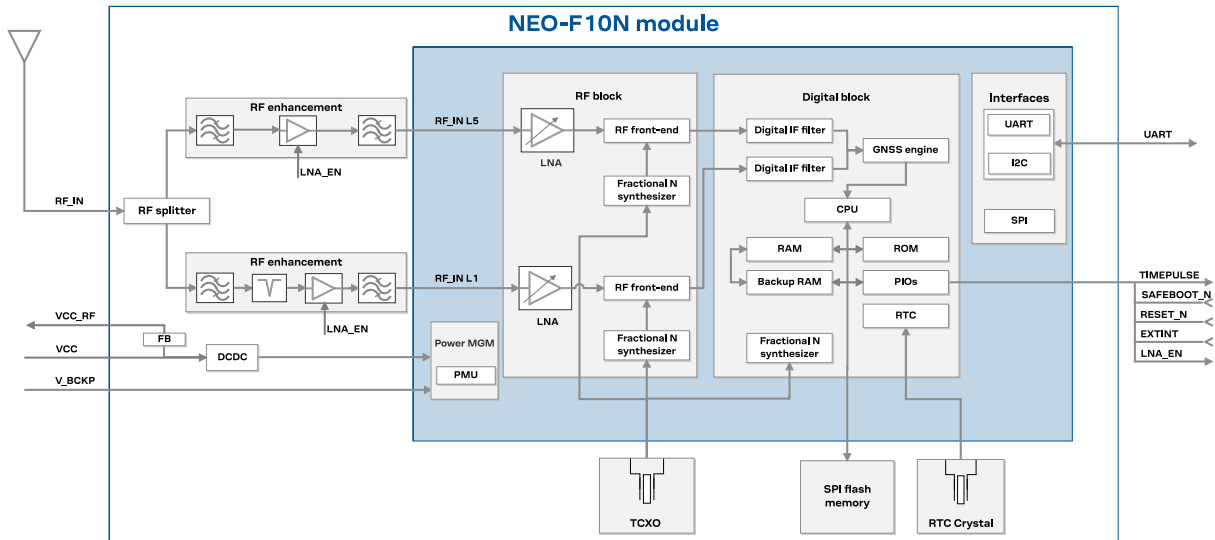


Figure 1: NEO-F10N block diagram

## 3 Pin definition

### 3.1 Pin assignment

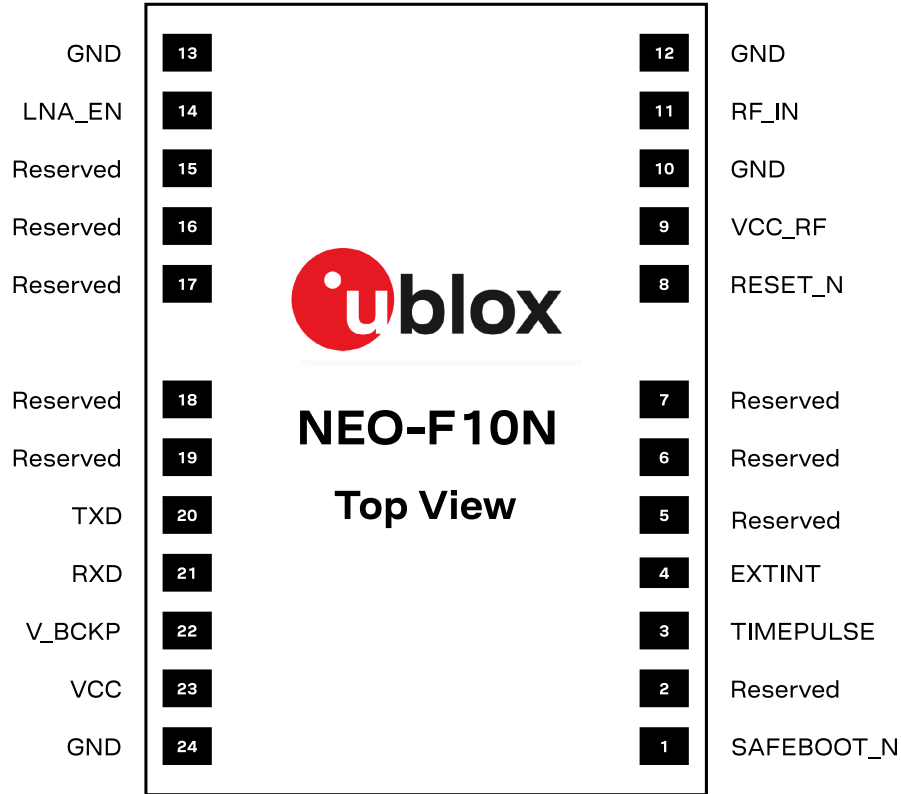


Figure 2: NEO-F10N pin assignment

Pin no.	Name	I/O	Description
1	SAFEBOOT_N	I	Safeboot mode (leave open) <sup>13</sup>
2	Reserved	-	Not connected
3	TIMEPULSE	O	Time pulse signal (shared with SAFEBOOT_N pin) <sup>13</sup>
4	EXTINT	I	External interrupt
5	Reserved	-	Not connected
6	Reserved	-	Not connected
7	Reserved	-	Not connected
8	RESET_N	I	RESET (active low)
9	VCC_RF	O	Output voltage RF section
10	GND	-	Ground
11	RF_IN	I	GNSS signal input
12	GND	-	Ground
13	GND	-	Ground

<sup>13</sup> The receiver enters safeboot mode if SAFEBOOT\_N pin is low at start up. The SAFEBOOT\_N pin is internally connected to TIMEPULSE pin through a 1 kΩ series resistor.



Pin no.	Name	I/O	Description
14	LNA_EN	O	On/Off internal LNAs and an optional external LNA or an active antenna
15	Reserved	-	Not connected
16	Reserved	-	Not connected
17	Reserved	-	Not connected
18	Reserved	-	Not connected
19	Reserved	-	Not connected
20	TXD	O	UART TX
21	RXD	I	UART RX
22	V_BCKP	I	Backup voltage supply
23	VCC	I	Supply voltage
24	GND	-	Ground



**Table 10: NEO-F10N pin assignment**

## 3.2 Pin state

Table 11 defines the state of the interface pins in different modes.

Pin no.	Function	Continuous mode	Software standby mode	Safe boot mode
21	RXD	Input pull-up	Input pull-up	Input pull-up
20	TXD	Output	Input pull-up	Output
1	SAFEBOOT_N <sup>13</sup>	Output	Input pull-up	Output (low)
3	TIMEPULSE	Output	Input pull-up	Output (low)
8	RESET_N	Input pull-up	Input pull-up	Input pull-up
4	EXTINT	Input pull-up	Input pull-up	Input pull-up

**Table 11: Pins state**

-  In the reset mode (RESET\_N = low), all interface pins are configured as input pull-ups.
-  Do not drive pins in the hardware backup mode (VCC = 0 V).

## 4 Electrical specifications

### 4.1 Absolute maximum ratings



CAUTION. Risk of device damage. Exceeding the absolute maximum ratings may affect the lifetime and reliability of the device or permanently damage it. Do not exceed the absolute maximum ratings.



This product is not protected against overvoltage or reversed voltages. Use appropriate protection to avoid device damage from voltage spikes exceeding the specified boundaries.

Symbol	Parameter	Min	Max	Unit
VCC	Main supply voltage	-0.3	3.6	V
	Voltage ramp on VCC <sup>14</sup>	25	35000	μs/V
V_BCKP	Backup supply voltage	-0.3	3.6	V
V_PIO	Input voltage on RESET_N and digital pins.	-0.3	VCC + 0.3	V
I_PIO	Max source / sink current, digital pins <sup>15</sup>	-10	10	mA
ICC_RF	Max source current, VCC_RF		250	mA
P <sub>rfin</sub>	RF input power on RF_IN <sup>16</sup>		0	dBm
T <sub>amb</sub>	Ambient temperature	-40	+85	°C
T <sub>s</sub>	Storage temperature	-40	+85	°C

**Table 12: Absolute maximum ratings**

### 4.2 Operating conditions

Table 13 shows the general operating conditions. Table 14 shows the electrical parameters for digital I/O.

Symbol	Parameter	Min	Typical	Max	Unit
VCC	Main supply voltage	2.7	3.0	3.6	V
V_BCKP	Supply voltage, backup domain	1.65		3.6	V
VCC <sub>SWITCH</sub>	VCC voltage threshold to switch an internal supply for the backup domain from VCC to V_BCKP		1.45		V
VCC_RF	VCC_RF output voltage		VCC - 0.1		V
ICC_RF	VCC_RF output current			50	mA
Z <sub>in</sub> <sup>17</sup>	Input impedance at RF_IN		50		Ω
NF <sub>tot</sub>	Receiver chain noise figure (L1/L5)		3/3		dB
Ext_gain <sup>18</sup>	External gain at RF_IN, normal gain mode (default)			25	dB
T <sub>opr</sub>	Operating temperature	-40		+85	°C

**Table 13: General operating conditions**

<sup>14</sup> Exceeding the voltage ramp speed may permanently damage the device.

<sup>15</sup> The SAFEBOOT\_N pin has an internal 1 kΩ series resistor.

<sup>16</sup> Test conditions: source impedance = 50 Ω, continuous wave.

<sup>17</sup> The RF\_IN input integrates a built-in DC block.

<sup>18</sup> The internal LNA gain is configurable.

Symbol	Parameter	Min	Typical	Max	Unit
$I_{leak}$	Leakage current input pins <sup>19</sup>		25		nA
$V_{in}$	Input pin voltage range	0		VCC	V
$V_{il}$	Low-level input voltage			0.63	V
$V_{ih}$	High-level input voltage	0.68 x VCC			V
$V_{ol}$	Low-level output voltage, $I_{out} = -2 \text{ mA}$ <sup>20</sup>			0.4	V
$V_{oh}$	High-level output voltage, $I_{out} = 2 \text{ mA}$ <sup>20</sup>	VCC - 0.4			V
$R_{pu, IO}$	Pull-up resistance, Digital IO	8	18	40	k $\Omega$
$R_{pd, IO}$	Pull-down resistance, Digital IO	21	80	180	k $\Omega$
$R_{pu, SAFEBOOT\_N}$	Pull-up resistance, SAFEBOOT_N <sup>21</sup>	6	17	72	k $\Omega$
$R_{pu, RESET\_N}$	Pull-up resistance, RESET_N	7	10	13	k $\Omega$

**Table 14: Digital IO**

### 4.3 Indicative power requirements

This section provides examples of typical current requirements. They are characterized on samples using a cold start command. The actual power requirements may vary depending on the firmware version used, the external circuitry, the number of satellites tracked, the signal strength, the type and time of start, duration, internal LNA gain mode, and the test conditions.

All values in [Table 15](#) and [Table 16](#) have been measured at 25 °C ambient temperature with the default configuration unless otherwise stated. SBAS is active in all measurements.

[Table 15](#) shows indicative current consumption for VCC with a 3.0 V supply.

Symbol (Parameter)	Conditions	GPS+GAL +BDS (Default)	GPS+BDS	GPS+GAL	GPS +NavIC	GPS	BDS	Unit
$I_{VCC}$ <sup>22</sup> (VCC current)	Acquisition <sup>23</sup>	26	26	22	21	20	24	mA
	Tracking	21	20	19	18	18	19	mA

**Table 15: Typical currents for 3.0 V supply at VCC**

The inrush current can go up to 100 mA at startup. Ensure that the external power supply is able to deliver up to 100 mA.

[Table 16](#) shows current consumption for backup modes.

Symbol	Parameter	Conditions	Typ.	Unit
$I_{V\_BCKP}$ <sup>24</sup>	Total current in hardware backup mode	$V\_BCKP = 3.0 \text{ V}$ ; $VCC = 0 \text{ V}$	31	$\mu\text{A}$
$I_{VCC}$	Total current in software standby mode	$VCC = 3.0 \text{ V}$	49	$\mu\text{A}$

**Table 16: Backup currents**

Extreme operating temperatures can significantly impact the specified values. If an application operates near the min or max temperature limits, ensure the specified values are not exceeded.

<sup>19</sup>  $V_{in} = VCC$ , at room temperature.

<sup>20</sup> TIMEPULSE has 4 mA current drive/sink capability.

<sup>21</sup> The SAFEBOOT\_N pin has an additional 1 k $\Omega$  series resistor.

<sup>22</sup> 1 Hz navigation update rate. Simulated signals using power levels of -130 dBm.

<sup>23</sup> Average current from start-up until the first fix.

<sup>24</sup>  $I_{V\_BCKP}$  current in normal operation ( $V\_BCKP = 3.0 \text{ V}$ ) is  $\sim 3 \mu\text{A}$ .

## 5 Communication interfaces

The receiver supports communication over UART only.

All the inputs have an internal pull-up resistor in normal operation and can be left open if not used. The voltage level at the PIO pins is related to the VCC supply voltage.

### 5.1 UART

The UART interface supports configurable baud rates. Hardware flow control is not supported. UART specifications are described in [Table 17](#).

Symbol	Parameter	Min	Max	Unit
$R_u$	Baud rate	9600	921600	bit/s
$\Delta_{Tx}$	Tx baud rate accuracy	-1%	+1%	-
$\Delta_{Rx}$	Rx baud rate tolerance	-2.5%	+2.5%	-

Table 17: UART specifications

### 5.2 Default interface settings

Interface	Settings
UART	<ul style="list-style-type: none"> <li>38400 baud<sup>25</sup>, 8 bits, no parity bit, 1 stop bit.</li> <li>Input messages: NMEA and UBX.</li> <li>Output messages: NMEA GGA, GLL, GSA, GSV, RMC, VTG and TXT.</li> </ul>

Table 18: Default interface settings

<sup>25</sup> 9600 baud in safe boot mode.

## 6 Mechanical specifications

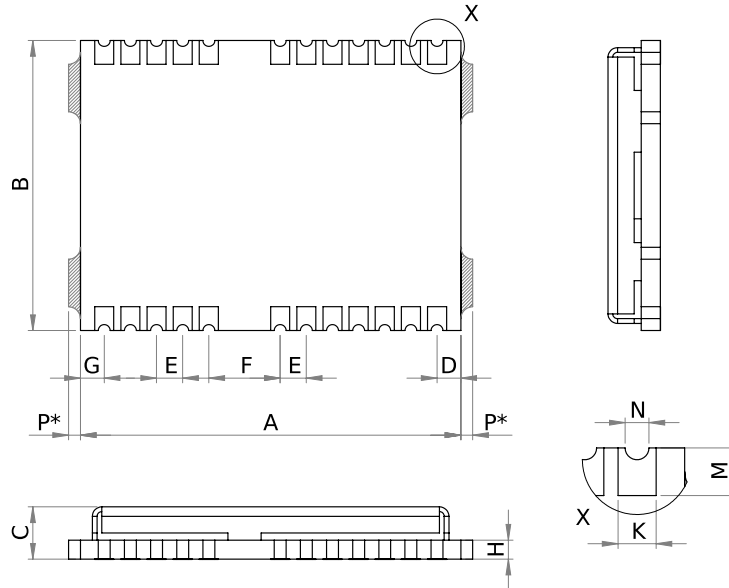




Figure 3: NEO-F10N mechanical drawing

Symbol	Min (mm)	Typical (mm)	Max (mm)	
A	15.9	16.0	16.1	
B	12.1	12.2	12.3	
C	2.2	2.4	2.6	
D	0.9	1.0	1.1	
E	1.0	1.1	1.2	
F	2.9	3.0	3.1	
G	0.9	1.0	1.1	
H	-	0.82	-	
K	0.7	0.8	0.9	
M	0.8	0.9	1.0	
N	0.4	0.5	0.6	
P*	0.0	-	0.5	The de-paneling residual tabs may be on either side (not both).
Weight		1.0 g		

Table 19: NEO-F10N mechanical dimensions

-  The mechanical picture of the de-paneling residual tabs (P\*) is an approximate representation, shape and position may vary.
-  Component keep-out area must consider that the de-paneling residual tabs can be on either side (not both).

## 7 Approvals

The NEO-F10N is designed for the presumption of conformity with the essential requirements and other relevant provisions of Radio Equipment Directive (RED) 2014/53/EU.

The NEO-F10N complies with the Directive 2011/65/EU (EU RoHS 2) and its amendment Directive (EU) 2015/863 (EU RoHS 3).

The Declaration of Conformity (DoC) is available at [u-blox website](#) within Support > File Category > Conformity and Certification.

## 8 Product handling

### 8.1 Moisture sensitivity level

The moisture sensitivity level (MSL) relates to the packaging and handling precautions required. NEO-F10N LCC (professional grade) package is rated at MSL level 4. For MSL standard, see IPC/JEDEC J-STD-020 [5].

## 9 Labeling and ordering information

This section provides information about product labeling and ordering.

### 9.1 Product labeling

The labeling of NEO-F10N package provides product information and revision information. For more information contact u-blox sales.

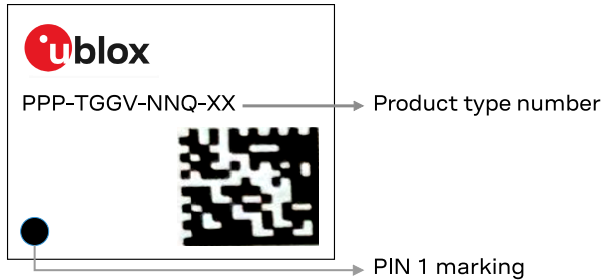


Figure 4: NEO-F10N label

The parts of the product code are explained in [Table 20](#)

Code	Meaning	Example
PPP	Product family	NEO
TGG	Platform	F10 = u-blox F10
V	Variant	N = Standard precision, TCXO, SAW filter, and LNA
NN	Option	00, 01, 02, ...
Q	Quality grade	A = Automotive, B = Professional
XX	Product detail	Describes hardware and firmware versions

Table 20: Part identification code

### 9.2 Explanation of product codes

Three product code formats are used in the product label. The product name is used in documentation such as this data sheet and identifies all u-blox products, independent of packaging and quality grade. The ordering code includes options and quality, while the type number includes the hardware and firmware versions.

[Table 21](#) describes the three different product code formats used in the NEO-F10N module.

Format	Structure	Product code
Product name	PPP-TGGV	NEO-F10N
Ordering code	PPP-TGGV-NNQ	NEO-F10N-00B
Type number	PPP-TGGV-NNQ-XX	NEO-F10N-00B-00

Table 21: Product code formats

### 9.3 Ordering codes

Ordering code	Product	Remark
NEO-F10N-00B	u-blox F10 multi-band GNSS receiver module, 24 pin LCC, professional grade	

Table 22: Product ordering codes





Product changes affecting form, fit or function are documented by u-blox. For a list of Product Change Notifications (PCNs) see our website at: <https://www.u-blox.com/en/product-resources>.

## Related documents

- [1] NEO-F10N Integration manual, [UBXDOC-963802114-12193](#)
- [2] u-blox F10 SPG 6.00 Interface description, [UBX-23002975](#)
- [3] u-blox F10 SPG 6.00 Release note, [UBXDOC-963802114-12318](#)
- [4] u-blox Package Information Guide, [UBX-14001652](#)
- [5] MSL standard IPC/JEDEC J-STD-020, [www.jedec.org](http://www.jedec.org)



For regular updates to u-blox documentation and to receive product change notifications please register on our homepage <https://www.u-blox.com>.

## Revision history

Revision	Date	Comments
R01	27-Jun-2023	Initial release
R02	01-Nov-2023	Engineering sample Updated: <ul style="list-style-type: none"><li>• KPIs in section Performance</li><li>• Supported signals in Assisted GNSS services in section Supported GNSS constellations</li><li>• Description of Reserved pins in section Pin definition</li><li>• Pin state of SAFEBOOT_N and TIMPULSE in section Pin state</li><li>• Typical and backup currents in section Indicative power requirements</li></ul>
R03	22-Dec-2023	Initial production Added: <ul style="list-style-type: none"><li>• Approvals section</li></ul> Updated: <ul style="list-style-type: none"><li>• Overview section with information related to GPS L5</li><li>• Pin state of SAFEBOOT_N and TIMPULSE in section Pin state</li><li>• Operating conditions section with input impedance at RF_IN</li></ul>

## Contact

### u-blox AG

Address: Zürcherstrasse 68  
8800 Thalwil  
Switzerland

For further support and contact information, visit us at [www.u-blox.com/support](http://www.u-blox.com/support).