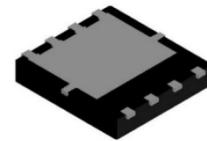


WNM6006

Single N-Channel, 60V, 100A, Power MOSFET

<http://www.ovt.com/>

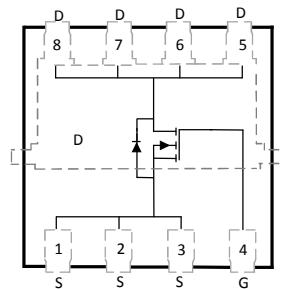
V_{DS} (V)	Max. R_{DS(on)} (mΩ)
60	3.4 @ V _{GS} =10V
	6.0 @ V _{GS} =6V



Description

The WNM6006 is N-Channel enhancement MOS Field Effect Transistor. Uses advanced trench technology and design to provide excellent R_{DS(ON)} with low gate charge. This device is suitable for use in DC-DC conversion, power switch and charging circuit. Standard Product WNM6006 is in compliance with RoHS.

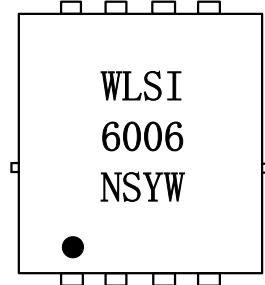
PDFN5X6-8L



Pin configuration (Top view)

Features

- Trench Technology
- Supper high density cell design
- Low ON resistance
- Package PDFN5X6-8L



WLSI = Company Code

6006 = Device Code

NS = Special Code

Y = Year

W = Week(A~z)

Marking

Applications

- DC/DC converters
- Power supply converters circuit
- Load/Power Switching for portable device

Order information

Device	Package	Shipping
WNM6006-8/TR	PDFN5X6-8L	3000/Tape&Reel

Absolute Maximum ratings

Parameter	Symbol	Maximum	Unit
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _{GS}	±20	
Continuous Drain Current	I _D	100	A
		63	A
Pulsed Drain Current ^c	I _{DM}	310	A
Continuous Drain Current	I _{DSM}	32	A
		25	
Avalanche Energy L=0.3mH	E _{AS}	158	mJ
Power Dissipation ^b	P _D	62	W
		25	
Power Dissipation ^d	P _{DSM}	6.3	W
		4.0	
Operating Junction Temperature	T _J	-55 to 150	°C
Storage Temperature Range	T _{STG}	-55 to 150	°C

Thermal resistance ratings

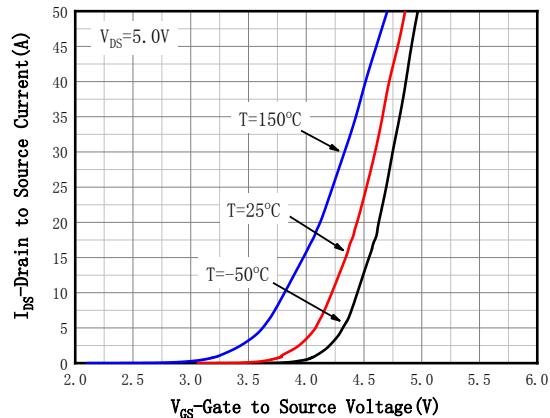
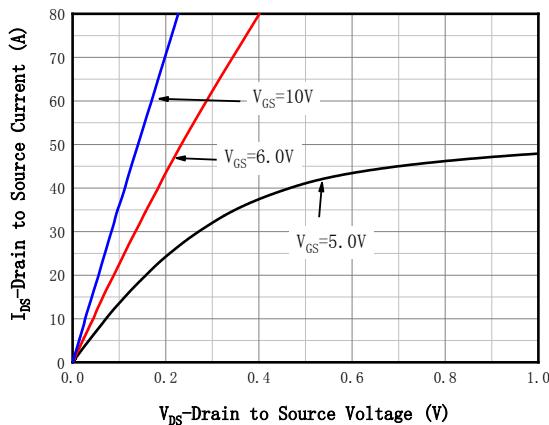
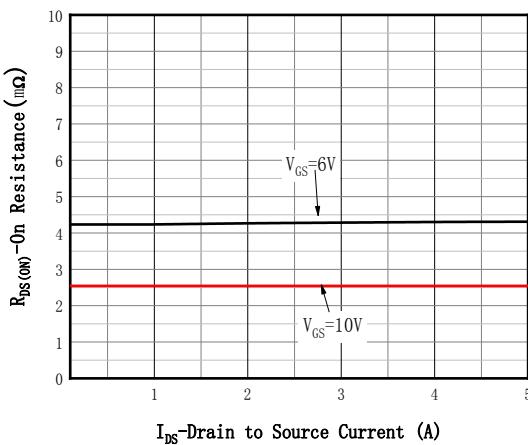
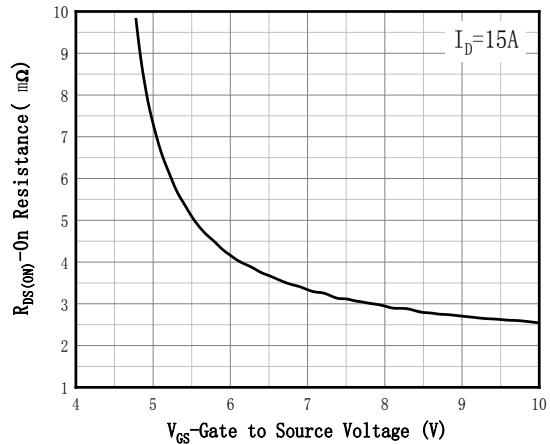
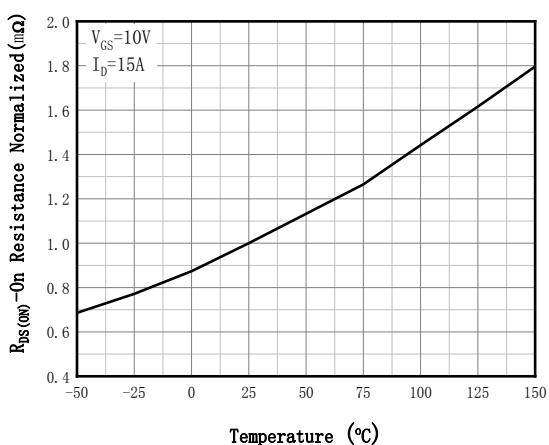
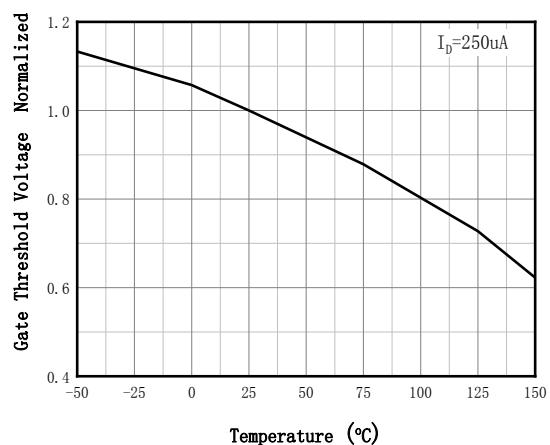
Single Operation					
Parameter		Symbol	Typical	Maximum	Unit
Junction-to-Ambient Thermal Resistance ^a	t ≤ 10 s	R _{θJA}	16	20	°C/W
	Steady State		41	50	
Junction-to-Case Thermal Resistance		R _{θJC}	1.5	2.0	

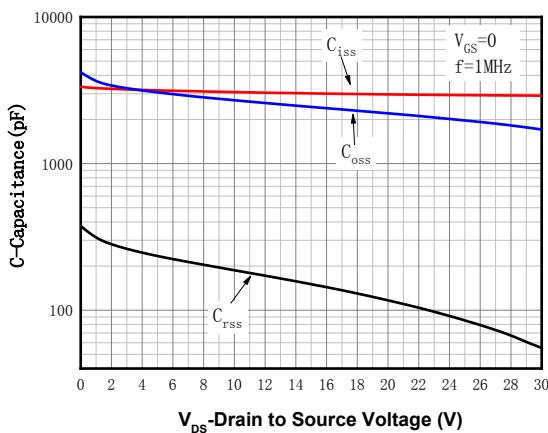
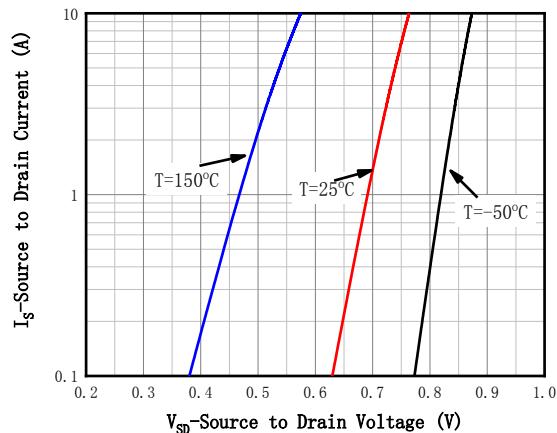
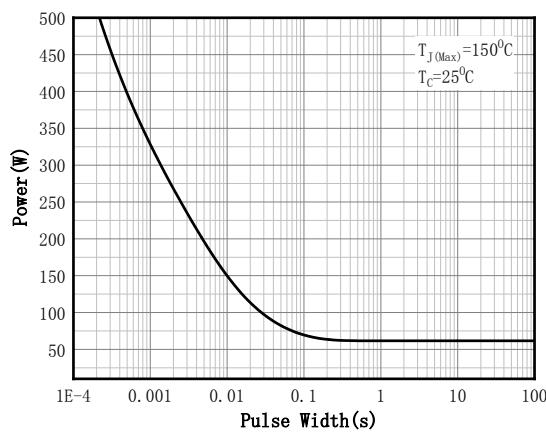
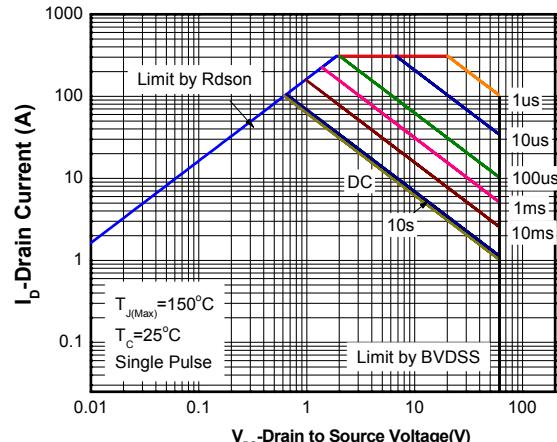
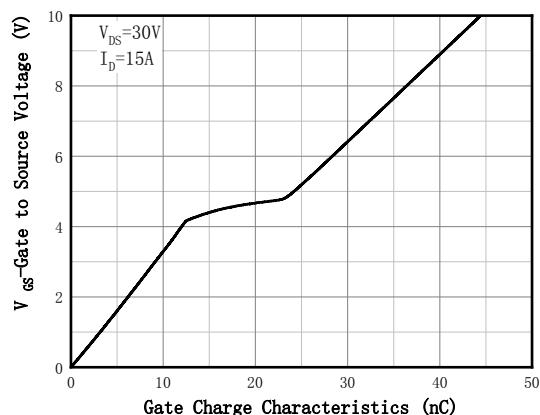
Note:

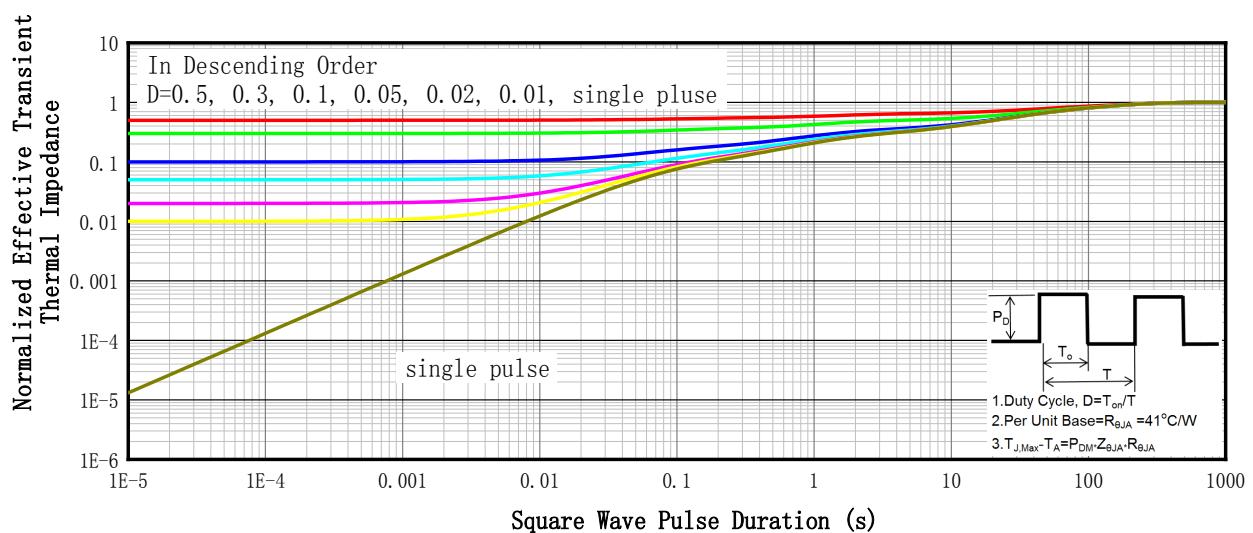
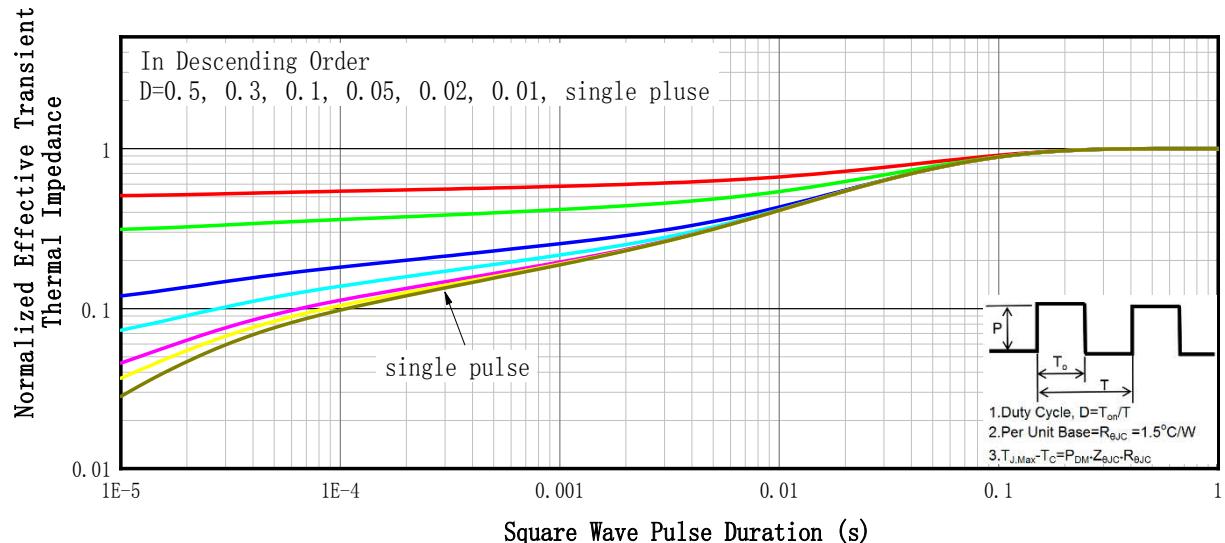
- a FR-4 board (38mm X 38mm X t1.6mm, 70um Copper) partially covered with copper (645mm² area).
- b The power dissipation P_D is based on T_{J(MAX)}=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- c Repetitive rating, ~10us pulse width, duty cycle ~1%, keep initial T_J =25°C, the maximum allowed junction temperature of 150°C.
- d The power dissipation P_{DSM} is based on Junction-to-Ambient thermal resistance R_{θJA} t≤10s value and the T_{J(MAX)}=150°C.
- e The static characteristics are obtained using ~380us pulses, duty cycle ~1%.

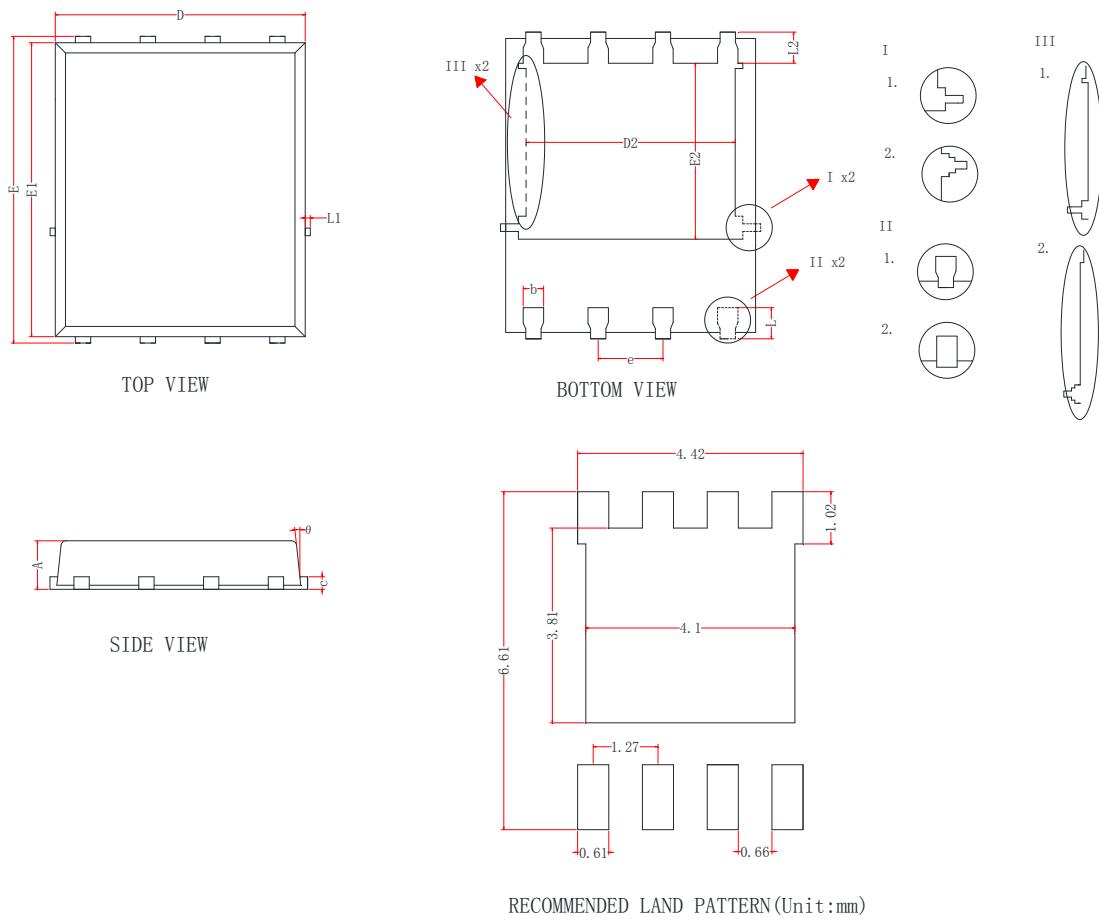
Electronics Characteristics (Ta=25°C, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	V_{DSS}	$V_{GS} = 0 \text{ V}, I_D = 250\mu\text{A}$	60			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 48\text{V}, V_{GS} = 0\text{V}$			1	μA
Gate-to-source Leakage Current	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20\text{V}$			± 100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS} = V_{DS}, I_D = 250\mu\text{A}$	2	3	4	V
Drain-to-source On-resistance	$R_{DS(on)}$	$V_{GS} = 10\text{V}, I_D = 15\text{A}$		2.5	3.4	$\text{m}\Omega$
Drain-to-source On-resistance	$R_{DS(on)}$	$V_{GS} = 6\text{V}, I_D = 10\text{A}$		4.2	6.0	$\text{m}\Omega$
CHARGES, CAPACITANCES AND GATE RESISTANCE						
Input Capacitance	C_{iss}	$V_{GS} = 0\text{V}, f = 1.0\text{MHz}, V_{DS} = 30 \text{ V}$		2910		pF
Output Capacitance	C_{oss}			1720		
Reverse Transfer Capacitance	C_{RSS}			55		
Total Gate Charge	$Q_{G(TOT)}$	$V_{GS} = 10 \text{ V}, V_{DS} = 30\text{V}, I_D = 15\text{A}$		42		nC
Threshold Gate Charge	$Q_{G(TH)}$			9.0		
Gate-to-Source Charge	Q_{GS}			14		
Gate-to-Drain Charge	Q_{GD}			7.5		
Gate Resistance	R_g	f=1MHz		1.2		Ω
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	$t_{d(ON)}$	$V_{GS} = 10 \text{ V}, V_{DS} = 30 \text{ V}, R_G=4.7\Omega, I_D=15\text{A}$		17		ns
Rise Time	t_r			37		
Turn-Off Delay Time	$t_{d(OFF)}$			33		
Fall Time	t_f			23		
BODY DIODE CHARACTERISTICS						
Forward Voltage	V_{SD}	$V_{GS} = 0 \text{ V}, I_S = 1\text{A}$		0.7	1.2	V

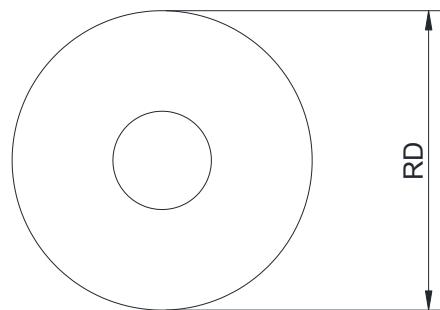
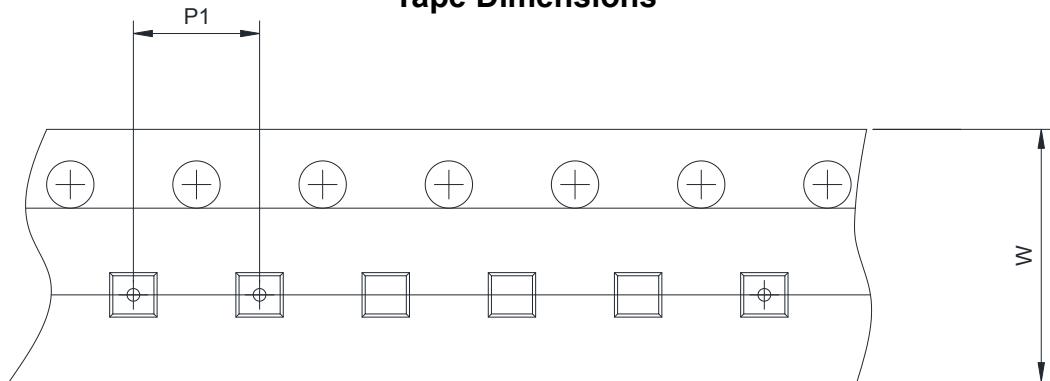
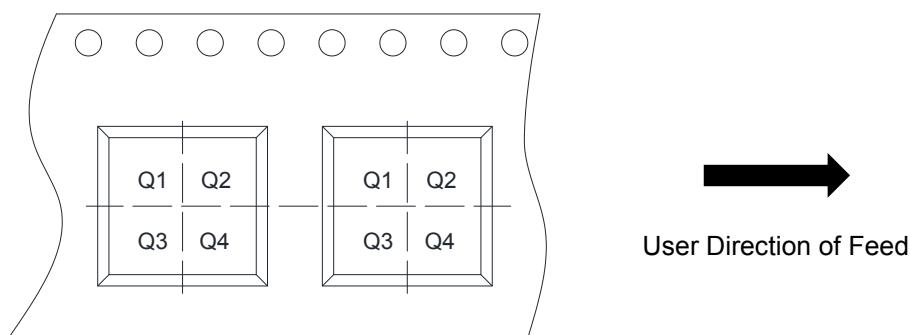
Typical Characteristics (Ta=25°C, unless otherwise noted)

Output Characteristics

Transfer Characteristics

On-Resistance vs. Drain Current

On-Resistance vs. Gate-to-Source Voltage

On-Resistance vs. Junction Temperature^e
Threshold Voltage vs. Temperature


Capacitance

Body Diode Forward Voltage ^e

Single Pulse power

Safe Operating Power

Gate Charge Characteristics


Transient Thermal Response (Junction-to-Ambient)

Transient Thermal Response (Junction-to-Case)

PACKAGE OUTLINE DIMENSIONS
PDFN5x6-8L


Symbol	Dimensions in Millimeters		
	Min.	Typ.	Max.
A	0.85	0.95	1.00
c	0.15	-	0.34
D	4.80	-	5.30
D2	3.82	-	4.45
E	5.90	-	6.15
E1	5.45	-	5.80
E2	3.18	3.45	3.73
e	1.27BSC		
b	0.30	0.40	0.50
L	0.45	-	0.71
L1	0.00	-	0.15
L2	0.68Ref		
θ	0°	-	12°

TAPE AND REEL INFORMATION
Reel Dimensions

Tape Dimensions

Quadrant Assignments For PIN1 Orientation In Tape


RD	Reel Dimension	<input type="checkbox"/> 7inch <input checked="" type="checkbox"/> 13inch
W	Overall width of the carrier tape	<input type="checkbox"/> 8mm <input checked="" type="checkbox"/> 12mm <input type="checkbox"/> 16mm
P1	Pitch between successive cavity centers	<input type="checkbox"/> 2mm <input type="checkbox"/> 4mm <input checked="" type="checkbox"/> 8mm
Pin1	Pin1 Quadrant	<input checked="" type="checkbox"/> Q1 <input type="checkbox"/> Q2 <input type="checkbox"/> Q3 <input type="checkbox"/> Q4