

### **GRF4142**

LNA/Driver w/Bypass Tuning Range: 0.1 to 3.8 GHz



#### **Features**

Reference: 3.3V/50mA/1.9 GHz

Gain: 15.3 dB

EVB NF: 0.90 dB

OP1dB: 19.3 dBm

OIP3: 33.0 dBm

Bypass Mode Gain: -1.9 dB

Bypass OP1dB: 25.4

Bypass OIP3: 43.7

Flexible Bias Voltage and Current

Single Control Logic Input

• Pre-matched to 50  $\Omega$ 

Process: GaAs pHEMT

#### **Applications**

Cellular Repeaters and Signal Boosters

Cellular Infrastructure

Revision Date: 09/11/18

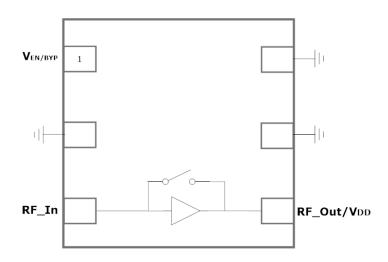
VHF/UHF and ISM Radios

#### **Product Description**

GRF4142 is a low noise amplifier (LNA) with low loss bypass which requires only a single control input. It is designed for high performance applications up to 3.8 GHz.

With minimal external matching, the device achieves outstanding noise figure (NF), high gain and high linearity. The LNA is operated from a single positive supply of 1.8 to 5.0 V with a selectable Iddq range of 15 to 80 mA.

Consult with the GRF applications engineering team for custom tuning/evaluation board data and device sparameters.



1.5 x 1.5 mm DFN-6



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### **Absolute Ratings:**

Parameter	Symbol	Min.	Max.	Unit
Supply Voltage	V <sub>DD</sub>	0	6.0	V
RF Input Power CW (Load VSWR < 2:1; V <sub>D</sub> : 5.0 volts)	P <sub>IN MAX</sub>		20	dBm
Operating Temperature (Package Heat Sink)	Т <sub>АМВ</sub>	-40	105	°C
Maximum Channel Temperature (MTTF > 10^6 Hours)	Тмах		170	°C
Maximum Dissipated Power	P <sub>DISS MAX</sub>		500	mW
Electrostatic Discharge:				
Charged Device Model:	CDM	1500		V
Human Body Model:	НВМ	250		V
Storage:				
Storage Temperature	T <sub>STG</sub>	-65	150	°C
Moisture Sensitivity Level	MSL		2	



Caution! ESD Sensitive Device



Exceeding Absolute Maximum Rating conditions may cause permanent damage to the device.

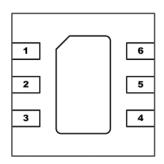
Note: For package dimensions and manufacturing information, see the Guerrilla-RF.com website for the following document located on the GRF4142 landing page (TBD): Manufacturing Note—MN-001 Product Tape and Reel, Solderability and Package Outline Specification.

Link to manufacturing note:



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#### Pin Out (Top View)



#### Pin Assignments:

Pin	Name	Description	Note		
1	VENABLE/BYPASS	Enable Voltage Input	Venable < =0.2 volts sets bypass Mode. Venable and external series resistor control the device Iddq when Venable is high.		
2	NC	No Connect or Ground	No internal connection to die		
3	RF_In	LNA RF input	Partially-matched 50 $\Omega$ . An external DC blocking cap must be used.		
4	RF_Out	LNA RF output	Internally matched 50 $\Omega$ . $V_{DD}$ must be applied through a choke to this pir		
5	NC	No Connect or Ground	No internal connection to die		
6	NC	No Connect or Ground	No internal connection to die		
PKG BASE	GND	Ground	Provides DC and RF ground for LNA, as well as thermal heat sink. Recommend multiple 8 mil vias beneath the package for optimal RF and thermal performance. Refer to evaluation board top layer graphic on schematic page.		

### **Control Logic Truth Table:**

Mode	Description	Vdd	Venable/bypass
High Gain	High LNA Gain	1	1
Bypass	Linear Bypass Mode	1	0
Logic Level "0"	Logic Low	0.0V to 0.2V	0.0V to 0.2V
Logic Level "1"	Logic High	1.8V to 5.0V	1.5V to Vdd



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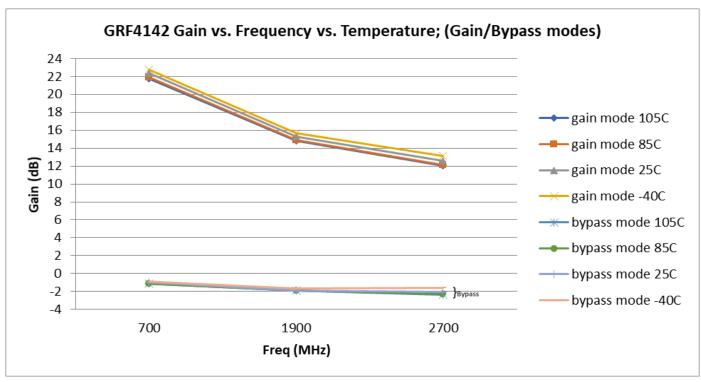
#### **Nominal Operating Parameters: (Standard Match)**

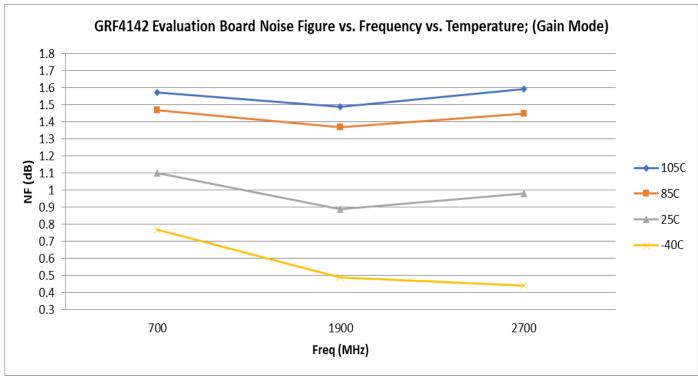
Parameter	Symbol	Specification		Unit	Condition		
raiailletei	Зуппоот	Min.	Тур.	Max.	Ullit	Condition	
High Gain Mode						V <sub>DD</sub> = 3.3 V; V <sub>ENABLE</sub> : High	
Test Frequency	F <sub>TEST</sub>		1.9		GHz		
Gain	S21	14.3	15.3		dB		
Noise Figure (Evaluation Board)	NF		0.90	1.1	dB		
Output 1dB Compression Point	OP1dB	17.7	19.2		dBm		
Output Third Order Intercept Point	OIP3		33.0		dBm		
Switching Rise Time	T <sub>RISE</sub>		800		ns	Bypass to gain mode	
Switching Fall Time	$T_{FALL}$		200		ns	Gain to bypass mode	
Supply Current	I <sub>DD</sub>		55		mA		
Enable Current	I <sub>ENABLE</sub>		2.0		mA		
Bypass Mode						VDD: 3.3 V; Ven: 0.0 V;	
Gain	S(2,1)	-3.0	-2.0		dB		
Output 1dB Compression Point	OP1dB		25.4		dBm		
Output Third Order Intercept Point	OIP3		43.7		dBm		
Leakage Current	ILEAKAGE		475		uA	Vdd: 3.3V; Venable: 0.0V	
Thermal Data							
Thermal Resistance (Infra-Red Scan)	Θјс		132		°C/W		
Channel Temperature @ +85 C reference (Package heat sink)	T <sub>CHANNEL</sub>		109		°C	V <sub>DD</sub> : 3.3 V; I <sub>DDQ</sub> : 55 mA; No RF; Dissi- pated Power:182 mW	



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#### **GRF4142** Evaluation Board Data: (3.3V/55mA)

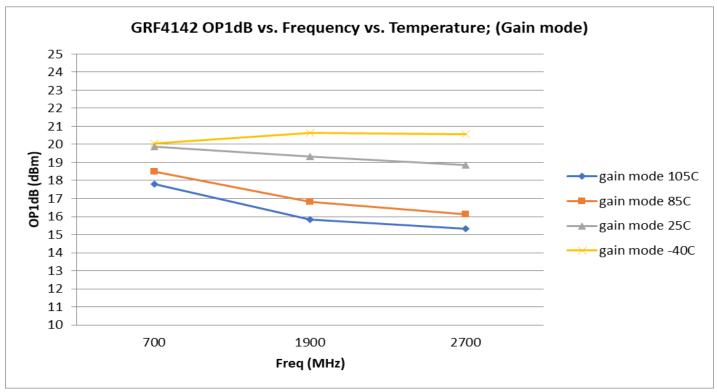


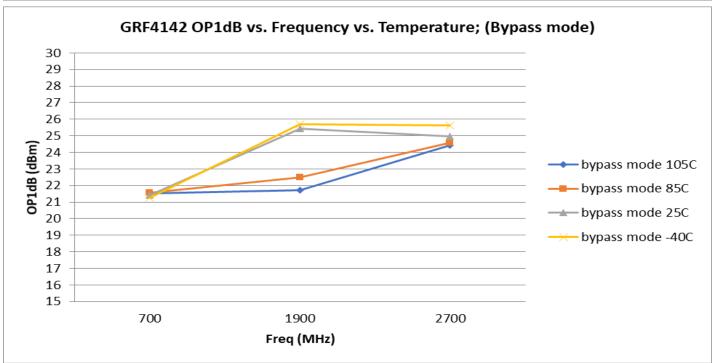




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#### **GRF4142** Evaluation Board Data: (3.3V/55mA)





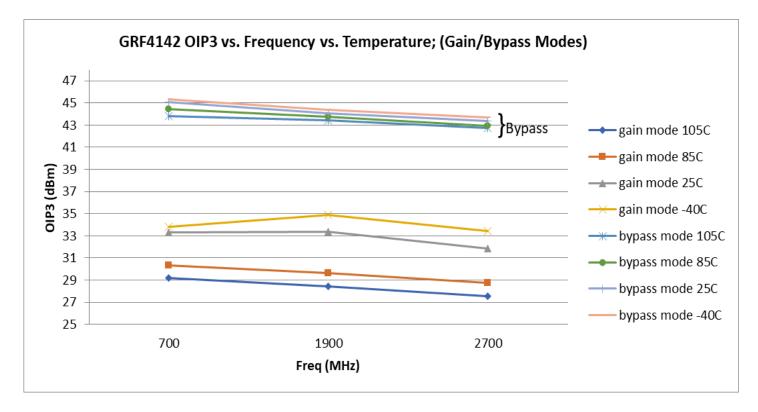


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## **GRF4142**

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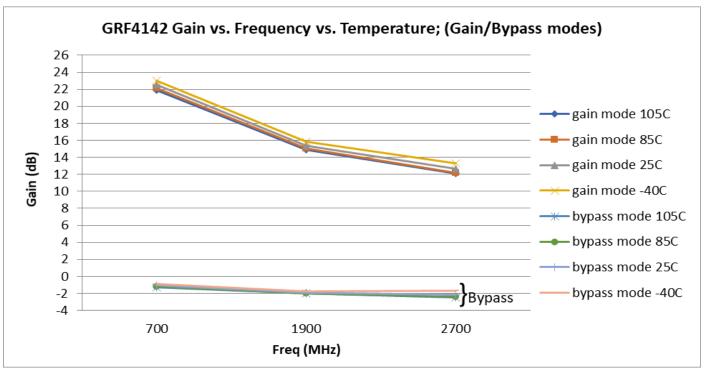
### **GRF4142** Evaluation Board Data: (3.3V/55mA)

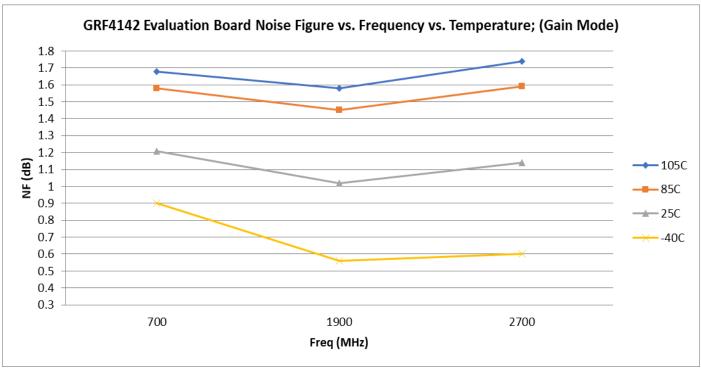




LNA/Driver w/Bypass Tuning Range: 0.1 to 3.8 GHz

#### **GRF4142 Evaluation Board Data:** (5.0V/70mA)



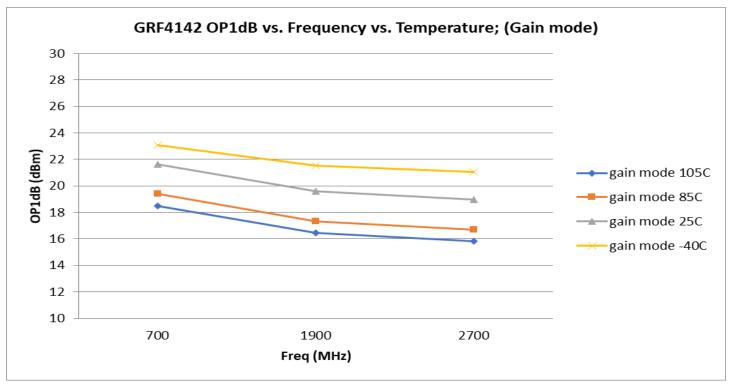


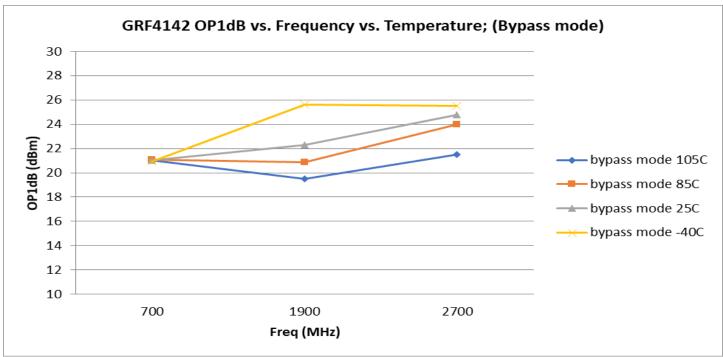
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#### **GRF4142 Evaluation Board Data:** (5.0V/70mA)



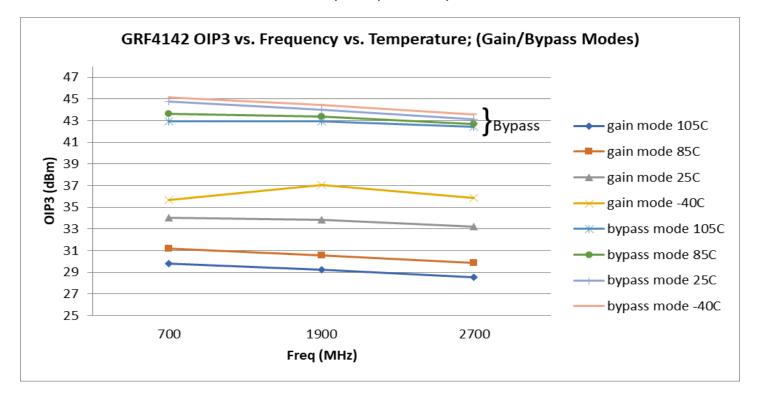


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#### **GRF4142 Evaluation Board Data:** (5.0V/70mA)

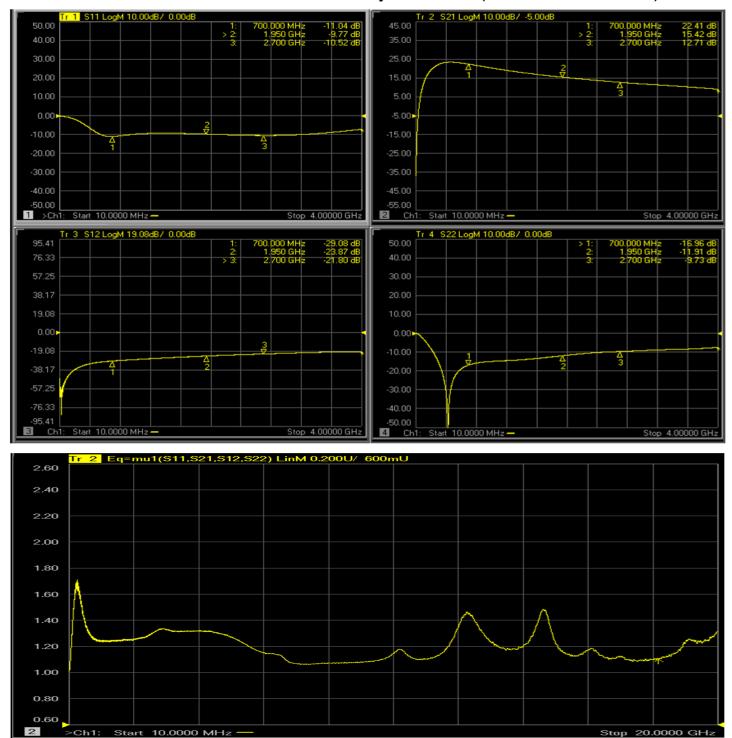




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#### **GRF4142** Evaluation Board S-Pars and Stability Mu Factor: (0.7 to 2.7 GHz Match)



Note: Mu factor >= 1.0 implies unconditional stability.

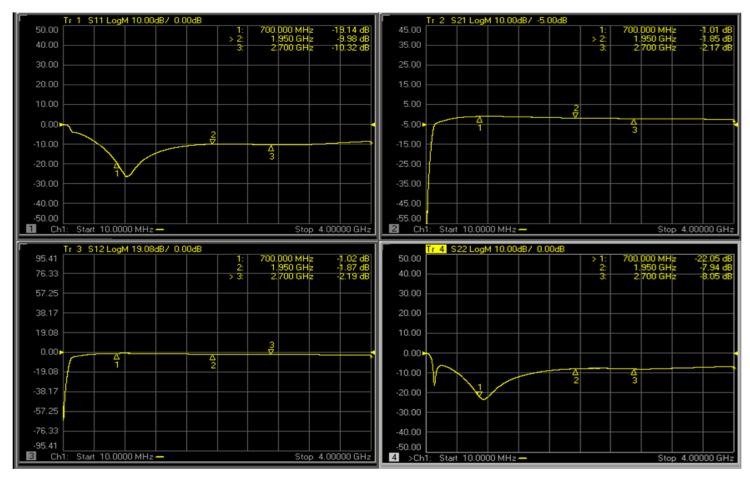


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### **GRF4142**

LNA/Driver w/Bypass Tuning Range: 0.1 to 3.8 GHz

#### **GRF4142** Evaluation Board S-Pars and Stability Mu Factor: (Bypass Mode; 0.7 to 2.7 GHz)

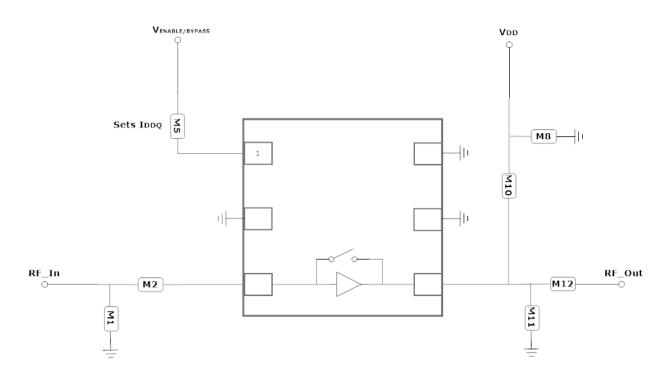




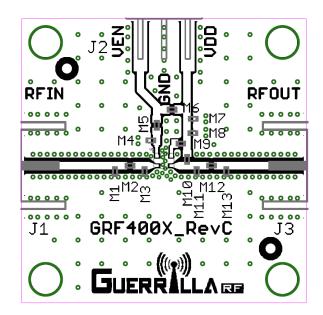
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**GRF4142 Application Schematic** 



**GRF4142 Evaluation Board Assembly Diagram** 



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#### GRF4142 Standard Evaluation Board BOM: (Standard 0.7 to 2.7 GHz Tune)

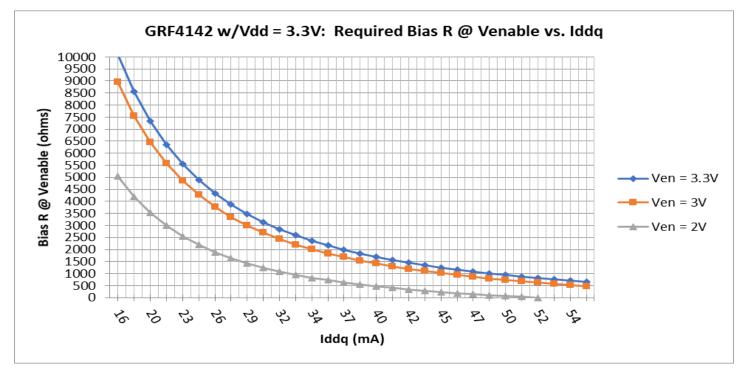
Component	Туре	Manufacturer	Family	Value	Package Size	Substitution
M1	Inductor	Murata	LQG	22 nH	0402	ok
M2	Capacitor	Murata	GJM	30 pF	0402	ok
M5 (See Curves)	Resistor	Various	5%	Sets Iddq	0402	ok
M8	Capacitor	Murata	GRM	0.1 uF	0402	ok
M10	Inductor	Murata	LQG	39 nH	0402	ok
M11	Capacitor	Murata	GJM	0.5 pF	0402	ok
M12	Capacitor	Murata	GRM	100 pF	0402	ok
Evaluation Board	GRF400X_RevC					

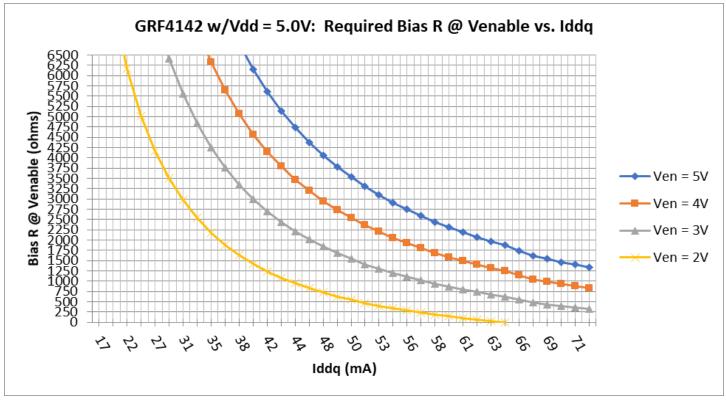


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#### **GRF4142 Bias Resistor Selection Charts**







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Data Sheet Release Status:	Notes
Advance	S-parameter and NF data based on EM simulations for the fully packaged device using foundry supplied transistor s-parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on evaluation board measurements in the Guerrilla RF Applications Lab.
Released	All data based on device qualification data. Typically, this data is nearly identical to the data found in the preliminary version. Max and min values for key RF parameters are included.

Information in this datasheet is specific to the Guerrilla RF, Inc. ("Guerrilla RF") product identified.

Revision Date: 09/11/18

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