

## DESCRIPTION

The PSR05 is a low capacitance steering diode TVS array, designed to protect two I/O lines from the effects of Electrostatic Discharge (ESD) and Electrical Fast Transients (EFT). The PSR05 exceeds Level 4 IEC 61000-4-2, with a peak pulse power rating of 500 Watts for an 8/20µs waveshape.

The low capacitance of the steering diode allows the designer to protect high speed data applications. The small SOT-143 package, with four leads reduces the internal lead inductance for low overshoot voltage during fast front time transient events, such as ESD and EFT. The PSR05 meet the IEC 61000-4-2, IEC 61000-4-4 and IEC 61000-4-5 requirements.

#### FEATURES

- Compatible with IEC 61000-4-2 (ESD): Air 15kV, Contact 8kV
- Compatible with IEC 61000-4-4 (EFT): 40A 5/50ns
- Compatible with IEC 61000-4-5 (Surge): 24A, 8/20µs Level 2(Line-Gnd) & Level 3 (Line-Line)
- 500 Watts Peak Pulse Power per Line (tp = 8/20µs)
- Protects Two I/O Ports & Power Supply
- Low Capacitance: 10pF
- RoHS Compliant
- REACH Compliant

## **MECHANICAL CHARACTERISTICS**

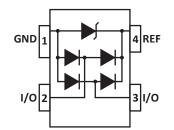
- Molded JEDEC SOT-143 Package
- Approximate Weight: 9 milligrams
- Lead-Free Pure-Tin Plating (Annealed)
- Solder Reflow Temperature:
- Pure-Tin Sn, 100: 260-270°C
- 8mm Tape and Reel Per EIA Standard 481
- Flammability Rating UL 94V-0

## Ethernet - 10/100/1000 Base T USB

APPLICATIONS

- USB
- Wireless Communications
- FireWire

## **PIN CONFIGURATION**



## TYPICAL DEVICE CHARACTERISTICS

MAXIMUM RATINGS @ 25°C Unless Otherwise Specified							
PARAMETER	SYMBOL	VALUE	UNITS				
Operating Temperature	TL	-55 to 150	°C				
Storage Temperature	T <sub>stg</sub>	-55 to 150	°C				
Peak Pulse Power (tp = $8/20\mu$ s) - See Figure 1	P <sub>PP</sub>	500	Watts				
Peak Forward Voltage - I <sub>F</sub> = 1A, $8/20\mu s$	V <sub>F</sub>	1.5	Volts				

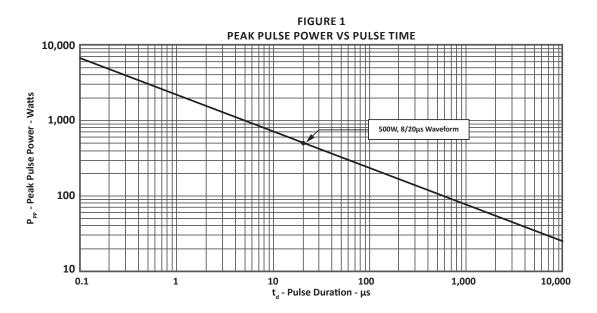
ELECTRICAL CHARACTERISTICS PER LINE @ 25°C Unless Otherwise Specified									
PART NUMBER	DEVICE MARKING	RATED STAND-OFF VOLTAGE V VWM VOLTS	MINIMUM BREAKDOWN VOLTAGE @ 1mA V <sub>(BR)</sub> VOLTS	MAXIMUM CLAMPING VOLTAGE (Fig. 2) @ I <sub>p</sub> = 1A V <sub>c</sub> VOLTS	MAXIMUM CLAMPING VOLTAGE (Fig. 2) @ 8/20µs V <sub>c</sub> @ I <sub>pp</sub> VOLTS	MAXIMUM LEAKAGE CURRENT @ V <sub>wm</sub> Ι <sub>D</sub> μΑ	MAXIMUM CAPACITANCE PER LINE (Note 1) (Fig. 5) OV, 1MHz C <sub>J(SD)</sub> pF		
PSR05	5A	5.0	6.0	9.8	20.0V @ 28.0A	5	10		
NOTES									

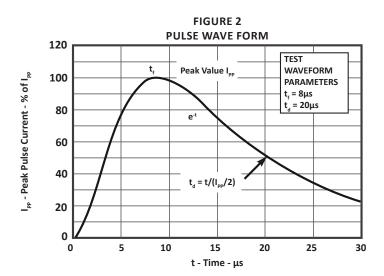
NOTES

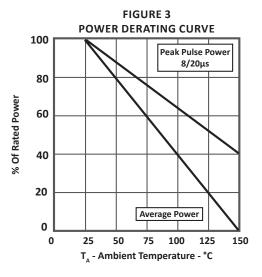
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1. As shown in Figure 5, REF 1 is connected to ground, REF 2 is connected to + $V_{cc}$  and input applies to  $V_{cc}$  = 5V,  $V_{sigN}$  = 30mV, F = 1MHz.

## **TYPICAL DEVICE CHARACTERISTICS**







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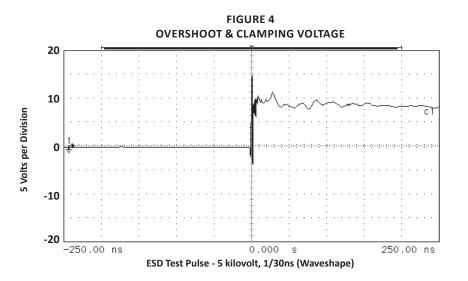


FIGURE 5 INPUT CAPACITANCE CIRCUIT

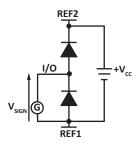
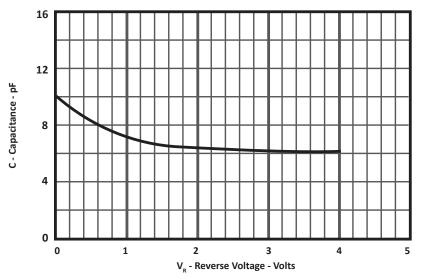


FIGURE 6 TYPICAL REVERSE VOLTAGE VS CAPACITANCE



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#### SPICE MODEL

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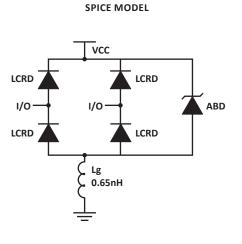


FIGURE 1

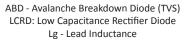
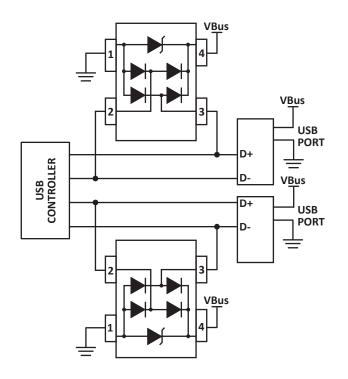


TABLE 1 - SPICE PARAMETERS							
PARAMETER	UNIT	ABD(TVS)	LCRD				
BV	V	6.0	200				
IBV	μΑ	1	0.01				
C <sub>jo</sub>	pF	230	6				
۱ <sub>s</sub>	A	1E-11	1E-11				
Vj	V	0.6	0.6				
М	-	0.33	0.33				
N	-	1	1				
R <sub>s</sub>	Ohms	0.014	0.75				
TT	S	1E-8	1E-9				
EG	eV	1.11	1.11				

#### 

#### APPLICATION INFORMATION



#### **FIGURE 1 - USB PROTECTION**

Two PSR05s (Unidirectional) in a Common-Mode configuration. Circuit connectivity is as follows:

- Pins 2 and 3 are connected to the datalines
- Pin 1 is connected to ground
- Pin 4 is connected to the databus

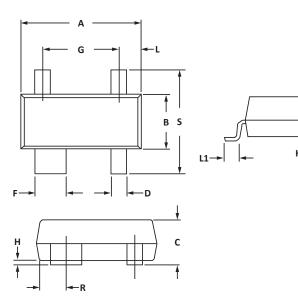
#### **CIRCUIT BOARD RECOMMENDATIONS**

Circuit board layout is critical for electromagnetic compatibility protection. The following guidelines are recommended:

- The protection device should be placed near the input terminals or connectors, the device will divert the transient current immediately before it can be coupled into the nearby traces.
- The path length between the TVS device and the protected line should be minimized.
- All conductive loops including power and ground loops should be minimized.
- The transient current return path to ground should be kept as short as possible to reduce parasitic inductance.
- Ground planes should be used whenever possible. For multilayer PCBs, use ground vias.

## SOT-143 PACKAGE INFORMATION

OUTLINE DIMENSIONS							
DIM	MILLIN	1ETERS	INCHES				
DIIVI	MIN	MAX	MIN	MAX			
А	2.80	3.04	0.110	0.120			
В	1.20	1.39	0.047	0.055			
С	0.84	1.14	0.033	0.045			
D	0.39	0.50	0.015	0.020			
F	0.79	0.93	0.031	0.037			
G	1.78	2.03	0.070	0.080			
J	0.08	0.15	0.003	0.006			
к	0.46	0.60	0.018	0.024			
L	0.445	0.60	0.0175	0.024			
L1	0.40	0.60	0.016	0.024			
R	0.72	0.83	0.028	0.033			
S	2.11	2.48	0.083	0.098			
NOTES							



NOTES

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1. Dimensioning and tolerances per ANSI Y14.M, 1985.

2. Controlling dimension: inches.

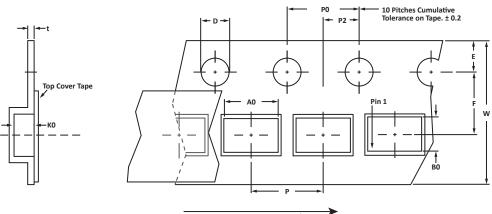
3. Dimensions are exclusive of mold flash and metal burrs.

PAD LAYOUT DIMENSIONS								
DIM	MILLIN	IETERS	INCHES					
	MIN	MAX	MIN	MAX				
А	1.88	2.13	0.074	0.084				
В	1.80	2.06	0.071	0.081				
С	0.71	0.97	0.028	0.038				
D	0.76	1.02	0.030	0.040				
E	1.07	1.32	0.042	0.052				
F	0.71	0.97	0.028	0.038				
	NOTES 1. Controlling dimension: inches.							

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## TAPE AND REEL

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User Direction of Feed

SPECIFICATIONS												
REEL DIA.	TAPE WIDTH	A0	В0	ко	D	E	F	W	PO	P2	Р	tmax
178mm (7")	8mm	3.10 ± 0.10	2.70 ± 0.10	1.35 ± 0.10	$1.50 \pm 0.10$	1.75 ± 0.10	3.50 ± 0.05	8.00 ± 0.30	4.00 ± 0.10	2.00 ± 0.05	$4.00 \pm 0.10$	0.25
NOTES 1. Dimensions are in millimeters.												

2. Surface mount product is taped and reeled in accordance with EIA-481.

3. Suffix - T7 = 7" Reel - 3,000 pieces per 8mm tape.

4. Suffix - T13 = 13" Reel - 10,000 pieces per 8mm tape.

5. Marking on Part - marking code (see page 2) and date code.

Package outline, pad layout and tape specifications per document number 06011.R4 8/10.

ORDERING INFORMATION								
BASE PART NUMBER LEADFREE SUFFIX TAPE SUFFIX QTY/REEL REEL SIZE TUBE QT								
PSR05	-LF	-T7	3000	7"	n/a			
PSR05	-LF	-T13	10,000	13"	n/a			

This device is only available in a Lead-Free configuration.

#### COMPANY INFORMATION

#### **COMPANY PROFILE**

In business more than 25 years, ProTek Devices<sup>™</sup> is a privately held semiconductor company. The company offers a product line of overvoltage protection and overcurrent protection components. These include transient voltage suppressor array (TVS arrays) avalanche breakdown diode, steering diode TVS array and electronics SMD chip fuses. These components deliver circuit protection in electronic systems from numerous overvoltage and overcurrent events. They include lightning; electrostatic discharge (ESD); nuclear electromagnetic pulses (NEMP); inductive switching; and electromagnetic interference (EMI) / radio frequency interference (RFI). ProTek Devices also offers LED wafer die for ESD protection and related high frequency products. ProTek Devices is ISO 9001:2015 certified.

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