

### Discription

The HCEST23NC24VU protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. Excellent clamping capability, low leakage, low capacitance, and fast response time provide best in class protection on designs that are exposed to ESD. It gives designer the flexibility to protect 2 unidirectional line in applications where arrays are not practical.

#### **Features**

- ★ We declare that the material of product compliance with RoHS requirements and Halogen Free.
- ★ 2 Unidirectional transil functions
- ★ Low leakage current:IR max< 20 µA at VRM
- ★ 300W peak pulse power(8/20µs)
- $\star$  Transient protection for data lines as per
- ★ IEC61000-4-2(ESD) 15KV(air) 8KV(contact)
- ★ IEC61000-4-5(Lightning) see IPPM below

### **Orderingin formation**

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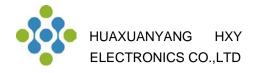
SOT-23

Circuit Diagram

Product ID	Pack	Qty(PCS)
HCEST23NC24VU	SOT-23	3000

### Absolute Ratings(Tamb = 25°C)

Symbol	Parameter	Value	Units
P <sub>PP</sub>	Peak Pulse Power (t <sub>p</sub> = 8/20µs)	100	W
TL	Maximum lead temperature for soldering during 10s	260	°C
T <sub>stg</sub>	Storage Temperature Range	-55 to +150	°C
T <sub>op</sub>	Operating Temperature Range	-40 to +125	°C
Tj	Maximum junction temperature	150	°C
	IEC61000-4-2 (ESD) air discharge contact discharge		KV



Device	V <sub>RWM</sub> (V)	I <sub>R</sub> (uA) @V <sub>RWM</sub>	V <sub>BR</sub> (V)@ I <sub>T</sub> (Note 1)	IT	V <sub>C</sub> (V) @ Max I <sub>PP</sub> *	І <sub>РР</sub> (А)*	Р <sub>РК</sub> (W)*	C (pF)
	Max	Мах	Min	mA	Max	Мах	Max	Тур
HCEST23NC24VU	24	1.0	26	1	50	2	100	25

Electrical Characteristics Ratings at 25°C ambient temperature unless otherwise specified.

\*Surge current waveform per Figure 1.

1.  $V_{BR}$  is measured with a pluse test current  $I_T$  at an ambient temperature of  $25^{\circ}$ C.

## **Typical Characteristics**

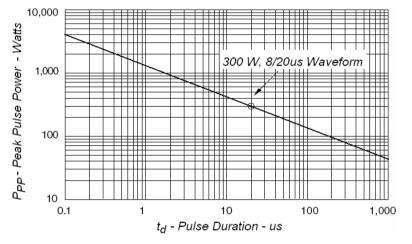


Fig1. Peak Pulse Power VS Pulse Time

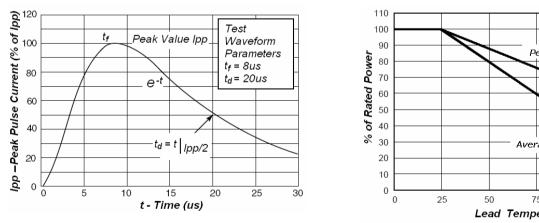
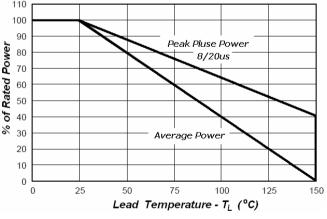
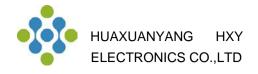


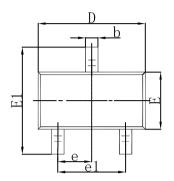
Fig2. Pulse Waveform

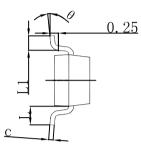


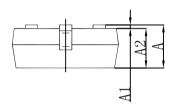
**Fig3.Power Derating** 



# SOT-23 Package Outline Dimensions

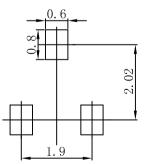






Symbol	Dimensions	In Millimeters	Dimensions In Inches		
	Min	Max	Min	Max	
Α	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
E	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950 TYP		0.037	7 TYP	
e1	1.800	2.000	0.071	0.079	
L	0.550 REF		0.022	2 REF	
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	

### SOT-23 Suggested Pad Layout



Note: 1.Controlling dimension: in millimeters.

2.General tolerance:± 0.05mm.
 3.The pad layout is for reference purposes only.



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