# N-Channel 60 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	60				
$R_{DS(on)} (\Omega)$ at $V_{GS} = 10 V$	0.0050				
$R_{DS(on)} (\Omega)$ at $V_{GS} = 4.5 V$	0.0120				
I <sub>D</sub> (A)	97				
Configuration	Single				

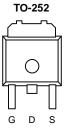
#### FEATURES

- TrenchFET<sup>®</sup> Power MOSFET
- Package with Low Thermal Resistance
- 100 % R<sub>g</sub> and UIS Tested

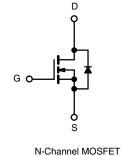


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Top View



PARAMETER		SYMBOL	LIMIT	UNI
Drain-Source Voltage		V <sub>DS</sub>	60	V
ate-Source Voltage		V <sub>GS</sub>	± 20	V
Continuous Drain Current	T <sub>C</sub> = 25 °C	I <sub>D</sub>	97	
	T <sub>C</sub> = 125 °C		56	
Continuous Source Current (Diode Conduction) <sup>a</sup>		Is	100	А
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	290	
Single Pulse Avalanche Current		I <sub>AS</sub>	45	1
Single Pulse Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	101	mJ
Maximum Power Dissipation <sup>b</sup>	T <sub>C</sub> = 25 °C	P	136	- w
	T <sub>C</sub> = 125 °C	P <sub>D</sub>	45	

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	LIMIT	UNIT		
Junction-to-Ambient	PCB Mount <sup>c</sup>	R <sub>thJA</sub>	50	°C/W		
nction-to-Case (Drain)		R <sub>thJC</sub>	1.1	0/10		

T<sub>J</sub>, T<sub>stg</sub>

- 55 to + 175

#### Notes

a. Package limited.

服务热线:400-655-8788

b. Pulse test; pulse width  $\leq 300~\mu s,~duty~cycle \leq 2~\%.$ 

Operating Junction and Storage Temperature Range

- c. When mounted on 1" square PCB (FR-4 material).
- d. Parametric verification ongoing.

°C

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static	•							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$		60	-	-	v	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$			4.0		
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$		-	-	± 100	nA	
		$V_{GS} = 0 V$	V <sub>DS</sub> = 60 V	-	-	1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{GS} = 0 V$	V <sub>DS</sub> = 60 V, T <sub>J</sub> = 125 °C	-	-	50	μA	
		$V_{GS} = 0 V$	V <sub>DS</sub> = 60 V, T <sub>J</sub> = 175 °C	-	-	150		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = 10 V	$V_{DS} \ge 5 V$	50	-	-	Α	
		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 25 A	-	0.0050	-	Ω	
Duain Course On Otata Desistance?	P	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 25 A, T <sub>J</sub> = 125 °C	-	0.0117	-		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 10 V$	I <sub>D</sub> = 25 A, T <sub>J</sub> = 175 °C	-	0.0149	-		
		$V_{GS} = 4.5 V$	I <sub>D</sub> = 20 A	-	0.0120	-		
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 25 A		-	177	-	S	
Dynamic <sup>b</sup>		·						
Input Capacitance	C <sub>iss</sub>			-	4844	6060		
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$	V <sub>GS</sub> = 0 V V <sub>DS</sub> = 25 V, f = 1 MHz		441	555	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	1		-	200	250	1 1	
Total Gate Charge <sup>c</sup>	Qg			-	82	125		
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	V <sub>GS</sub> = 10 V	$V_{DS} = 30 \text{ V}, I_D = 50 \text{ A}$	-	14.5	-	nC	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>	1		-	13.5	-		
Gate Resistance	R <sub>g</sub>	f = 1 MHz		1	2	3	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>	$V_{DD} = 30 \text{ V}, \text{ R}_{\text{L}} = 0.6 \Omega$ $\text{I}_{\text{D}} \cong 50 \text{ A}, \text{ V}_{\text{GEN}} = 10 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$		-	14	21	ns	
Rise Time <sup>c</sup>	t <sub>r</sub>			-	5	8		
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			-	41	62		
Fall Time <sup>c</sup>	t <sub>f</sub>			-	7	11		
Source-Drain Diode Ratings and Chara	acteristics <sup>b</sup>							
Pulsed Current <sup>a</sup>	I <sub>SM</sub>			-	-	290	А	
Forward Voltage	V <sub>SD</sub>	I <sub>F</sub> = 50 A, V <sub>GS</sub> = 0 V			0.9	1.5	V	

Notes

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

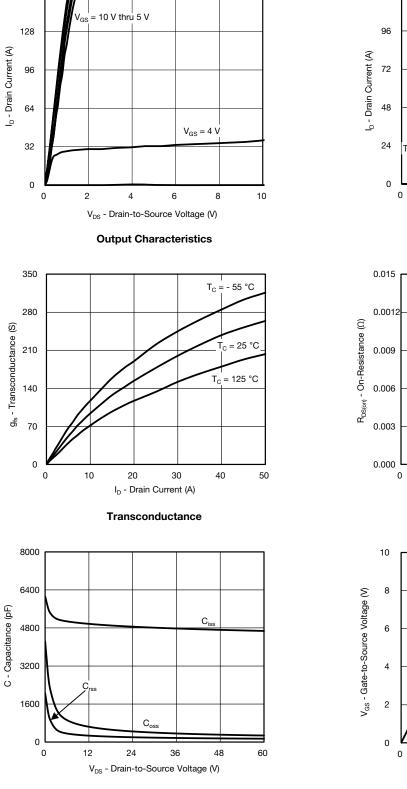
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

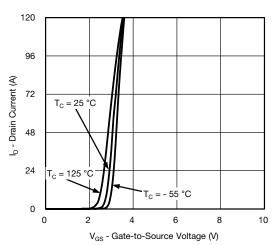
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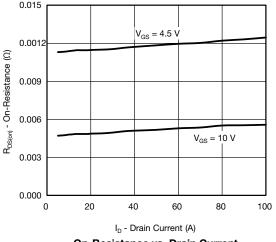
## **TYPICAL CHARACTERISTICS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)



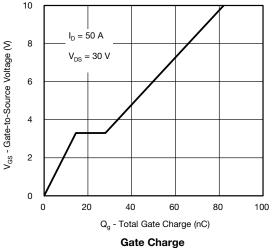
Capacitance



**Transfer Characteristics** 

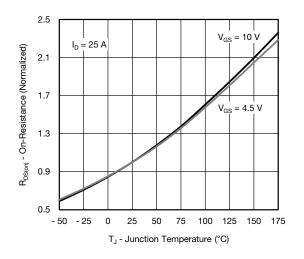


**On-Resistance vs. Drain Current** 

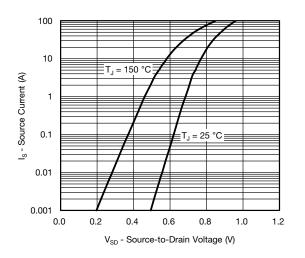




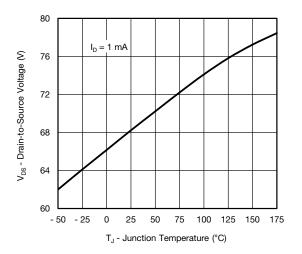
## **TYPICAL CHARACTERISTICS** ( $T_A = 25$ °C, unless otherwise noted)



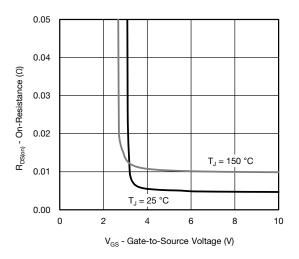
**On-Resistance vs. Junction Temperature** 



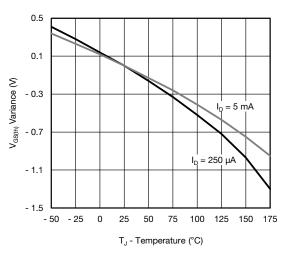
Source Drain Diode Forward Voltage



Drain Source Breakdown vs. Junction Temperature



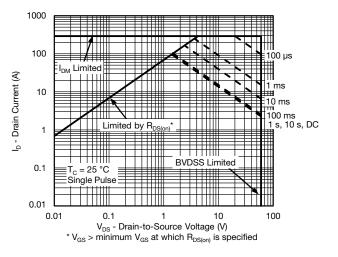
**On-Resistance vs. Gate-to-Source Voltage** 



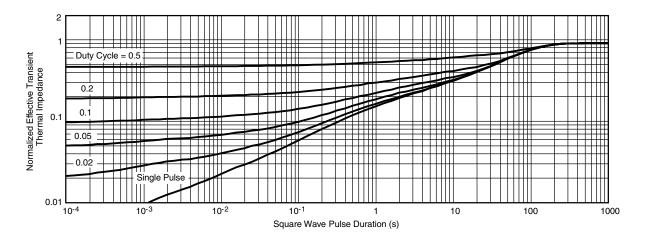
**Threshold Voltage** 



## **THERMAL RATINGS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



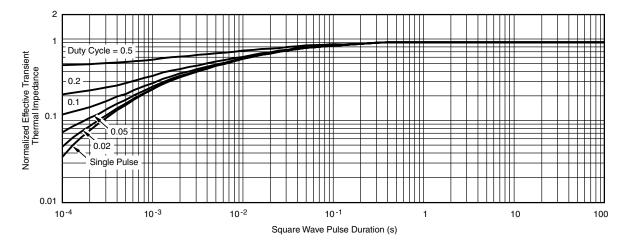
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



## **THERMAL RATINGS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



#### Normalized Thermal Transient Impedance, Junction-to-Case

#### Note

The characteristics shown in the two graphs

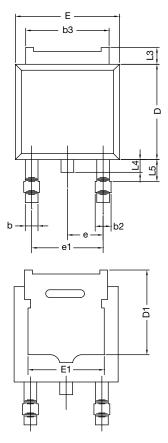
- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

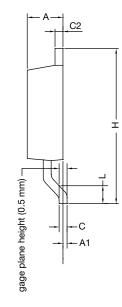
- Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



# **TO-252AA CASE OUTLINE**





	MILLIN	METERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
А	2.18	2.38	0.086	0.094	
A1	-	0.127	-	0.005	
b	0.64	0.88	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	
С	0.46	0.61	0.018	0.024	
C2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	
D1	5.21	-	0.205	-	
E	6.35	6.73	0.250	0.265	
E1	4.32	-	0.170	-	
Н	9.40	10.41	0.370	0.410	
е	2.28	BSC	0.090 BSC		
e1	4.56	BSC	0.180 BSC		
L	1.40	1.78	0.055	0.070	
L3	0.89	1.27	0.035	0.050	
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	
ECN: X12-0247-Rev. M, 24-Dec-12 DWG: 5347					

Note

• Dimension L3 is for reference only.



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