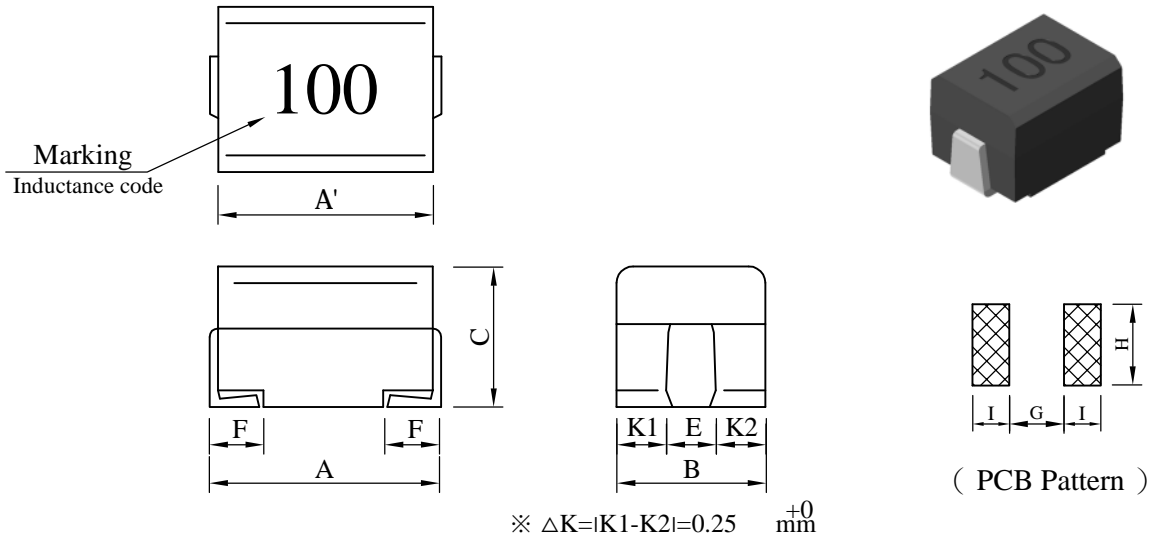


SPECIFICATION FOR APPROVAL

REF.::

PROD. NAME	Wound Chip Inductor	ABC'S DWG NO.	CM3225□□□□S□-□□□		
		REV.	20240913-D	PAGE	1

I . Configuration and dimensions :



Unit : mm

A	A'	B	C	E	F	G	H	I
3.20 ±0.4	2.90 ±0.2	2.50 ±0.2	2.20 ±0.2	1.00 ±0.2	0.60 $\overset{+0.3}{-0.0}$	1.80	1.40	1.00

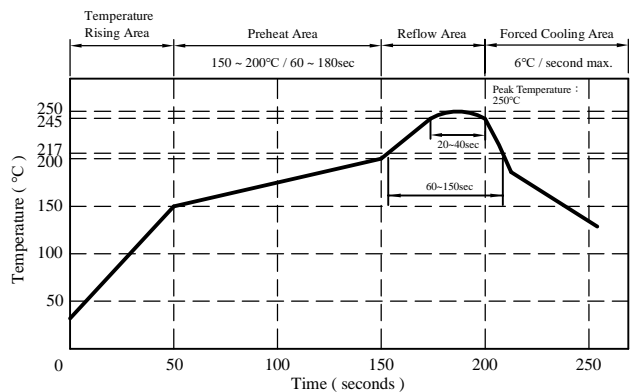
II . Description :

- a . Ferrite drum core construction.
- b . Enamelled copper wire : H class
- c . Product weight : 0.05 g (ref.)
- d . Moisture sensitivity Level 1
- e . Products comply with RoHS' requirements

III . General Specification :

- a . Temp. rise : 20°C max.
- b . Ambient temp. : 100°C max.
- c . Operating temp. : -40°C----+125°C
(Temp. rise included)
- d . Terminal pull strength : 1.5 kg min.
- e . Rated current : Current cause
inductance drop within 10%
- f . Resistance to solder heat : 260°C.10 secs.
- g . Resistance to solvent : Per MIL-STD-202F

Reflow profile
 Peak Temp : 250°C max.
 Max time above 245°C : 20~40sec max.
 Max time above 217°C : 60~150sec max.
 200°C~250°C Average Ramp-up Rate : 3°C/second max.



AR-001C

SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Wound Chip Inductor	ABC'S DWG NO.	CM3225□□□□S□-□□□		
		REV.	20240913-D	PAGE	2

IV . Electrical characteristics :

DWG. No.	Inductance (μH)	Tolerance	Q min.	Test Freq (MHz)	SRF (MHz) typ.	RDC (Ω) max.	IDC (mA) max.
CM3225R12□S□ -□□□	0.12	K , M	30	25.200	875	0.22	450
CM3225R15□S□ -□□□	0.15	K , M	30	25.200	860	0.25	450
CM3225R18□S□ -□□□	0.18	K , M	30	25.200	685	0.28	450
CM3225R22□S□ -□□□	0.22	K , M	30	25.200	560	0.32	450
CM3225R27□S□ -□□□	0.27	K , M	30	25.200	525	0.36	450
CM3225R33□S□ -□□□	0.33	K , M	30	25.200	520	0.40	450
CM3225R39□S□ -□□□	0.39	K , M	30	25.200	470	0.45	450
CM3225R47□S□ -□□□	0.47	K , M	30	25.200	430	0.50	450
CM3225R56□S□ -□□□	0.56	K , M	30	25.200	395	0.55	450
CM3225R68□S□ -□□□	0.68	K , M	30	25.200	370	0.60	450
CM3225R82□S□ -□□□	0.82	K , M	30	25.200	310	0.65	450
CM32251R0□S□ -□□□	1.00	J , K , M	30	7.960	295	0.70	400
CM32251R2□S□ -□□□	1.20	J , K , M	30	7.960	255	0.75	390
CM32251R5□S□ -□□□	1.50	J , K , M	30	7.960	160	0.85	370
CM32251R8□S□ -□□□	1.80	J , K , M	30	7.960	125	0.90	350
CM32252R2□S□ -□□□	2.20	J , K , M	30	7.960	100	1.00	320
CM32252R7□S□ -□□□	2.70	J , K , M	30	7.960	65	1.10	290
CM32253R3□S□ -□□□	3.30	J , K , M	30	7.960	55	1.20	260
CM32253R9□S□ -□□□	3.90	J , K , M	30	7.960	50	1.30	250
CM32254R7□S□ -□□□	4.70	J , K , M	30	7.960	45	1.50	220
CM32255R6□S□ -□□□	5.60	J , K , M	30	7.960	40	1.60	200
CM32256R8□S□ -□□□	6.80	J , K , M	30	7.960	35	1.80	180
CM32258R2□S□ -□□□	8.20	J , K , M	30	7.960	30	2.00	170
CM3225100□S□ -□□□	10.00	J , K , M	30	2.520	30	2.10	150
CM3225120□S□ -□□□	12.00	J , K , M	30	2.520	28	2.50	140
CM3225150□S□ -□□□	15.00	J , K , M	30	2.520	25	2.80	130
CM3225180□S□ -□□□	18.00	J , K , M	30	2.520	22	3.30	120
CM3225220□S□ -□□□	22.00	J , K , M	30	2.520	19	3.70	110
CM3225270□S□ -□□□	27.00	J , K , M	30	2.520	18	5.00	80
CM3225330□S□ -□□□	33.00	J , K , M	30	2.520	17	5.60	70
CM3225390□S□ -□□□	39.00	J , K , M	30	2.520	15	6.40	65
CM3225470□S□ -□□□	47.00	J , K , M	30	2.520	14	7.00	60
CM3225560□S□ -□□□	56.00	J , K , M	30	2.520	13	8.00	55
CM3225680□S□ -□□□	68.00	J , K , M	30	2.520	11	9.00	50
CM3225820□S□ -□□□	82.00	J , K , M	30	2.520	10	10.00	45
CM3225101□S□ -□□□	100.00	J , K , M	20	0.796	9	11.00	40
CM3225121□S□ -□□□	120.00	J , K , M	20	0.796	8	11.00	70
CM3225151□S□ -□□□	150.00	J , K , M	20	0.796	7	15.00	65

- 1). Electrical specifications at 25°C
- 2). Tolerance : J = ±5%, K = ±10%, M = ±20%

AR-001C

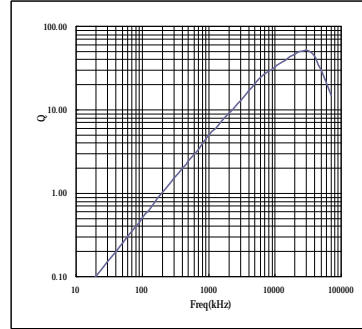
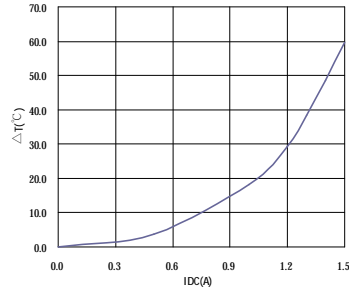
SPECIFICATION FOR APPROVAL

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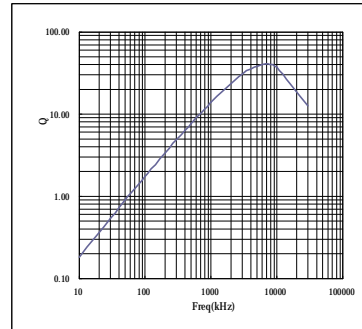
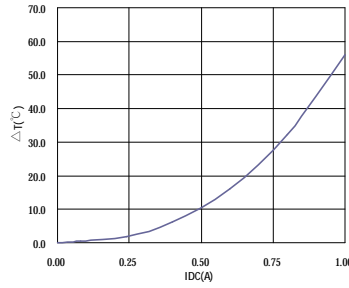
PROD. NAME	Wound Chip Inductor	ABC'S DWG NO.	CM3225□□□□S□-□□□		
		REV.	20240913-D	PAGE	3

V . Curve :

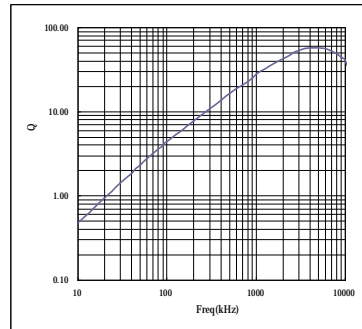
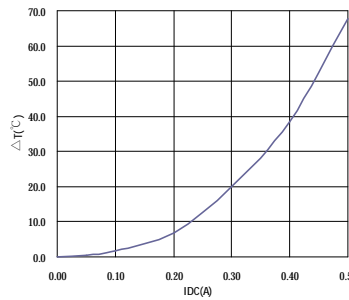
CM3225R12□S□



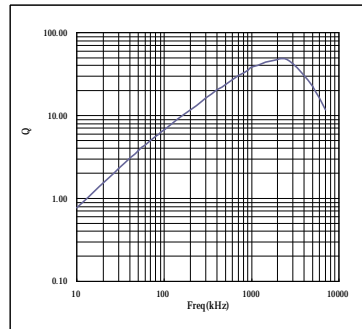
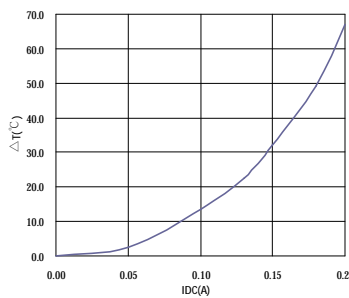
CM32251R0□S□



CM3225100□S□



CM3225121□S□



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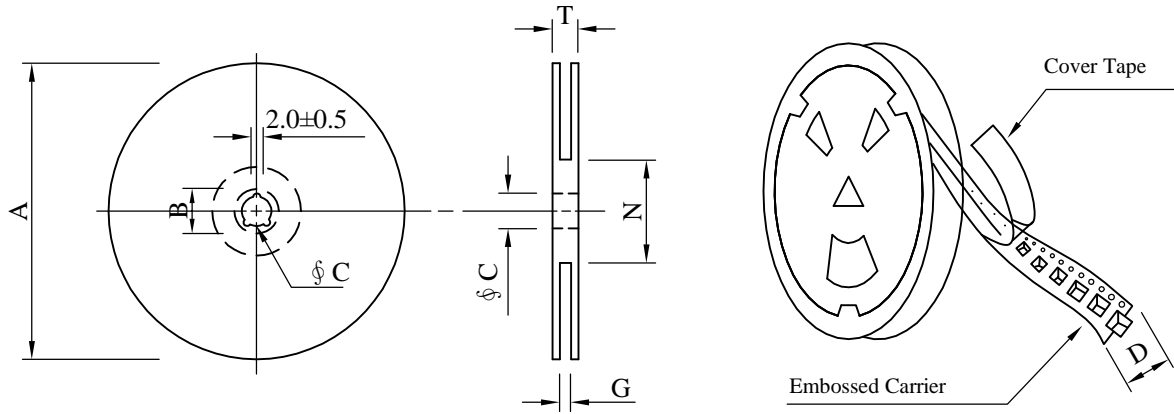
千如電子集團
ABC-ATEC ELECTRONICS GROUP

SPECIFICATION FOR APPROVAL

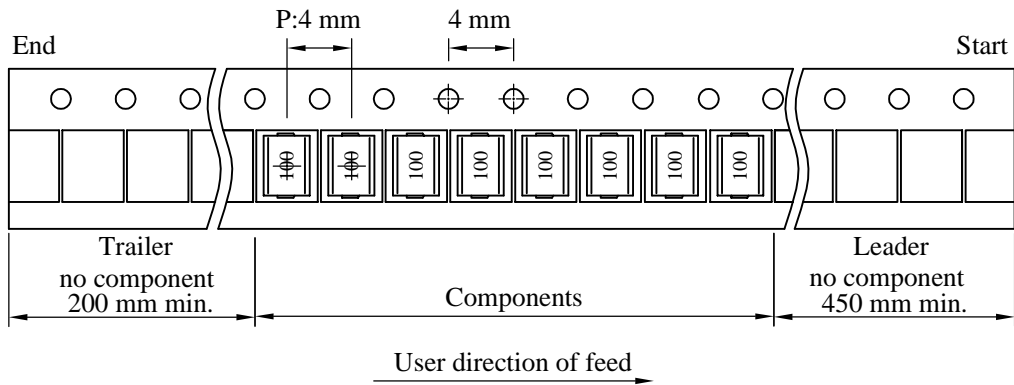
PROD. NAME	Wound Chip Inductor	ABC'S DWG NO.	CM3225□□□□S□-□□□		
		REV.	20240913-D	PAGE	4-1

VI-1 . Packaging information :

(1) Configuration



※ Carrier Tape Width : D



※ There is no differentiation or directions of polarity (marking) in the packaging method.

(2) Dimensions

Unit:mm

Style	A	B	C	D	G	N	T
07 - 08	178	21±0.8	13	8	10 ⁺⁰	50 ⁻⁰	12.5

(3) Q'TY & G.W. Per package

Code	Inner : Reel			Outer : Carton		
	Q'TY (pcs)	G.W. (g)	Style	Q'TY (pcs)	G.W. (kg)	Size (cm)
B	1,000	110	07 - 08	50,000	7.50	39.5 x 39.0 x 23.0

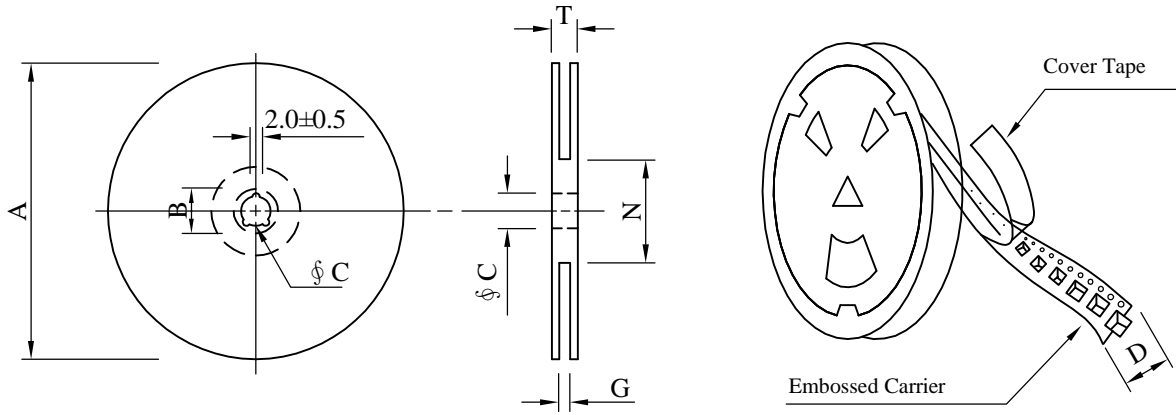
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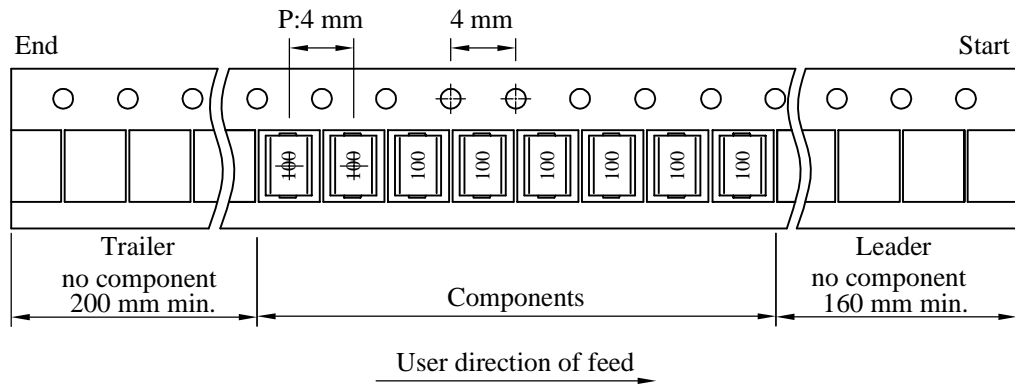
PROD. NAME	Wound Chip Inductor	ABC'S DWG NO.		CM3225□□□□S□-□□□	
		REV.	20240913-D	PAGE	4-2

VI-2 . Packaging information :

(1) Configuration



※Carrier Tape Width : D



※ There is no differentiation or directions of polarity (marking) in the packaging method.

(2) Dimensions

Unit:mm

Style	A	B	C	D	G	N	T
07 - 08	178	21±0.8	13	8	10 ⁺⁰	50 ⁻⁰	12.5

(3) Q'TY & G.W. Per package

Code	Inner : Reel			Outer : Carton		
	Q'TY (pcs)	G.W. (g)	Style	Q'TY (pcs)	G.W. (kg)	Size (cm)
F	2,000	220	07 - 08	100,000	15.00	39.5 x 39.0 x 23.0

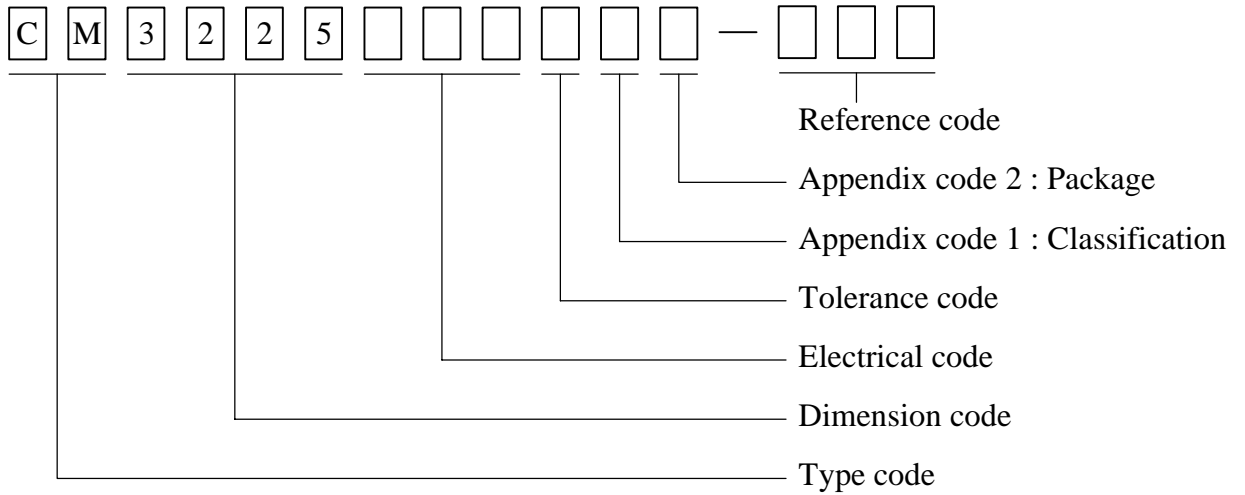
AR-001C

SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Wound Chip Inductor	ABC'S DWG NO.	CM3225□□□□S□-□□□		
		REV.	20240913-D	PAGE	5

VII . Drawing number expression :



Appendix code 1 : Product Classification

Appendix code 2 : Package Information

Code	Inner package	Cover tape	Carrier tape	Bag	Package Q'TY	Remark
B	T /R (Reel package)	Adhesive	Non-antistatic	Antistatic	1,000 pcs	
F	T /R (Reel package)	Adhesive	Non-antistatic	Antistatic	2,000 pcs	

AR-001C

SPECIFICATION FOR APPROVAL

REF. :

PROD.		ABC'S DWG NO.	CM3225□□□□S□-□□□		
NAME	Wound Chip Inductor	REV.	20240913-D	PAGE	6
VIII . Reliability test :					
Item	Reference documents	Test Condition	Test Specification		
1.High Temperature Exposure	MIL-STD-202 Method 108	1.Temperature: 125±2°C 2.Time:96±2 hours.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±10%.		
2.Temperature Cycling	JESD22-A 104	1.Temperature: -40°C ~ +125°C 2.Number of cycle:100 cycle 3.Dwell time:30 minutes	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±10%.		
3.Biased Humidity Test	MIL-STD-202 Method 103	1.Temperature : 85±2 °C 2.Humidity: 85% RH. 3.Time:96±2 Hours	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±10%.		
4.Operational Life	JESD22-A 108	1.Temperature: 125°C(Temp. rise included) 2.Time:96±2 hours. 3.Rated current	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±10%.		
5.External Visual	JESD22-B 101 & MIL-STD-883 Method 2009	Inspect product constructions, marking and workmanship.	1.No pollution on the surface of products. 2.Clear marking. 3.No crack.		
6.Physical Dimensions	JESD22-B 100	Verify physical dimensions to the applicable product detail specification.	Per product specification standard		
7.Resistance to solvents	MIL-STD-202 Method 215	Immerse into solvent for 3±0.5 minutes & brush 10 times for 3 cycles.	1.No body change in apperance. 2.No marking blurred. 3.Inductance shall not change more than ±10%.		
8.Vibration Test	MIL-STD-202 Method 204	1.Frequency and Amplitued :10-2000-10 Hz 2.Sweep time : 20 min 3.Acceleration : 5g 4.Direction : X , Y , Z 5.Number of sweep : 6 time/axis	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±10%.		
9.Resistance To Soldering Heat Test	MIL-STD-202 Method 210 & J-STD020	1.Highest temperature : 250±5°C. 2.Time (temp.≥ 217°C) : 60~150 Second. 3.Reflow times : 3 times.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±10%.		
10.Saturation Current	JIS C 6436 & User SPEC.	1.Applied rated current for 5 second. 2.Rated current	Inductance shall not drop more than 10% max.		
11.Over load	JIS C 6436 & User SPEC.	1.Applied one and half rated current for a period of 5 minutes. 2.Rated current	No electrical or mechanical damage		
12.Temperature Rise Current	JIS C 6436 & User SPEC.	1.Applied rated current for 10 minutes. 2.Temperature measure by digital surface thermometer. 3.Irms current	Surface temperature rise is less than 20°C max.		
13.Solderability Test	J-STD-002 & JESD22-B 102	1.Baking in pre-testing : 150±5°C / 16Hours±30 min. 2.Peak temperature : 240±5°C 3.Time (temp.≥217°C) : 60~150 second. 4.Reflow times : 1 times.	More than 95% soldering coverage min on terminations.		
14.Electrical Characteriazation	MIL-STD-202 Method 304 & User SPEC.	1.Operating temperature : -40°C~125°C 2.Room temperature : 25°C.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±10%.		
15.Withstanding Voltage Test	MIL-STD-202 Method 301 & User SPEC.	1.DC: 500 V (Terminal to Coating) 2.Time : 1minutes	1.During the test no breakdown. 2.No mechanical or electrical damage.		
16.Insulation Resistance	MIL-STD-202 Method 302	DC voltage 100V applied between inductor terminal and coating for 1 minute.	1.IR = 1000MΩ Min. 2.No mechanical or electrical damage.		
17.Drop	CNS-C6354 & GB/T 2423.8	1.Products shall be mounted on SPEC. pcb and dropped down from a heigh of 1m 2.Drop total time : 6 time (Every side ofsample drop 2 time)	1. Adhesion on PCB shall be enough. 2. Product appearance shall not break. 3. No electrical damage.		
18.Terminal Strength Test	IEC 60068-2-21	1.Apply push force to samples mounted on PCB. 2.Force of 1.8 kg for 60±1 seconds.	After test, inductors shall be no mechanical damage.		

AR-001C

SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Wound Chip Inductor	ABC'S DWG NO.		CM3225□□□□S□-□□□	
		REV.	20240913-D	PAGE	7

IX . Safety notes :

1. The risks of using the product are highly relevant to the field of application and need to be evaluated by both user and manufacturer. If the product is used for a purpose that directly affects personal safety or will cause significant impact or loss to the society , please be sure to contact us first for confirmation.
產品使用的風險與應用領域有高度相關，若您應用於直接影響人身安全系統或對社會會造成重大影響與損失之相關用途，請務必優先與我們聯繫確認。
2. The storage period is less than 12 months. Ensure to follow the storage conditions (Temperature: 5 to 30°C, Humidity: 10 to 60% RH or less). If the storage time is exceeded the limit, the electrodes might be deteriorate of terminal soldering.
儲存期不超過12個月，務必遵守儲存條件（溫度：5至30°C，濕度：10至60%RH以下）。如果超過了儲存時間，端子電極可能會氧化而影響焊接。
3. Do not use or store in locations where there are conditions such as gas corrosion (salt, acid, alkali, etc.).
不要在有氣體腐蝕等條件的地方使用或存放（鹽，酸，鹼等）。
4. Soldering corrections after mounting should be within the range of the conditions determined in the specifications. If overheated, a short circuit, performance deterioration, or lifespan shortening may occur.
安裝時的焊接條件應在規格範圍內。如果超過要求，可能會發生短路，性能下降或壽命縮短。
5. When using, it should try to avoid excessive mechanical impact on the product, such as collision / drop ... and other reasons.
使用時，應盡量避免產品受到過度機械衝擊，如碰撞/掉落... 等原因。
6. When embedding a printed circuit board where a chip is mounted to a set, be sure that residual stress is not given to the chip due to the overall distortion of the printed circuit board and partial distortion such as at screw tightening portions.
將已安裝新片的電路板組裝到裝置時，請注意應盡量避免電路板受到組裝變形... 等，導致產品受到應力。
7. Self heating (temperature increase) occurs when the power is turned ON, so the tolerance should be sufficient for the set thermal design.
產品會因通電而自我發熱(溫度上升)，因此在熱影響設計方面，需保留適當公差。
8. Do not expose the products to magnets or magnetic fields.
請勿將產品暴露於磁鐵或是磁場中。
9. If you would like to use this products to performance and/or quality require a more stringent level of safety or reliability, or whose failure, malfunction or trouble could cause serious damage to society, person or property, or if you have special requirements exceeding the range or conditions set forth in the each catalog, please contact us.
如果您希望將此產品用於性能和/或質量要求更嚴格的安全性或可靠性，或其失敗、故障或麻煩可能對社會、個人或財產造成嚴重損害，或者您有特殊情況要求超出目錄中規定的範圍或條件，請與我們聯繫。

AR-001C