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COMPLIANT

19N10G-T3P-T-VB Datasheet N-Channel 100-V (D-S) MOSFET

PRODUCT	SUMMARY	
V _{(BR)DSS} (V)	r _{DS(on)} (Ω)	I _D (A)
100	0.035 at V _{GS} = 10 V	85

FEATURES

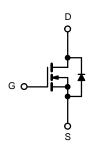
- TrenchFET[®] Power MOSFET
- 175 °C Junction Temperature
- Low Thermal Resistance Package
- 100 % R_g Tested

APPLICATIONS

• Isolated DC/DC Converters



TO-247AC



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T _C = 25 °C, unless oth	erwise noted		
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V _{DS}	100	V
Gate-Source Voltage		V _{GS}	± 20	- V
Continuous Drain Current ($T_1 = 175 ^{\circ}C$)	T _C = 25 °C	1-	85	
Continuous Drain Current (1) = 175 C)	T _C = 125 °C	I _D	60	Α
Pulsed Drain Current		I _{DM}	150	A
Avalanche Current		I _{AS}	39	
Single Pulse Avalanche Energy ^b		E _{AS}	61	mJ
Martine Draw Dissission	T _C = 25 °C	Р	375 ^c	10/
Maximum Power Dissipation ^b	T _A = 25 °C ^d	– P _D –	3.75	W
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C

THERMAL RESISTANCE R	ATINGS			
Parameter		Symbol	Limit	Unit
Junction-to-Ambient	PCB Mount	R _{thJA}	40	°C/W
Junction-to-Case (Drain)		R _{thJC}	0.4	0/11

Notes:

a. Package limited.

b. Duty cycle \leq 1 %.

c. See SOA curve for voltage derating.

d. When Mounted on 1" square PCB (FR-4 material).

SPECIFICATIONS $T_J = 25^{\circ}$ Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{DS} = 0 V, I _D = 250 μA	100			.,
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \ \mu A$	1		3	V
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
		$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}$			1	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V, T _J = 125 °C			50	μA
		$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 ^{\circ}\text{C}$			250	-
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	40			А
		V _{GS} = 10 V, I _D = 30 A		0.035		
Drain-Source On-State Resistance ^a	r _{DS(on)}	V_{GS} = 10 V, I _D = 30 A, T _J = 125 °C		0.063		Ω
		V_{GS} = 10 V, I_{D} = 30 A, T_{J} = 175 °C		0.084		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 30 A	25			S
Dynamic ^b						
Input Capacitance	C _{iss}			5100		
Output Capacitance	C _{oss}	V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz		480		pF
Reverse Transfer Capacitance	C _{rss}			210		
Total Gate Charge ^c	Qg			90		
Gate-Source Charge ^c	$\begin{tabular}{ c c c c c c } \hline C_{oss} & $V_{GS} = 0 V, $V_{DS} = 25 V, $f = 1 MHz & $$$$ \\ \hline C_{rss} & $$$ \\ \hline Q_g & $$$ \\ \hline Q_{gs} & $V_{DS} = 100 V, $V_{GS} = 10 V, $I_D = 65 A & $$$$ \\ \hline Q_{gd} & $$$ \\ \hline Q_{gd} & $$$$ \\ \hline Q_{gd} & $$$ \\ \hline Q_{gd} & $$$$ \\ \hline Q_{gd} & $$$ \\ \hline Q_{gd} & $$ \\ \hline Q_{gd} & $$$ \\ \hline Q_{gd} & $$$ \\ \hline Q_{gd} & $$ \\ \hline Q_{gd} & $$ \\ \hline Q_{gd} & $$ \\ \hline Q_{gd} & $$$ \\ \hline Q_{gd} & $$ \\ \hline \ \ Q_{gd} & $$ \\ \hline \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \$		23		nC	
Gate-Drain Charge ^c	Q _{gd}			34		
Gate Resistance	R _g		0.5	1.7	3.3	Ω
Turn-On Delay Time ^c	t _{d(on)}			24	35	
Rise Time ^c	t _r	V_{DD} = 100 V, R _L = 1.5 Ω		220	330	
Turn-Off Delay Time ^c	t _{d(off)}	$\text{I}_\text{D} \cong$ 65 A, V_GEN = 10 V, R_g = 2.5 Ω		45	70	ns
Fall Time ^c	t _f			200	300	
Source-Drain Diode Ratings and Cha	aracteristics 7	_C = 25 °C ^b				
Continuous Current	ا _S			85		
Pulsed Current	I _{SM}			150		A
Forward Voltage ^a	V _{SD}	$I_{F} = 65 \text{ A}, \text{ V}_{GS} = 0 \text{ V}$		1.0	1.5	V
Reverse Recovery Time	t _{rr}			130	200	ns
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = 50 A, di/dt = 100 A/µs		8	12	А
Reverse Recovery Charge	Q _{rr}			0.52	1.2	μC

Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

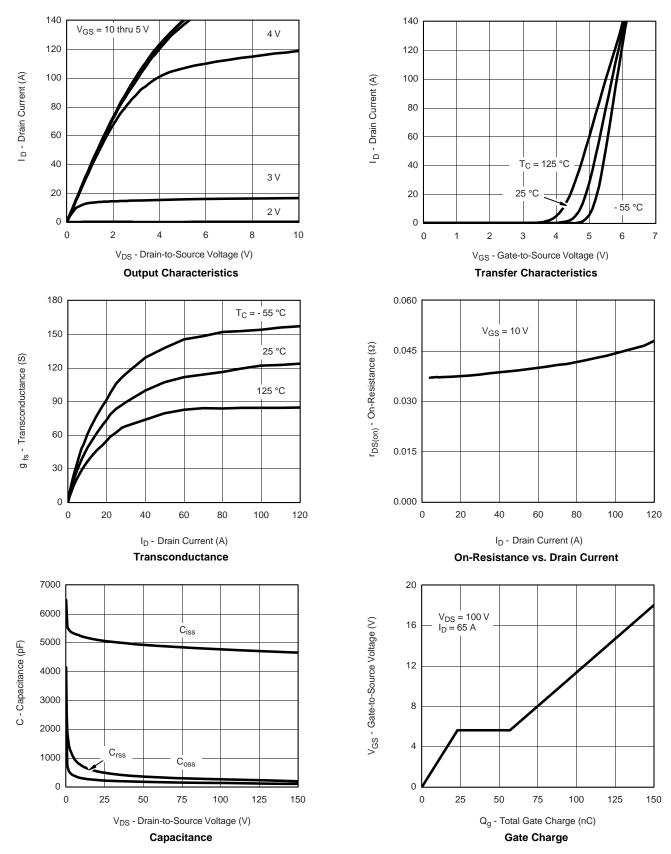
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Bsemi

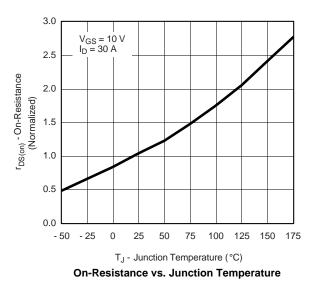


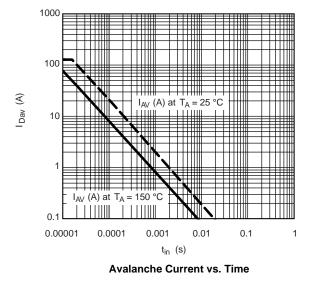


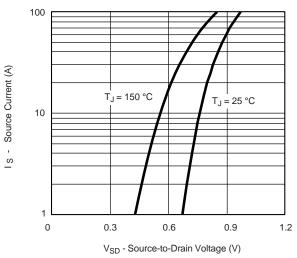




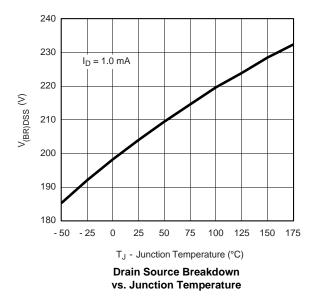
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







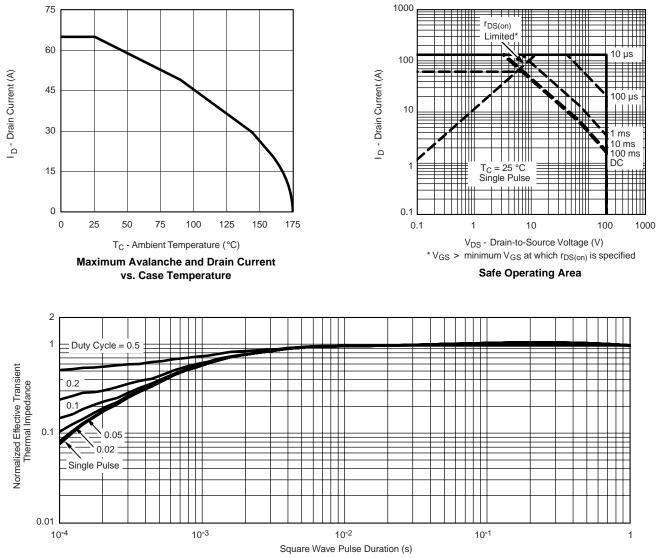
Source-Drain Diode Forward Voltage



19N10G-T3P-T-VB

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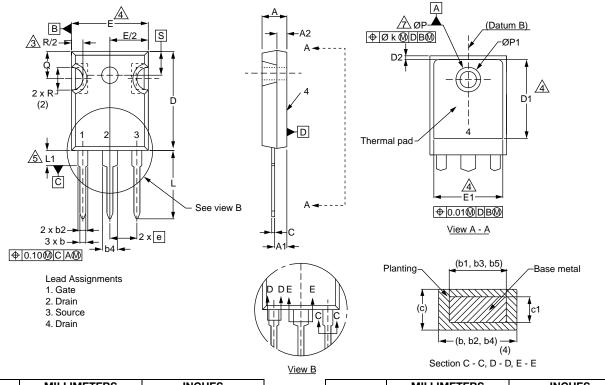
THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Case



TO-247AC



	MILLIMETERS		INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
А	4.58	5.31	0.180	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.17	2.49	0.046	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.53	2.39	0.060	0.094	
b3	1.65	2.37	0.065	0.093	
b4	2.42	3.43	0.095	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.86	0.015	0.034	
c1	0.38	0.76	0.015	0.030	
D	19.71	20.82	0.776	0.820	
D1	13.08	-	0.515	-	

	MILLIN	IETERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	
E1	13.72	-	0.540	-	
е	5.46	5.46 BSC		5 BSC	
Øk	0.:	254	0.0	010	
L	14.20	16.25	0.559	0.640	
L1	3.71	4.29	0.146	0.169	
Ν	7.62	7.62 BSC) BSC	
ØР	3.51	3.66	0.138	0.144	
Ø P1	-	7.39	-	0.291	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51 BSC		0.217	7 BSC	



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