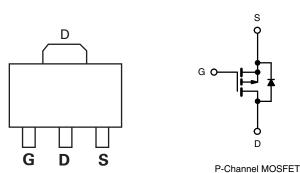


P-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY								
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)	Q _g (Typ.)					
- 100	0.200 at V _{GS} = - 10 V	- 3.0	13.2 nC					
- 100	0.230 at V _{GS} = - 6 V	- 2.4	13.2110					



FEATURES

- TrenchFET® Power MOSFET
- 100% R_q and UIS Tested

ROHS COMPLIANT HALOGEN FREE

APPLICATIONS

- Active Clamp in Intermediate DC/ DC Power Supplies
- H-Bridge High Side Switch for Lighting Application

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V _{DS}	- 100	V		
Gate-Source Voltage	V _{GS}	± 20	v		
	T _C = 25 °C		- 3.0		
Continuous Drain Current (T _{.I} = 150 °C)	T _C = 70 °C	1	-2.1		
Continuous Diairi Curient (1) = 150 °C)	T _A = 25 °C	I _D	- 2 ^{a, b}		
	T _A = 70 °C		- 1.6 ^{a, b}	Α	
Pulsed Drain Current	I _{DM}	- 12	A		
Continuous Course Brain Binds Courset	T _C = 25 °C	1	- 4.9		
Continuous Source-Drain Diode Current	T _A = 25 °C	ls -	- 2.5 ^{a, b}		
Avalanche Current	L = 0.1 mH	I _{AS}	- 15		
Single-Pulse Avalanche Energy	L = 0.1 IIII	E _{AS}	11.25	mJ	
	T _C = 25 °C		6.5		
Maximum Dawar Dissination	T _C = 70 °C		4.8	w	
Maximum Power Dissipation	T _A = 25 °C	P _D	3.1 ^{a, b}		
	T _A = 70 °C		2 ^{a, b}		
Operating Junction and Storage Temperature Range	T _J , T _{stq}	- 55 to 150	°C		

Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. t = 10 s.

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient ^{a, b}	t ≤ 10 s	R_{thJA}	33	40	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	17	21			

Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. Maximum under steady state conditions is 80 $^{\circ}\text{C/W}.$



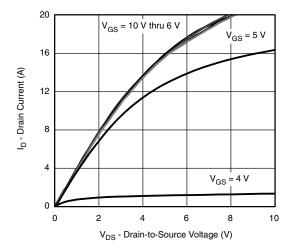
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static				•			
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 100			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = - 250 μA		- 165			
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	η = - 250 μΑ		- 6.6		mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 2		- 4	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zara Cata Valtaga Drain Current	l	V _{DS} = - 100 V, V _{GS} = 0 V			- 1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 100 V, V _{GS} = 0 V, T _J = 55 °C			- 10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 8			Α	
	D	V _{GS} = - 10 V, I _D = - 3 A		0.200		Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 6 V, I _D = - 2 A		0.230			
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = 3 A		12		S	
Dynamic ^b							
Input Capacitance	C _{iss}			819			
Output Capacitance	C _{oss}	$V_{DS} = -35 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		51		pF	
Reverse Transfer Capacitance	C _{rss}			32			
Total Cata Charge	Qg	$V_{DS} = -50 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -3 \text{ A}$		17.5	32		
Total Gate Charge				13.2	25	~C	
Gate-Source Charge	Q _{gs}	$V_{DS} = -50 \text{ V}, V_{GS} = -6 \text{ V}, I_{D} = -3 \text{ A}$		3.4		nC	
Gate-Drain Charge	Q _{gd}			6.4			
Gate Resistance	R_g	f = 1 MHz		6.1	9.2	Ω	
Turn-On Delay Time	t _{d(on)}			10	20		
Rise Time	t _r	$V_{DD} = -50 \text{ V}, R_L = 25 \Omega$		55	95		
Turn-Off DelayTime	t _{d(off)}	$I_D \cong -3 \text{ A}, V_{GEN} = -6 \text{ V}, R_g = 1 \Omega$		20	40		
Fall Time	t _f			15	30		
Turn-On Delay Time	t _{d(on)}			11	18	ns	
Rise Time	t _r	$V_{DD} = -50 \text{ V}, R_{L} = 25 \Omega$		18	32		
Turn-Off DelayTime	t _{d(off)}	$I_D \simeq -3 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$		32	58]	
Fall Time	t _f			20	35	<u>1 </u>	
Drain-Source Body Diode Characterist	ics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 13	۸	
Pulse Diode Forward Current ^a	I _{SM}				- 15	Α	
Body Diode Voltage	V_{SD}	I _S = - 3 A		- 0.8	- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			65	90	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = - 3 A, dl/dt = 100 A/μs, T _J = 25 °C		180	270	nC	
Reverse Recovery Fall Time	t _a	$_{1}^{1}$ $_{1}^{2}$ $_{2}^{3}$ $_{3}^{4}$ $_{4}^{3}$ $_{5}^{4}$ $_{1}^{3}$ $_{2}^{2}$ $_{3}^{3}$ $_{4}^{3}$ $_{5}^{4}$ $_{1}^{3}$ $_{2}^{2}$ $_{3}^{3}$ $_{4}^{3}$ $_{5}^{4}$ $_{1}^{3}$ $_{2}^{3}$ $_{3}^{4}$ $_{3}^{4}$ $_{4}^{3}$ $_{5}^{4}$ $_{1}^{3}$ $_{2}^{4}$ $_{3}^{4}$ $_{3}^{4}$ $_{4}^{3}$ $_{5}^{4}$ $_{5}^{4}$ $_{1}^{4}$ $_{2}^{4}$ $_{3}^{4}$ $_{3}^{4}$ $_{4}^{4}$ $_{5}^{4}$ $_{1}^{4}$ $_{2}^{4}$ $_{3}^{4}$ $_{3}^{4}$ $_{4}^{4}$ $_{5}^{4}$ $_{1}^{4}$ $_{2}^{4}$ $_{3}^{4}$ $_{3}^{4}$ $_{4}^{4}$ $_{5}^{4}$		45			
Reverse Recovery Rise Time	t _b			20		ns	

Notes:

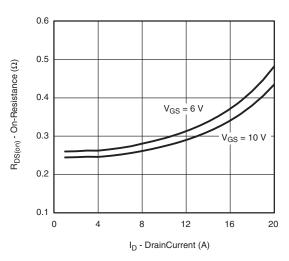
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

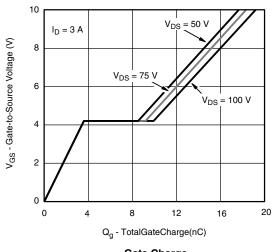




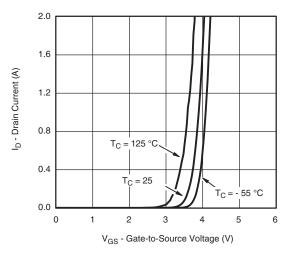
Output Characteristics



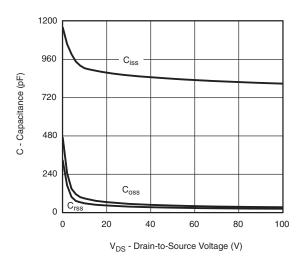
On-Resistance vs. Drain Current and Gate Voltage



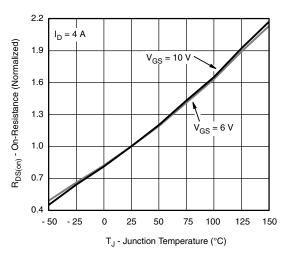
Gate Charge



Transfer Characteristics

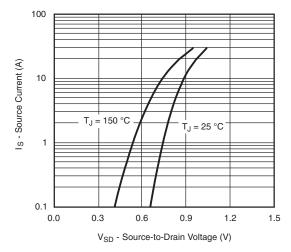


Capacitance

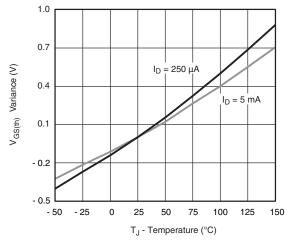


On-Resistance vs. Junction Temperature

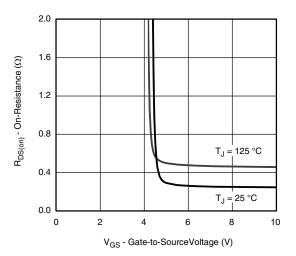




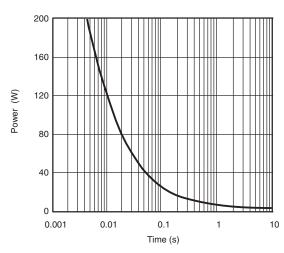
Source-Drain Diode Forward Voltage



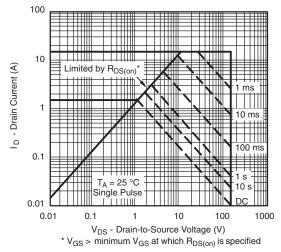
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage

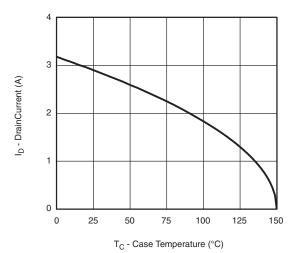


Single Pulse Power, Junction-to-Ambient

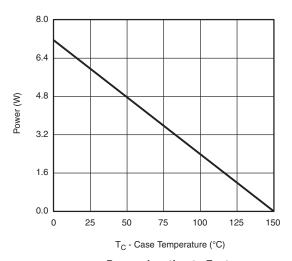


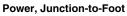
Safe Operating Area, Junction-to-Ambient

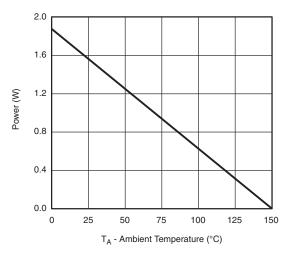




Current Derating*



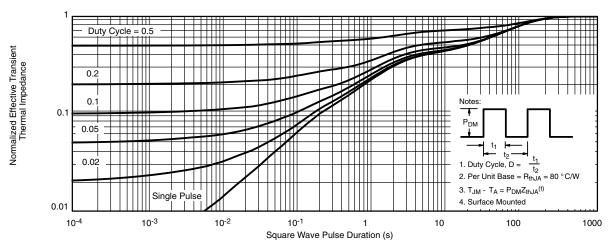




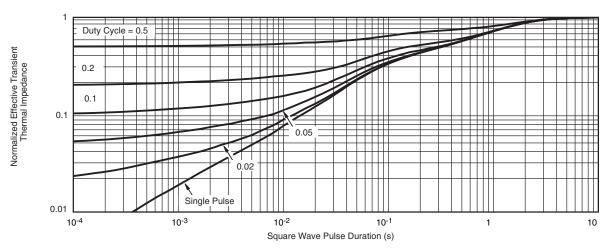
Power, Junction-to-Ambient

^{*} The power dissipation P_D is based on $T_{J(max.)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





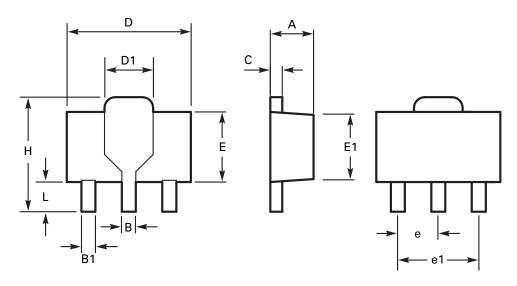
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot



Package outline - SOT89



DIM	Millin	neters	Inc	hes	DIM Millimeters Inches		Millimeters		hes
	Min	Max	Min	Max		Min	Max	Min	Max
Α	1.40	1.60	0.550	0.630	Е	2.29	2.60	0.090	0.102
В	0.44	0.56	0.017	0.022	E1	2.13	2.29	0.084	0.090
B1	0.36	0.48	0.014	0.019	е	1.50 BSC		0.059 BSC	
С	0.35	0.44	0.014	0.017	e1	3.00 BSC		0.118 BSC	
D	4.40	4.60	0.173	0.181	Н	3.94	4.25	0.155	0.167
D1	1.62	1.83	0.064	0.072	L	0.89	1.20	0.035	0.047

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches



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