

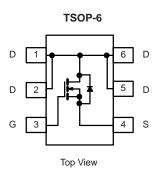
## N-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	60		
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS}$ = 10 V	0.030		
$R_{DS(on)}\left(\Omega\right)$ at $V_{GS}$ = 4.5 V	0.035		
I <sub>D</sub> (A)	7		
Configuration	Single		

#### FEATURES

- TrenchFET<sup>®</sup> power MOSFET
- 100 %  $\rm R_g$  and UIS tested





<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>C</sub> = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V <sub>DS</sub>	60	V	
Gate-Source Voltage		V <sub>GS</sub> ± 20		V	
Continuous Drain Current	T <sub>C</sub> = 25 °C	- I <sub>D</sub>	7		
	T <sub>C</sub> = 125 °C		4		
Continuous Source Current (Diode Conduction)		ا <sub>S</sub>	6	А	
Pulsed Drain Current <sup>a</sup>		I <sub>DM</sub>	29		
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	10		
Single Pulse Avalanche Energy	L = 0.1 mm	E <sub>AS</sub>	5	mJ	
Maximum Power Dissipation <sup>a</sup>	T <sub>C</sub> = 25 °C	PD	5	W	
	T <sub>C</sub> = 125 °C	I-D	1.6	۷V	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to +175	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount <sup>b</sup>	R <sub>thJA</sub>	110	°C/W
Junction-to-Foot (Drain)		R <sub>thJF</sub>	30	0/₩

Notes

a. Pulse test; pulse width  $\leq 300~\mu\text{s},~\text{duty~cycle} \leq 2~\%.$ 

b. When mounted on 1" square PCB (FR-4 material).

PARAMETER	SYMBOL	TES	T CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static				•	•			
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub>	= 0, I <sub>D</sub> = 250 μA	60	-	-	V	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$		-	2.5	v	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 12 V$		-	-	± 500	nA	
		$V_{DS} = 0 V, V_{GS} = \pm 20 V$		-	-	± 1	mA	
Zero Gate Voltage Drain Current		$V_{GS} = 0 V$	V <sub>DS</sub> = 60 V	-	-	1		
	I <sub>DSS</sub>	$V_{GS} = 0 V$	$V_{DS}=60~V,~T_J=125~^\circ C$	-	-	50	μA	
		$V_{GS} = 0 V$	$V_{DS} = 60 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$	-	-	150		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = 10 V	$V_{DS} \ge 5 V$	10	-	-	А	
		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 5 A	-	0.030	-	Ω	
Drain-Source On-State Resistance <sup>a</sup>	Р	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 5 A, T <sub>J</sub> = 125 °C	-	0.050	-		
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 5 A, T <sub>J</sub> = 175 °C	-	0.070	-		
		$V_{GS} = 4.5 V$	I <sub>D</sub> = 4 A	-	0.035	-		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub>	= 15 V, I <sub>D</sub> = 4 A	-	12	-	S	
Dynamic <sup>b</sup>		<u>.</u>						
Input Capacitance	C <sub>iss</sub>		V <sub>DS</sub> = 30 V, f = 1 MHz	-	560	700	pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$		-	85	105		
Reverse Transfer Capacitance	C <sub>rss</sub>			-	55	70		
Total Gate Charge <sup>c</sup>	Qg	V <sub>GS</sub> = 4.5 V	V <sub>DS</sub> = 30 V, I <sub>D</sub> = 4 A	-	7.6	12		
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>			-	2.1	-	nC	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			-	4.1	-		
Gate Resistance	Rg	f = 1 MHz		1.2	2.4	3.6	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			-	9	14		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 30 \text{ V}, \text{ R}_{\text{L}} = 7.5 \ \Omega$ $\text{I}_{\text{D}} \cong 4 \text{ A}, \text{ V}_{\text{GEN}} = 10 \text{ V}, \text{ R}_{\text{g}} = 1 \ \Omega$		-	12	18	- ns	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			-	19	29		
Fall Time <sup>c</sup>	t <sub>f</sub>			-	7	11		
Source-Drain Diode Ratings and Charact	eristics <sup>b</sup>	-					-	
Pulsed Current <sup>a</sup>	I <sub>SM</sub>			-	-	29	А	
Forward Voltage	V <sub>SD</sub>	$I_F = 1.6 \text{ A}, V_{GS} = 0$		-	0.75	1.2	V	

Notes

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

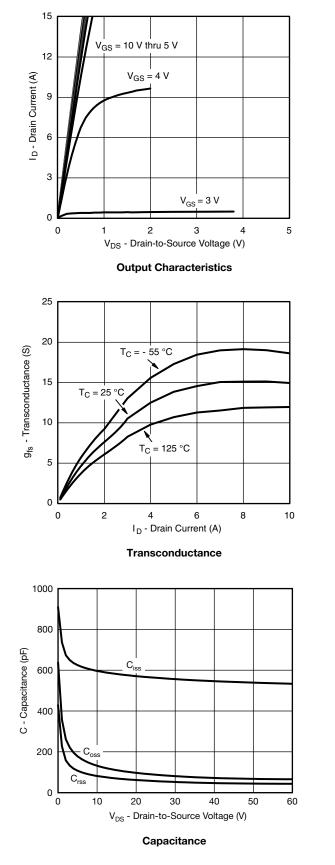
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

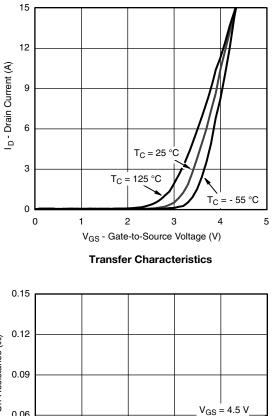
Bsemi

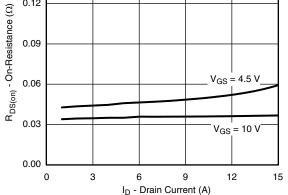
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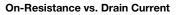


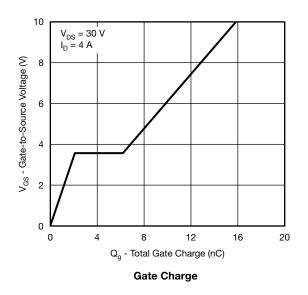
### **TYPICAL CHARACTERISTICS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)





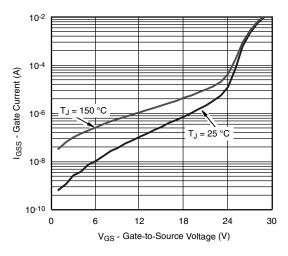




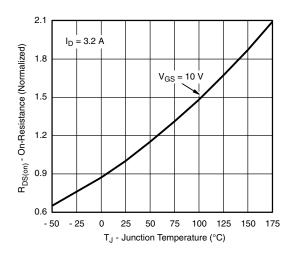




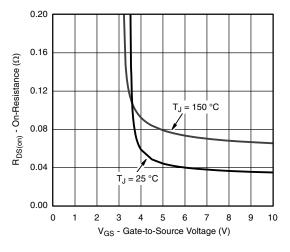
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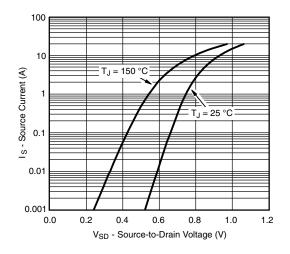
Gate Current vs. Gate-Source Voltage



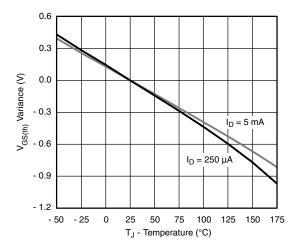
**On-Resistance vs. Junction Temperature** 



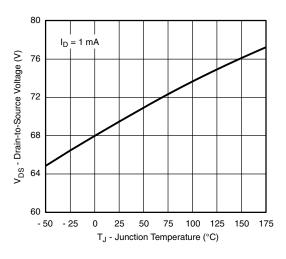
On-Resistance vs. Gate-Source Voltage



Source-Drain Diode Forward Voltage



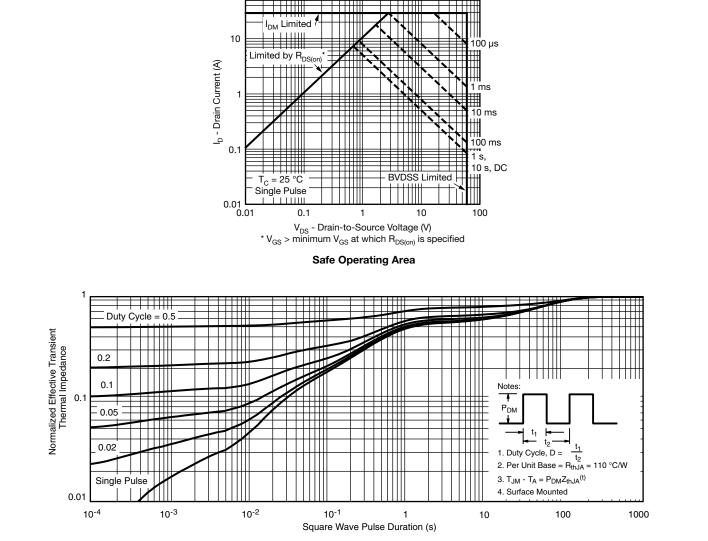




Drain-Source Breakdown vs. Junction Temperature



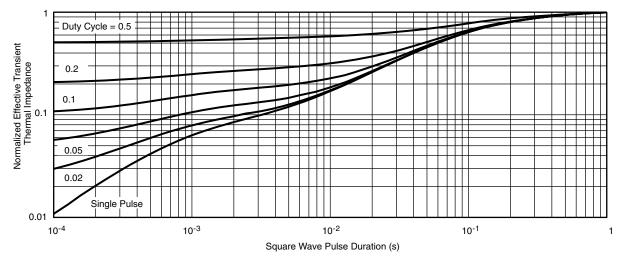
#### **THERMAL RATINGS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)



Normalized thermal Transient Impedance, Junction-to-Ambient



#### **THERMAL RATINGS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)



Normalized thermal Transient Impedance, Junction-to-Foot

#### Note

• The characteristics shown in the two graphs

- Normalized Transient Thermal Impedance Junction to Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction to Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



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