

Features

- Supply Range: 2.2V to 5.5V
- Supply Current: 510 μ A/ch
- Input Offset Voltage: 2.5mV (MAX)
- Gain Bandwidth: 8MHz
- Rail-to-Rail Input and Output
- Slew rate: 6.5V/ μ s
- Operating Temperature: -40°C ~ +85°C
- Type Package: SOT23-5

Applications

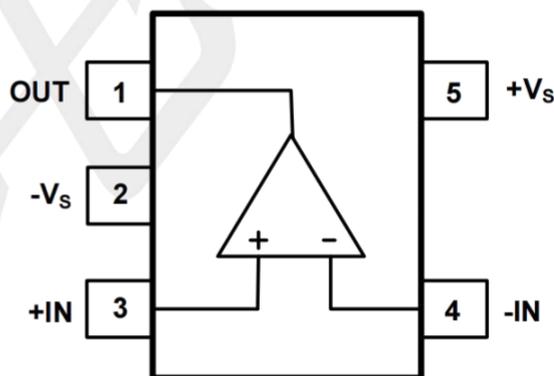
- Temperature Sensors
- Battery-Powered Instruments
- Smoke/Gas/Environment Sensors
- Medical Equipment
- Portable Instruments and Mobile Device
- Active Filters
- Piezo Electrical Transducer Amplifier
- Sensor Interface
- Handheld Test Equipment

General Description

The families of products offer low voltage operation and rail-to-rail input and output, as well as excellent speed/power consumption ratio, providing an excellent bandwidth (8MHz), a slew rate of 6.5V/ μ s, and a quiescent current of 510 μ A/amplifier at 5V. The op-amps are unity gain stable and feature an ultra-low input bias current.

The are designed to provide optimal performance in low-voltage systems. They provide rail-to-rail I/O, and the maximum input offset voltage are 2.5mV for the devices. Their capacitive load capability is also good at low supply voltages.

Pinout (top view)



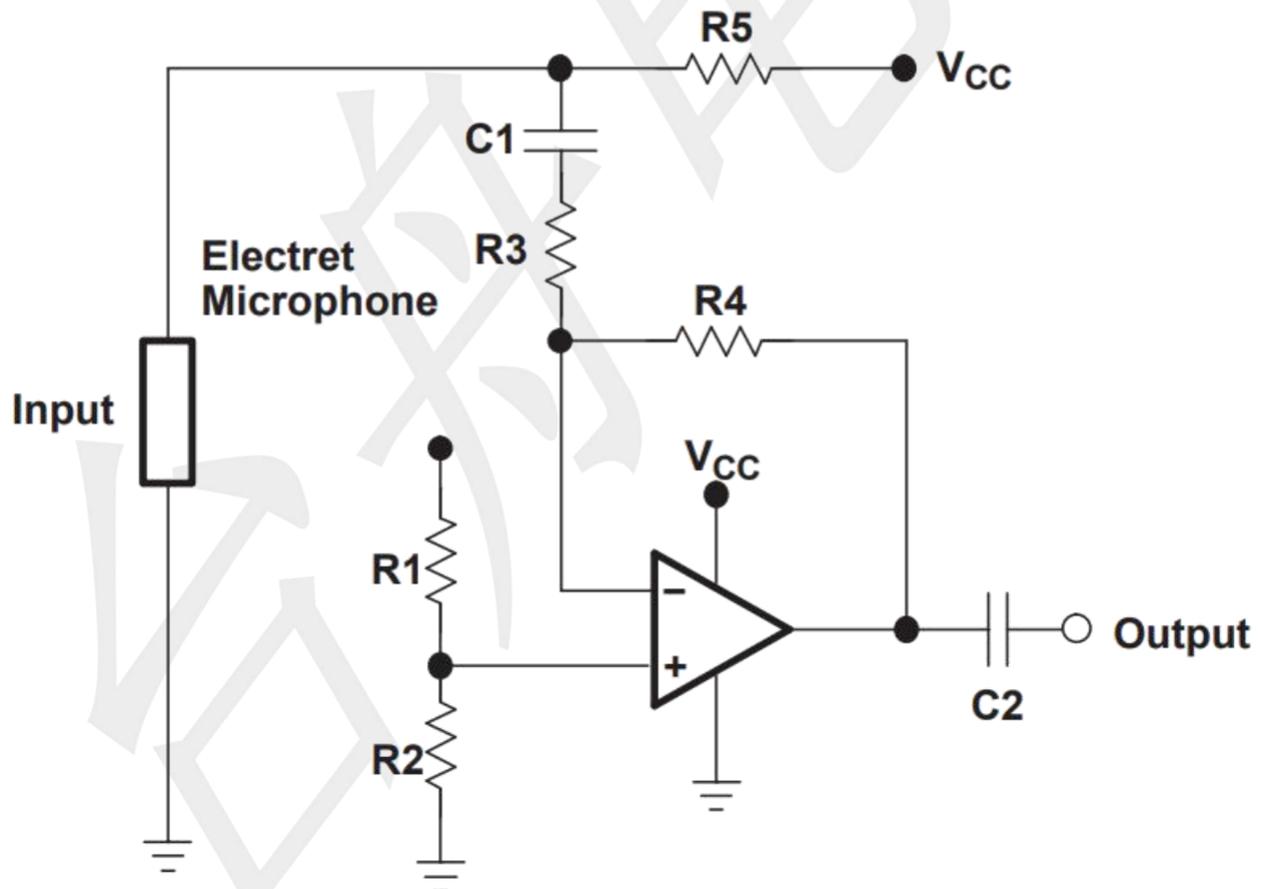
Pin Configurations

Pin Number	Pin Name	Pin Function
1	OUT	Output
2	-Vs	Chip Supply Voltage(Negative)/GND
3	+IN	In-phase input
4	-IN	Reverse input
5	+Vs	Chip Supply Voltage(Positive)/VDD

Absolute Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Condition		Rating	UNIT
VDD to GND	Power Supply Voltage	-0.5V~+6V	V
IN+ or IN-	Signal Input Terminals Voltage	GND-0.5V~VDD+0.5V	V
IIN	Signal Input Terminals Current	-10 ~ +10	mA
IOUT	Signal output Terminals Current	-200 ~ +200	mA
IOUT to GND	Output Short-Circuit	Continuous	mA
LT	Lead Temperature (Soldering, 10 sec.)	260	°C
TA	Operating Temperature Range	-40 85	°C
TJ	Junction Temperature	-40 150	°C
Tstg	Storage Temperature Range	-65 150	°C

Typical Application



Electrical Characteristics

(At $T_A = +25^\circ\text{C}$, $V_S = +5\text{V}$, $V_{IN}=V_{OUT}= V_S/2$, unless otherwise noted.)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT	
OFFSET VOLTAGE						
V_{OS}	Input Offset Voltage	-2.5	0.8	2.5	mV	
dV_{OS}/dT	Input Offset Voltage Average Drift	$T_A = -40^\circ\text{C}$ to 85°C	0.6	--	$\mu\text{V}/^\circ\text{C}$	
INPUT CURRENT						
I_B	Input Bias Current	--	10	--	pA	
I_{OS}	Input Offset Current	--	5	--	pA	
NOISE						
V_N	Input Voltage Noise	$f=0.1\text{Hz}$ to 10Hz	4	--	μV_{PP}	
e_n	Input Voltage Noise PSD	$f=1\text{kHz}$	8	--	$\text{nV}/\sqrt{\text{Hz}}$	
INPUT VOLTAGE						
V_{CM}	Common-Mode Voltage Range	V_{S-}	--	$V_{S+}+0.1$	V	
CMRR	Common-Mode Rejection Ratio	$V_{CM}=0.1\text{V}$ to 4V	85	95	dB	
FREQUENCY RESPONSE						
GBW	Gain-Bandwidth Product	--	8	--	MHz	
SR	Slew Rate	$G = +1$, $V_{IN}=2\text{V}$ Step	--	6	$\text{V}/\mu\text{s}$	
t_s	Settling Time	$G = +1$, $V_{IN}=2\text{V}$ Step	--	0.6	μs	

Electrical Characteristics

(At $T_A = +25^\circ\text{C}$, $V_S = +5\text{V}$, $V_{IN}=V_{OUT}= V_S/2$, unless otherwise noted.)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT	
OUTPUT						
A_V	Open-Loop Voltage Gain	$V_{OUT}=0.5\text{V to }4.8\text{V}$	95	105	--	dB
V_{OH}	High output voltage swing	$R_L=10\text{k}\Omega$	--	--	5	mV
V_{OL}	Low output voltage swing	$R_L=10\text{k}\Omega$	--	--	5	mV
I_{SC}	Output Short-Circuit Current	Source Current	--	26	--	mA
		Sink Current	--	50	--	mA
$C_L^{(1)}$	Capacitive Load Drive	$G = +1$, $V_{IN}=0.2\text{V Step}$	--	--	1000	pF
POWER SUPPLY						
PSRR	Power-Supply Rejection Ratio	$V_S=2.5\text{V to }5.5\text{V}$	90	100	--	dB
V_S	Operating Voltage Range	$I_O=0\text{A}$	2.2	--	5.5	V
I_Q	Quiescent Current/Amplifier	$I_O=0\text{A}$	--	510	600	μA

Note 1: Capacitive load drive means that above a given maximum value, the output waveform will oscillate under the step response.

TYPICAL CHARACTERISTICS

(At $T_A = +25^\circ\text{C}$, $V_S = \pm 2.5\text{V}$, $V_{IN} = V_{OUT} = V_S/2$, unless otherwise noted.)

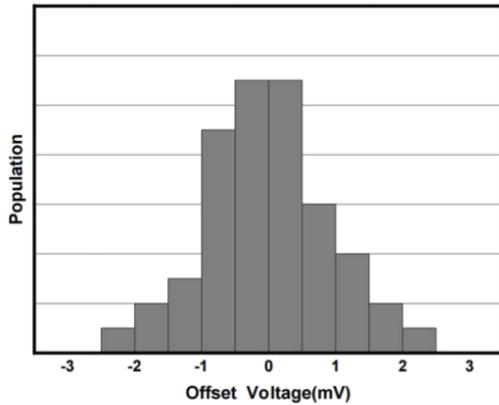


Figure 2. Offset Voltage Production Distribution

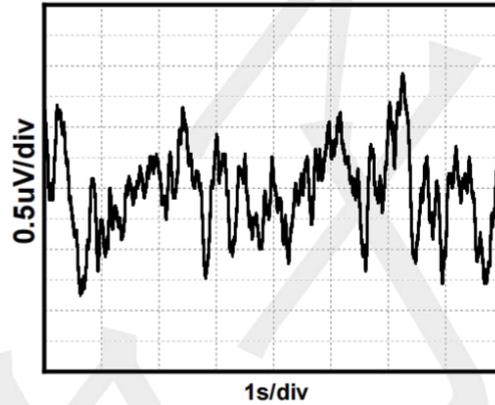


Figure 3. 0.1Hz to 10Hz Noise

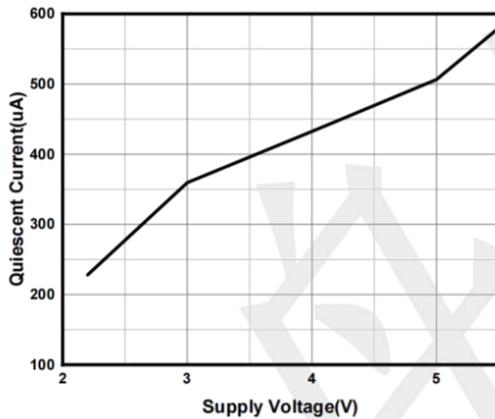


Figure 4. Quiescent Current vs Supply Voltage

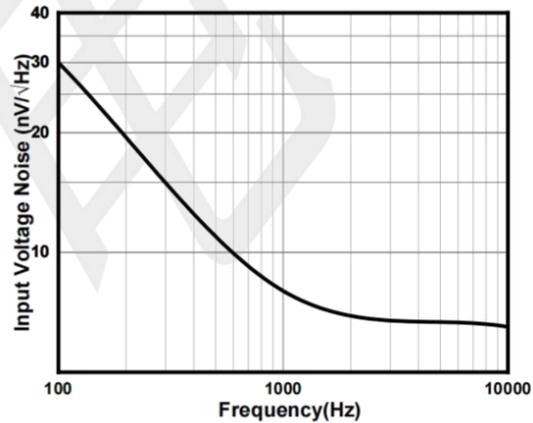


Figure 5. Input Voltage Noise Spectral Density

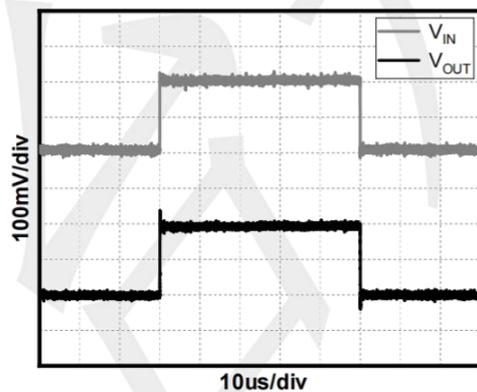


Figure 6. Small-Signal Step Response

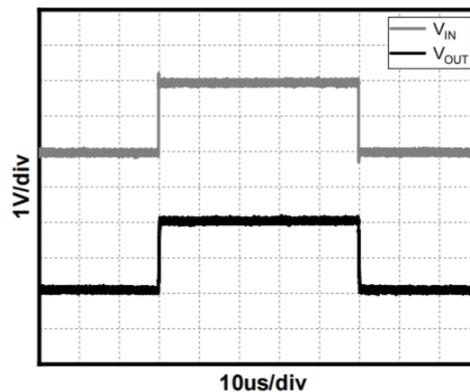
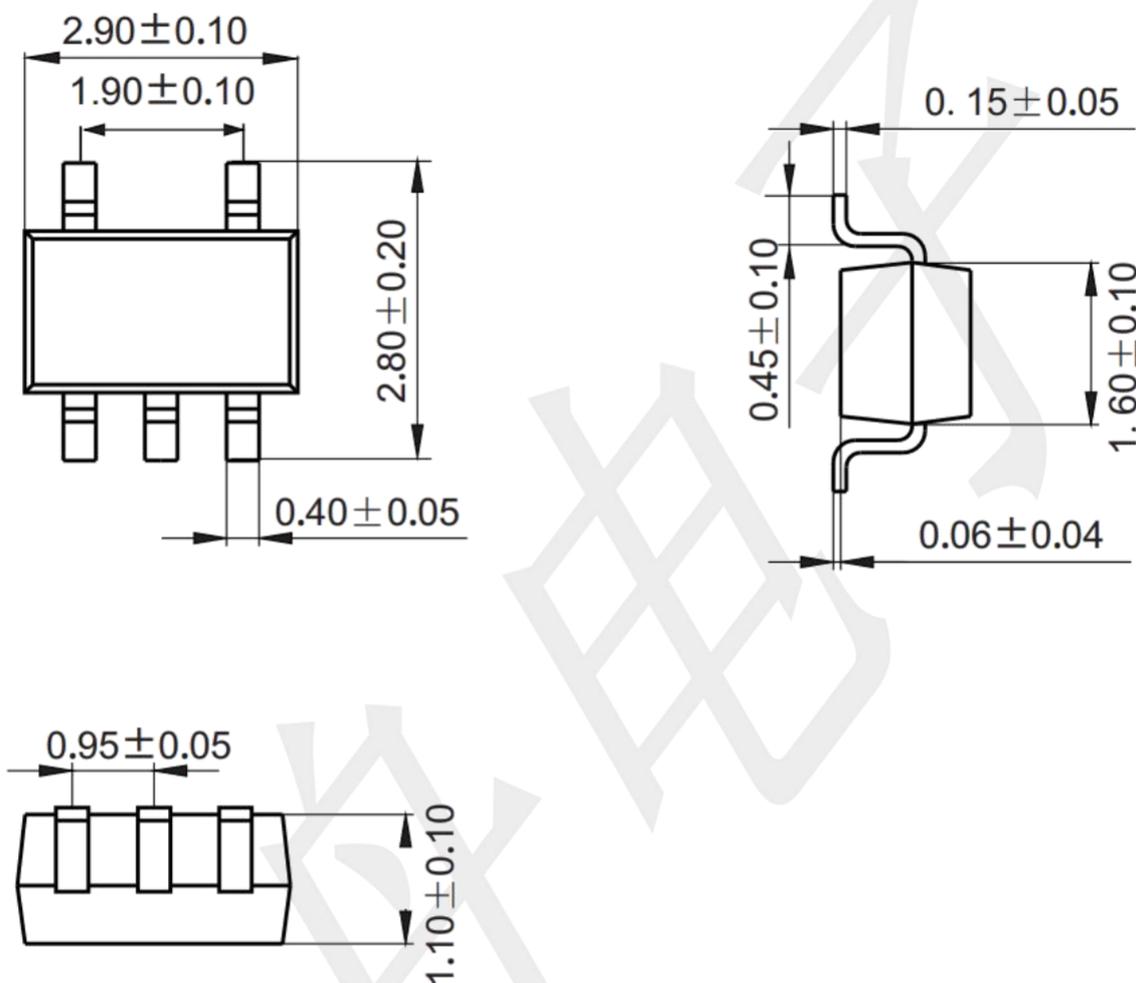


Figure 7. Large-Signal Step Response

Package information (Unit: mm)

SOT23-5



Mounting Pad Layout (Unit: mm)

