

RoHS

COMPLIANT HALOGEN

FREE

### IRF7313QTRPBF-VB Datasheet Dual N-Channel 30-V (D-S) MOSFET

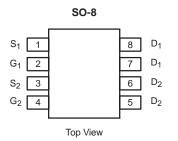
PRODUCT SUMMARY						
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) Q <sub>g</sub> (T)				
30	0.022 at $V_{GS}$ = 10 V	6.8	15 nC			
	0.026 at V <sub>GS</sub> = 4.5 V	6.0	15110			

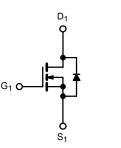
#### FEATURES

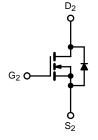
- Halogen-free According to IEC 61249-2-21
   Definition
- Trench Power MOSFET
- 100 % UIS Tested
- 100 % Rg Tested
- Compliant to RoHS Directive 2002/95/EC

#### **APPLICATIONS**

- Set Top Box
- Low Current DC/DC







N-Channel MOSFET

N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> T <sub>A</sub> = 25 °C, unless otherwise noted						
Parameter		Symbol	Limit	Unit		
Drain-Source Voltage		V <sub>DS</sub>	30	V		
Gate-Source Voltage		V <sub>GS</sub>	± 20	v		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 25 °C T <sub>C</sub> = 70 °C		6.8 <sup>a</sup> 5.6			
	T <sub>A</sub> = 25 °C T <sub>A</sub> = 70 °C	- I <sub>D</sub>	6.2 <sup>b, c</sup> 5.2 <sup>b, c</sup>			
Pulsed Drain Current		I <sub>DM</sub>	30	— A		
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C T <sub>A</sub> = 25 °C	I <sub>S</sub>	2.25 1.48 <sup>b, c</sup>			
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	5			
Single Pulse Avalanche Energy		E <sub>AS</sub>	1.25	mJ		
Maximum Power Dissipation	$T_{C} = 25 °C$ $T_{C} = 70 °C$ $T_{A} = 25 °C$ $T_{A} = 70 °C$	- P <sub>D</sub>	2.7 1.77 1.78 <sup>b, c</sup> 1.14 <sup>b, c</sup>	W		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient <sup>a, c, d</sup>	t ≤ 10 s	R <sub>thJA</sub>	58	70	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	38	45	0/11		

Notes:

a. Package limited,  $T_C = 25$  °C.

b. Surface Mounted on 1" x 1" FR4 board.

c. t = 10 s.

d. Maximum under Steady State conditions is 110 °C/W.

Parameter

**SPECIFICATIONS** T<sub>J</sub> = 25 °C, unless otherwise noted

Symbol

			MWW.V	<sup>®</sup> VBsei ∕Bsemi.c
ed				
Test Conditions	Min.	Тур.	Max.	Unit
	•	•		•
<sub>GS</sub> = 0 V, I <sub>D</sub> = 250 µA	30			V
1 050 1		32		
I <sub>D</sub> = 250 μA		- 5.0		mV/°C
<sub>OS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	1.0		2.5	V
= -0.1/1/(1 - 1 - 1)/(1 - 1)		1	. 100	~ ^

	-,						
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$	30			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	L _ 250 uA		32			
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA		- 5.0		mV/°C	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	1.0		2.5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zene Cete Maltere Drein Current		$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 \text{ °C}$			10	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	10			А	
	Р	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A		0.022			
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 4 \text{ A}$		0.026		Ω	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 5 \text{ A}$		16		S	
Dynamic <sup>b</sup>				•	•		
Input Capacitance	C <sub>iss</sub>			586			
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ = 15 V, $V_{GS}$ = 0 V, f = 1 MHz		117		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			55			
Total Gate Charge	Qg	$V_{DS}$ = 15 V, $V_{GS}$ = 10 V, $I_{D}$ = 5 A		15		nC	
Iotal Gate Charge				3.7	5.6		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = 15 V, $V_{GS}$ = 4.5 V, $I_{D}$ = 5 A		1.4			
Gate-Drain Charge	Q <sub>gd</sub>			1.05			
Gate Resistance	Rg	f = 1 MHz	0.8	4.3	8.6	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			12	24		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 3 $\Omega$		55	100		
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_\text{D}\cong 5$ A, $\text{V}_\text{GEN}$ = 4.5 V, $\text{R}_\text{g}$ = 1 $\Omega$		11	22		
Fall Time	t <sub>f</sub>			8	16		
Turn-On Delay Time	t <sub>d(on)</sub>			4	8	ns	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 3 $\Omega$		9	18	-	
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_\text{D}\cong \text{5}$ A, $\text{V}_\text{GEN}$ = 10 V, $\text{R}_\text{g}$ = 1 $\Omega$		10	20		
Fall Time	t <sub>f</sub>			6	12		
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	۱ <sub>S</sub>	T <sub>C</sub> = 25 °C			2.25	А	
Pulse Diode Forward Current	I <sub>SM</sub>				24		
Body Diode Voltage	V <sub>SD</sub>	$I_{S} = 2 \text{ A}, V_{GS} = 0 \text{ V}$		0.8	1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			11	20	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 5 A, dl/dt = 100 A/μs, T <sub>J</sub> = 25 °C		4	8	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	$r_{\rm F} = 0.7$ , and $= 100.7$ ( $\mu$ s, $r_{\rm J} = 20.0$		7			
Reverse Recovery Rise Time	t <sub>b</sub>			4		ns	

Notes:

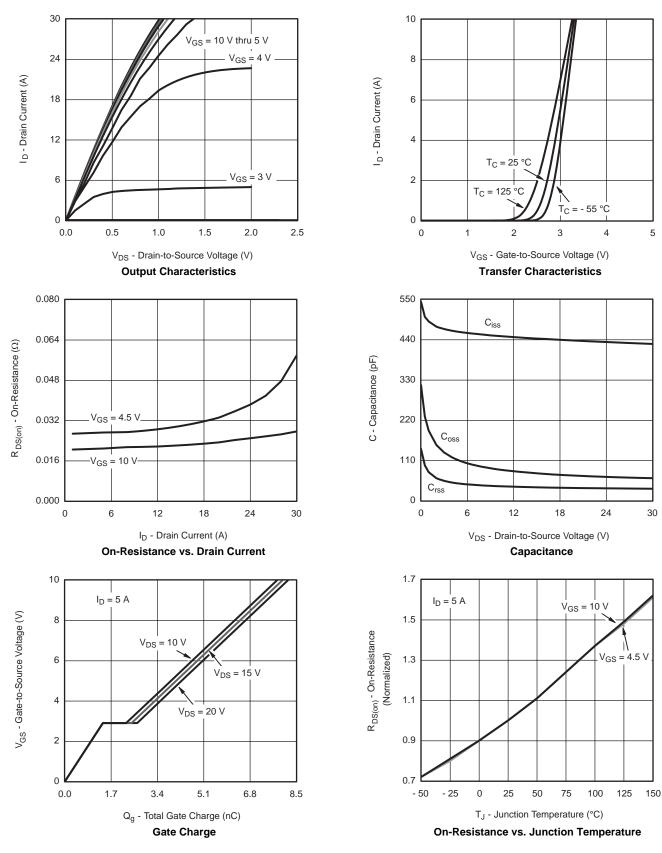
a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.









 $I_D = 5 A$ 

T<sub>J</sub> = 125 °C

T<sub>J</sub> = 25 °C

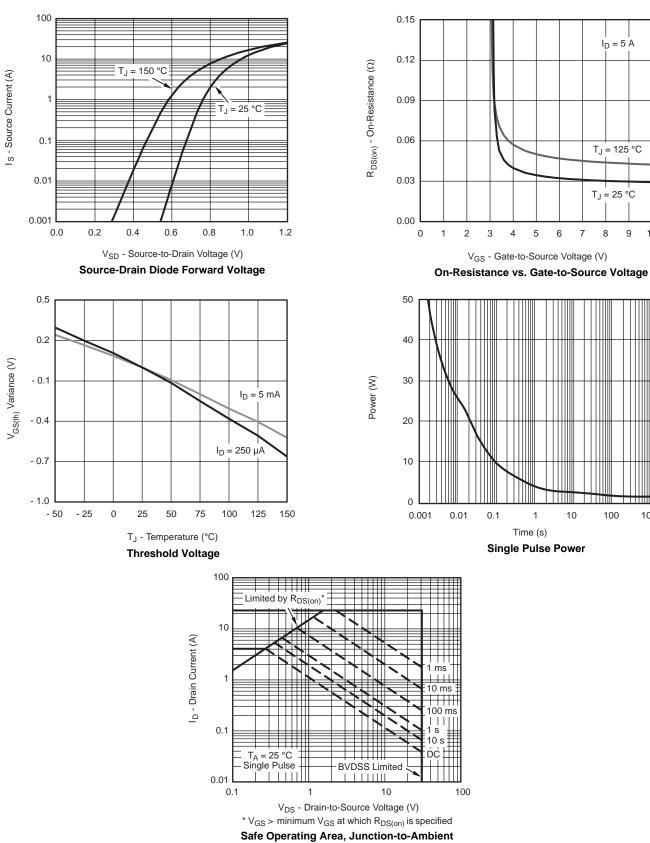
8 9 10

100

1000

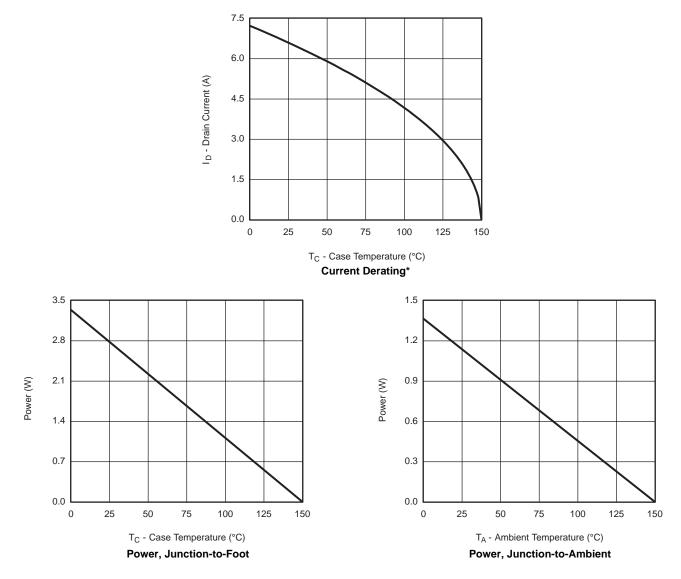
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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





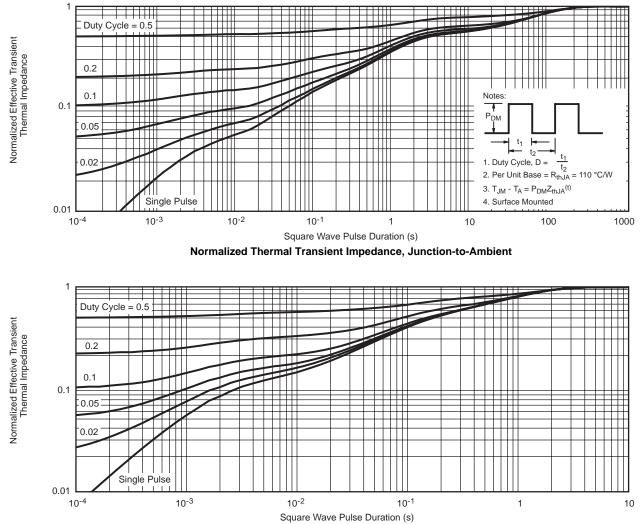
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



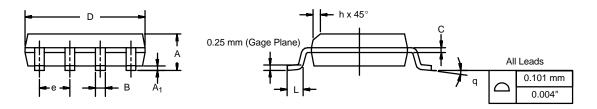
Normalized Thermal Transient Impedance, Junction-to-Foot



#### SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012





	MILLIMETERS		INC	HES		
DIM	Min	Max	Min	Max		
A	1.35	1.75	0.053	0.069		
A <sub>1</sub>	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
E	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050 BSC			
н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498						



**RECOMMENDED MINIMUM PADS FOR SO-8** 



Recommended Minimum Pads Dimensions in Inches/(mm)



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