

## **Description**

The UPA2815T1S uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V.This device is suitable for use as a Battery protection or in other Switching application.

### **General Features**

 $V_{DS} = -20V I_{D} = -48A$ 

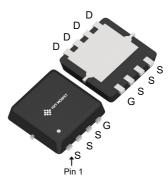
 $R_{DS(ON)}$  < 10 m $\Omega$  @  $V_{GS}$ =-4.5 V

## **Application**

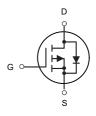
**Battery protection** 

Load switch

Uninterruptible power supply



DFN3X3-8L (HWSON-8(3.3x3.3))



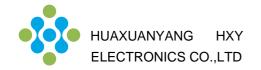
P-Channel MOSFET

**Package Marking and Ordering Information** 

Product ID	Pack	Brand	Qty(PCS)
UPA2815T1S	DFN3X3-8L (HWSON-8(3.3x3.3))	HXY MOSFET	5000

# Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	-20	V
Vgs	Gate-Source Voltage	±8	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ -4.5V <sup>1</sup>	-48	А
I <sub>D</sub> @T <sub>C</sub> =70°C	Continuous Drain Current, V <sub>GS</sub> @ -4.5V <sup>1</sup>	-35	А
IDM	Pulsed Drain Current <sup>2</sup>	-100	А
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>3</sup>	29	W
P <sub>D</sub> @T <sub>C</sub> =70°C	Total Power Dissipation <sup>3</sup>	19	W
Тѕтс	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
R <sub>0</sub> JA	Thermal Resistance Junction-Ambient <sup>1</sup>	75	°C/W
R <sub>0</sub> JA	Thermal Resistance Junction-Ambient ¹ (t ≤10s)	40	°C/W
Rejc	Thermal Resistance Junction-Case <sup>1</sup>	4.2	°C/W



# Electrical Characteristics (TA=25 °C unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA	-20	-24		V
BVDSS/	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =-1mA		-0.012		V/°C
i   I J		V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-10A		7.5	10	
RDS(ON)	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}$ =-2.5V , $I_D$ =-8A		8.7	11.5	mΩ
		V <sub>GS</sub> =-1.8V , I <sub>D</sub> =-6A		13	15	
V <sub>G</sub> S(th)	Gate Threshold Voltage		-0.3	0.6	-1.0	V
₹ V <sub>GS(th)</sub>	V <sub>GS(th)</sub> Temperature Coefficient	$V_{GS}=V_{DS}$ , $I_D=-250uA$		2.94		mV/°C
IDSS	Drain-Source Leakage Current	V <sub>DS</sub> =-20V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	uA
Igss	Gate-Source Leakage Current	V <sub>GS</sub> =±8V , V <sub>DS</sub> =0V			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =-5V , I <sub>D</sub> =-10A		43		S
Qg	Total Gate Charge (-4.5V)			63		
Qgs	Gate-Source Charge	V <sub>DS</sub> =-15V , V <sub>GS</sub> =-4.5V ,		9.1		nC
Qgd	Gate-Drain Charge	ID IUA		13		
Td(on)	Turn-On Delay Time			15.8		
Tr	Rise Time	V <sub>DD</sub> =-10V , V <sub>GS</sub> =-4.5V ,		76.8		
Td(off)	Turn-Off Delay Time	R <sub>G</sub> =3.3 , I <sub>D</sub> =-10A		193		ns
Tf	Fall Time			186.4		
Ciss	Input Capacitance			5783		
Coss	Output Capacitance	V <sub>DS</sub> =-15V , V <sub>GS</sub> =0V , f=1MHz		509		pF
Crss	Reverse Transfer Capacitance			431		
Is	Continuous Source Current <sup>1,4</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			-10.7	Α
Іѕм	Pulsed Source Current <sup>2,4</sup>				-60	Α
Vsp	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =-1A , T <sub>J</sub> =25°C			-1.2	V
trr	Reverse Recovery Time	IF=-10A , dI/dt=100A/μs ,		27		nS
Qrr	Reverse Recovery Charge	T <sub>J</sub> =25°C		17.8		nC

#### Note

<sup>1.</sup>The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.

<sup>2.</sup>The data tested by pulsed , pulse width  $\leqq 300 us$  , duty cycle  $\leqq 2\%$ 

<sup>3.</sup>The power dissipation is limited by 150°C junction temperature

<sup>4.</sup> The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.



## **Typical Characteristics**

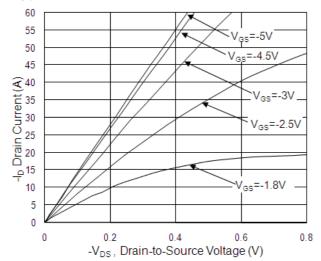


Fig.1 Typical Output Characteristics

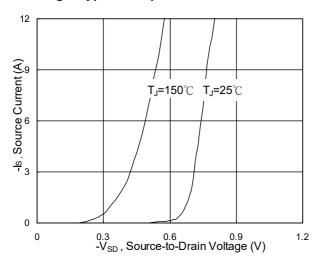


Fig.3 Forward Characteristics of Reverse

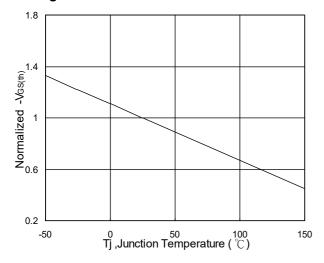


Fig.5 Normalized V<sub>GS(th)</sub> vs. T<sub>J</sub>

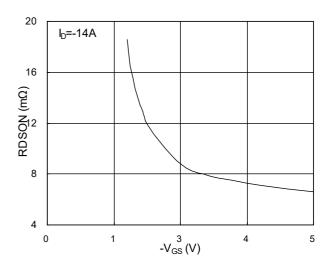


Fig.2 On-Resistance vs. G-S Voltage

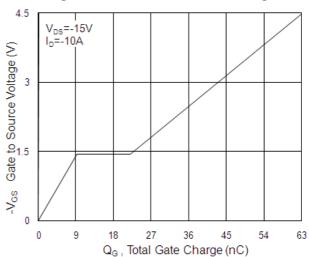


Fig.4 Gate-charge Characteristics

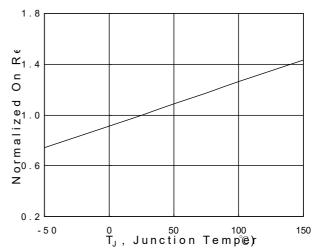
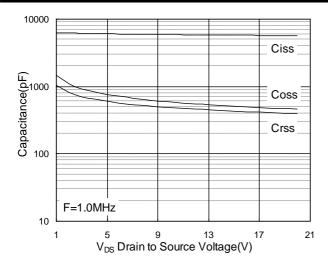


Fig.6 Normalized RDSON vs. TJ





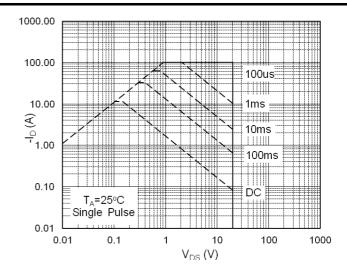


Fig.7 Capacitance

Fig.8 Safe Operating Area

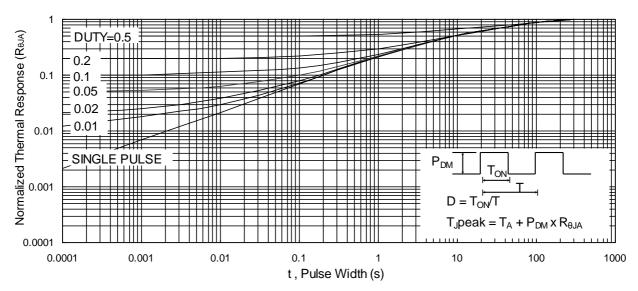
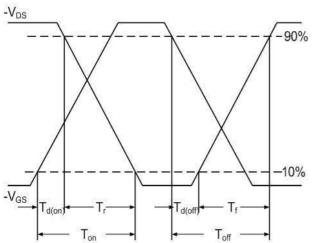


Fig.9 Normalized Maximum Transient Thermal Impedance





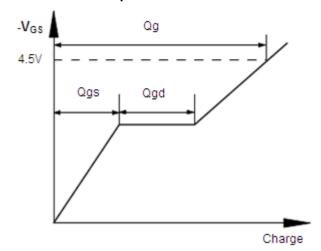
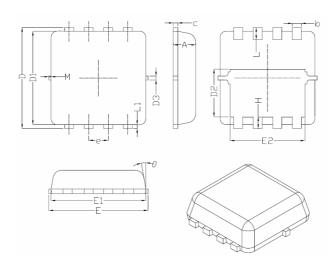


Fig.11 Gate Charge Waveform



# DFN3X3-8L(HWSON-8(3.3x3.3)) Package Information



Q.,bl	Dimensions In Millimeters			
Symbol	Min.	Nom.	Max.	
А	0.70	0.75	0.80	
b	0.25	0.30	0.35	
С	0.10	0.15	0.25	
D	3.25	3.35	3.45	
D1	3.00	3.10	3.20	
D2	1.48	1.58	1.68	
D3	-	0.13	-	
E	3.20	3.30	3.40	
E1	3.00	3.15	3.20	
E2	2.39	2.49	2.59	
е	0.65BSC			
Н	0.30	0.39	0.50	
L	0.30	0.40	0.50	
L1	-	0.13	-	
M	*	*	0.15	
θ		10°	12 <sup>°</sup>	



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