General Description

The WSR135N15 is the highest performance trench N-Ch MOSFET with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSR135N15 meet the RoHS and Green Product requirement,100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

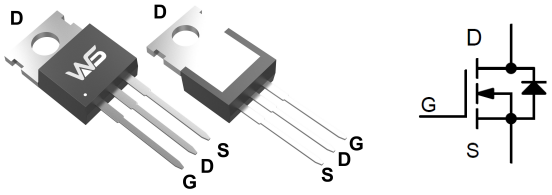
Product Summery

BV_{DSS}	R_{DSON}	I_D
150V	9.5mΩ	135A

Applications

- Power Management in TV Converter.
- DC-DC Converter
- LED TV Back Light

TO-220AB Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	150	V
V_{GS}	Gate-Source Voltage	±20	V
I_D@T_C=25℃	Continuous Drain Current, V_{GS} @ 10V[1]	135	A
I_D@T_C=100℃	Continuous Drain Current, V_{GS} @ 10V[1]	80	A
I_{DM}	Pulsed Drain Current[2],T_C=25°C	360	A
EAS	Avalanche Energy, Single pulse,L=0.5mH	406	mJ
I_{AS}	Avalanche Current, Single pulse,L=0.5mH	43	A
P_D@T_C=25℃	Total Power Dissipation[4]	160	W
P_D@T_C=100℃	Total Power Dissipation[4]	75	W
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	150	℃

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient [1]	---	62	℃/W
$R_{\theta JC}$	Thermal Resistance Junction-Case[1]	---	0.78	℃/W

Electrical Characteristics (T_J=25℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	150	---	---	V
△BV_{DSS}/△T_J	BV_{DSS} Temperature Coefficient	Reference to 25℃ , I_D=1mA	---	0.096	---	V/℃
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=10V , I_D=30A	---	9.5	12	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =250uA	2.5	3.5	4.5	V
△$V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-5.5	---	mV/℃
I_{DSS}	Drain-Source Leakage Current	V_{DS}=100V , V_{GS}=0V , T_J=25℃	---	---	1	uA
		V_{DS}=100V , V_{GS}=0V , T_J=55℃	---	---	100	
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA
gfs	Forward Transconductance	V_{DS}=5V , I_D=30A	---	40	---	S
R_g	Gate Resistance	V_{DS}=0V , V_{GS}=0V , f=1MHz	---	1.7	2.8	Ω
Q_g	Total Gate Charge (10V)	V_{DS}=80V , V_{GS}=10V , I_D=40A	---	66	---	nC
Q_{gs}	Gate-Source Charge		---	26	---	
Q_{gd}	Gate-Drain Charge		---	17	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DD}=80V , V_{GS}=10V , R_G=2Ω, I_D=40A	---	36	---	ns
T_r	Rise Time		---	95	---	
$T_{d(off)}$	Turn-Off Delay Time		---	56	---	
T_f	Fall Time		---	11	---	
C_{iss}	Input Capacitance	V_{DS}=25V , V_{GS}=0V , f=1MHz	---	5460	---	pF
C_{oss}	Output Capacitance		---	1711	---	
C_{rss}	Reverse Transfer Capacitance		---	189	---	

Guaranteed Avalanche Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
EAS	Single Pulse Avalanche Energy[5]	V_{DD}=25V , L=0.5mH , I_{AS}=43A	200	---	---	mJ

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current[1,6]	V_G=V_D=0V , Force Current	---	---	120	A
I_{SM}	Pulsed Source Current[2,6]		---	---	406	A
V_{SD}	Diode Forward Voltage[2]	V_{GS}=0V , I_S=20A , T_J=25℃	---	---	1.3	V
t_{rr}	Reverse Recovery Time	I_F=40A,dI/dt=100A/μs,T_J=25℃	---	76	---	nS
Q_{rr}	Reverse Recovery Charge		---	285	---	nC

Note :
1.The data tested by surface mounted on a 1 inch^2 FR-4 board with 2OZ copper,t<10sec.
2.The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%
3.The EAS data shows Max. rating . The test condition is V_{DS}=25V,V_{GS}=10V,L=0.5mH,I_{AS}=43A
4.The power dissipation is limited by 150℃ junction temperature
5.The Min. value is 100% EAS tested guarantee.
6.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Operating Characteristics

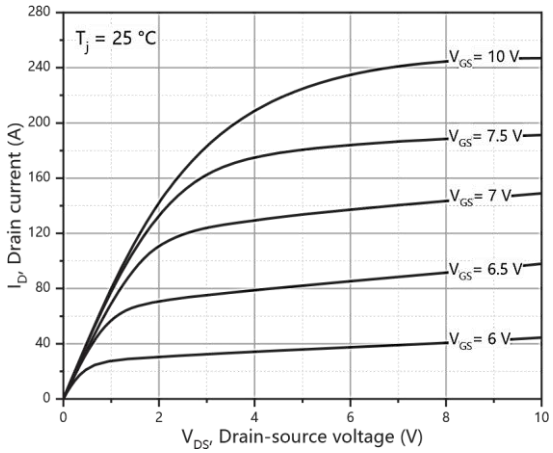


Figure 1. Type. output characteristics

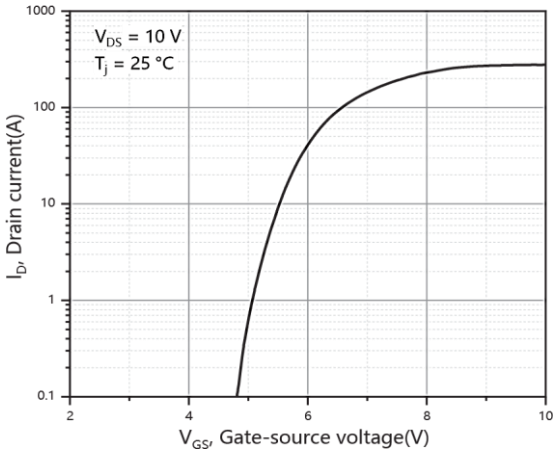


Figure 2. Type. transfer characteristics

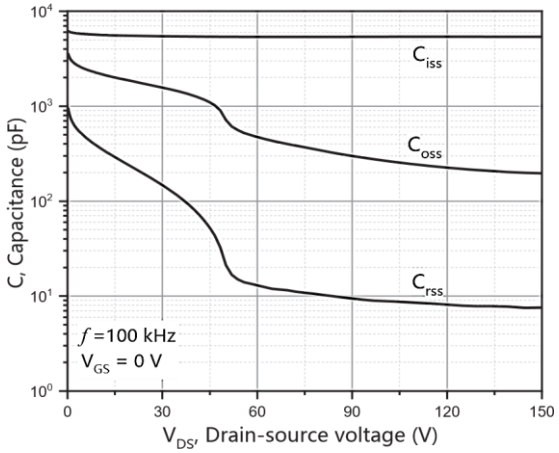


Figure 3. Type. capacitances

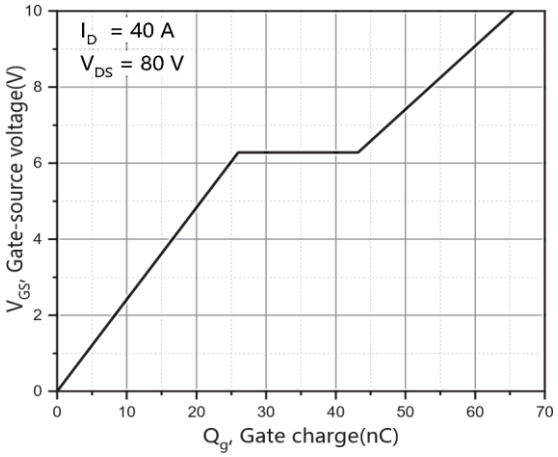


Figure 4. Type. gate charge

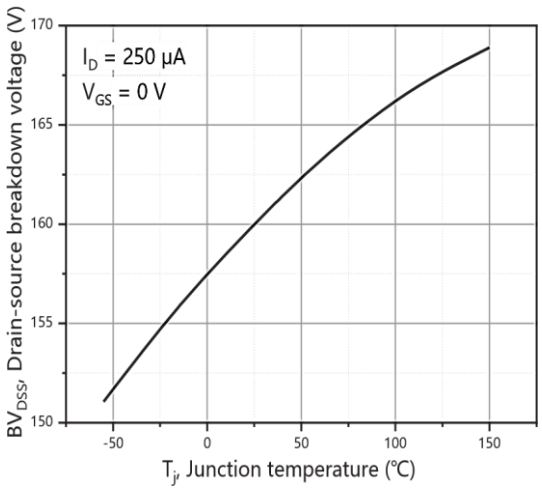


Figure 5. Drain-source breakdown voltage

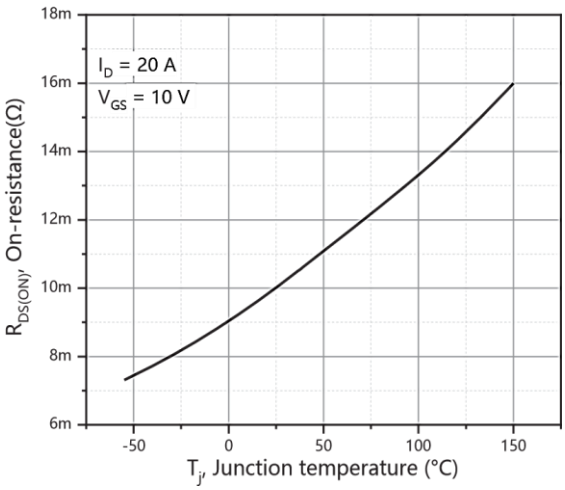


Figure 6. Drain-source on-state resistance

Typical Operating Characteristics (Cont.)

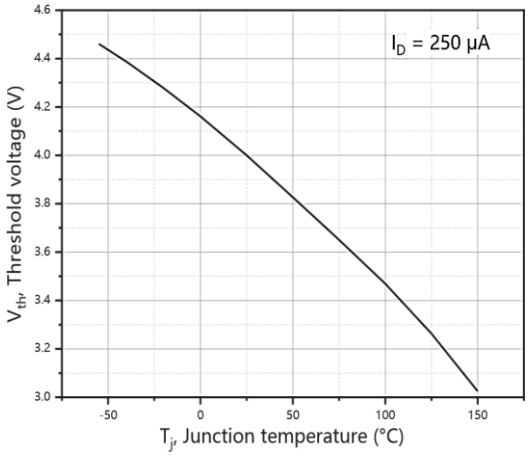


Figure 7. Threshold voltage

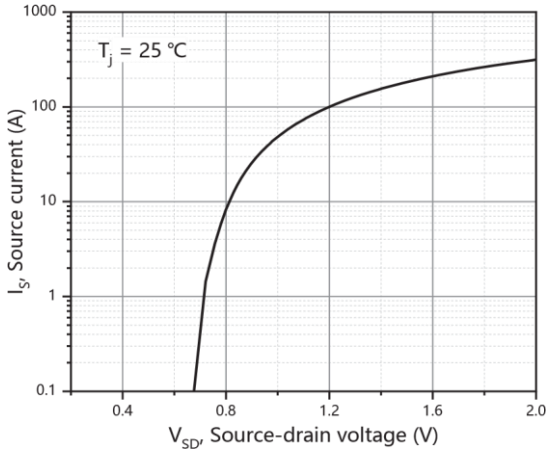


Figure 8. Forward characteristic of body diode

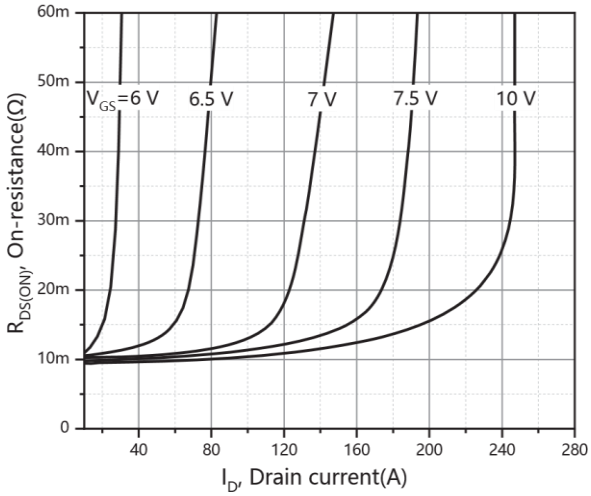


Figure 9. Drain-source on-state resistance

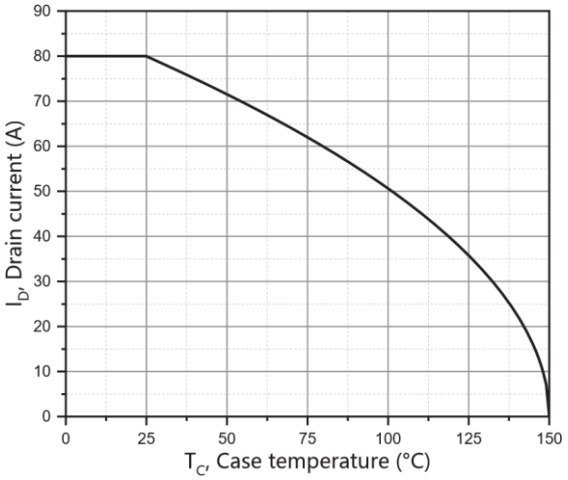


Figure 10. Drain current

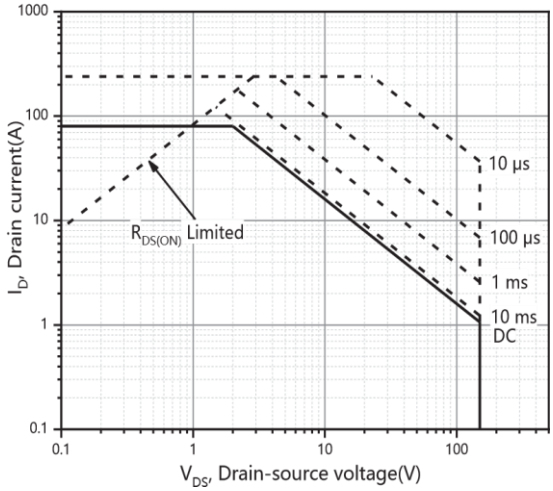


Figure 11. Safe operation area T_C=25℃

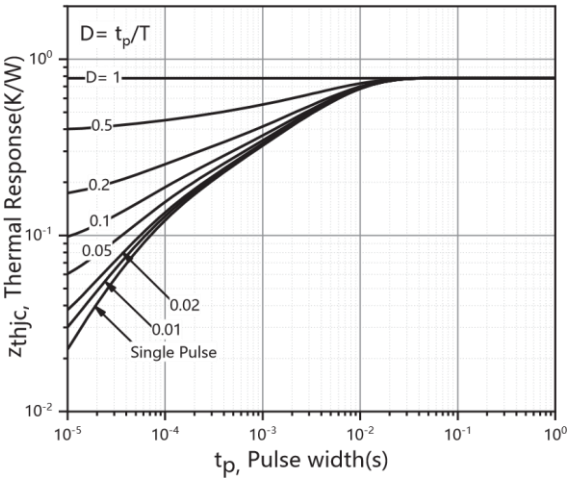
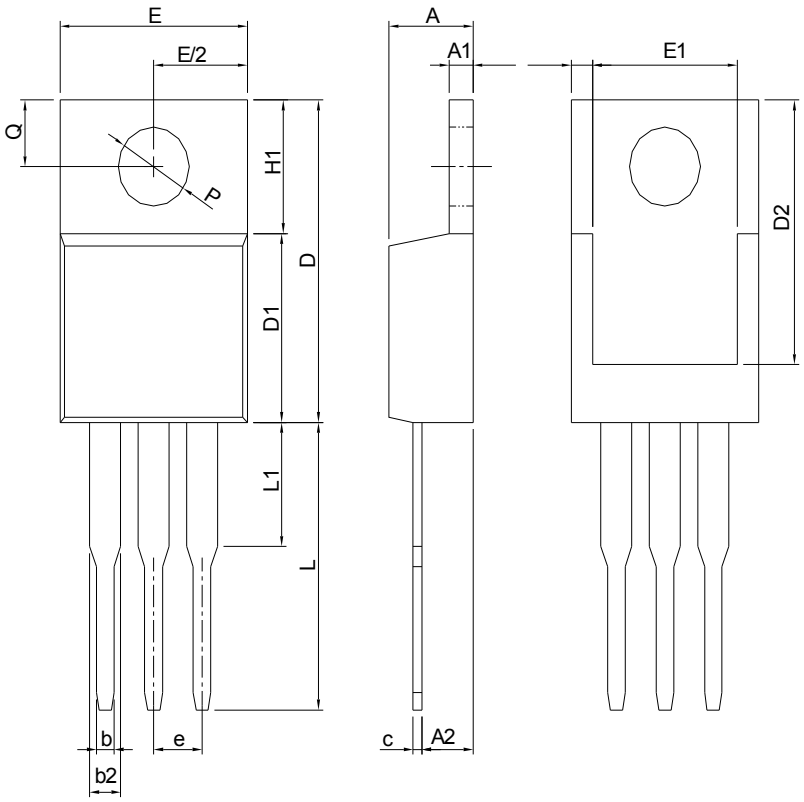


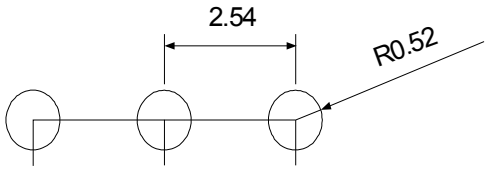
Figure 12. Max. transient thermal impedance

Package Information TO-220AB



SYMBOL	TO-220			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	3.56	4.83	0.140	0.190
A1	0.51	1.40	0.020	0.055
A2	2.03	2.92	0.080	0.115
b	0.38	1.02	0.015	0.040
b2	1.14	1.78	0.045	0.070
c	0.36	0.61	0.014	0.024
D	14.22	16.51	0.560	0.650
D1	8.38	9.02	0.330	0.355
D2	12.19	13.65	0.480	0.537
E	9.65	10.67	0.380	0.420
E1	6.86	8.89	0.270	0.350
e	2.54 BSC		0.100 BSC	
H1	5.84	6.86	0.230	0.270
L	12.70	14.73	0.500	0.580
L1		6.35		0.250
P	3.53	4.09	0.139	0.161
Q	2.54	3.43	0.100	0.135

RECOMMENDED LAND PATTERN



UNIT: mm

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