

200W Baseplate cooled



The RDF200 Series is a range of low profile, baseplate cooled DC-DC brick converters that delivers 200W and offers single output voltages ranging from 12V to 48VDC. The RDF200 series offers an ultra-wide 12:1 input range of 14 to 160VDC, which covers standard industrial voltages and meets all requirements of the EN50155 transportation standard. Baseplate cooling enables effective thermal management which ensures elevated levels of reliability.

With world-wide industrial safety approvals and compliance to transportation standards, high efficiency, high reliability, 3kVAC reinforced isolation, remote On/Off and wide output trimming, the RDF200 series benefits system designers with easy integration into a wide range of applications including; renewable energy, battery systems, autonomous equipment, factory automation and harsh environment railway applications.



Features

- ► Single voltage outputs from 12V to 48VDC
- ▶ Wide output voltage trim and remote sense
- ▶ 12:1 ultra-wide input range 14 to 160VDC
- Industry standard half brick format
- ► High efficiency, up to 90%
- ▶ 3kVAC reinforced input to output isolation
- ▶ ITE safety approvals and EN50155 compliance
- ▶ Remote On/Off with low 15mA stand-by current
- Overvoltage, overload, and short circuit protection
- -40°C to +100°C operating case temperature
- 3 year warranty

Applications







Industrial Electronics & Robotics



Railway

Dimensions

61.0 x 57.9 x 12.7 mm (2.40" x 2.28" x 0.50" in)

More resources

Click the link or scan the code





Models & ratings

Model number	land the second	Outmut assument	Input current ⁽⁶⁾		Ripple & noise(1)	Maximum	F45 siam as (2)	
Model number	Input voltage	Output voltage	Output current	No load	Full load	Rippie α noise	capacitive load	Efficiency ⁽²⁾
RDF20072S12		12V	16.7A	50mA		200mV	16700μF	89%
RDF20072S15	72VDC	15V	13.5A			200mV	13500µF	89%
RDF20072S24	(14-160VDC)	24V	8.4A	(15mA in inhibit mode)	3.2A	240mV	8000µF	88%
RDF20072S48	1	48V	4.2A			240mV	2200µF	89%

Notes:

- 1. Measured at 20MHz bandwidth and $1\mu F$ ceramic & $10\mu F$ tantalum capacitors (for 48V, $10\mu F$ electrolytic and 0.1 ceramic capacitor) at 72VDC input and full load.
- 2. Measured at nominal 72VDC input.
- 3. Optional M3 x 0.5 threaded baseplate fixing add suffix -T.

- 4. Recommended input capacitance of $100\mu F$ required to reduce input ripple voltage at $-40^{\circ}C$ operation. See application notes.
- 5. Add suffix -N for negative logic control.
- 6. Typical at 72VDC input.



Input

Characteristic	Minimum	Typical	Maximum	Units	Notes & conditions		
Input voltage range	14		160	VDC	24/48/72/110VDC nominal inputs. Derate output power to 75% with input below 16.5VDC		
Input surge			180	VDC	For 100ms		
		On: >12.5		VDC	On		
Undervoltage lockout		Off: <11.5		VDC	Off		
Hold up	See applicat	See application notes					
Lockout hysteresis		2		VDC			
Idle current		15		mA	When output is inhibited		
Inrush current			0.1	A²s			
Input reflected ripple current		50		mA pk-pk	Through 12µH inductor 5Hz to 20MHz at nominal input		
Recommended input fuse	25A time del	ay					
Input filter	Pi type						

Output

Characteristic	Minimum	Typical	Maximum	Units	Notes & conditions	
Output voltage	12		48	VDC	See Models & ratings	
Output trim	-20		+15	%	See Application notes	
Initial set accuracy			±1.0	%	At full load and 72VDC nominal input	
Minimum load	No minimum	load required				
Line regulation			±0.2	%	From minimum to maximum input at full load	
Load regulation			±0.2	%	From 0% to full load	
Transient response			±5.0	%	Maximum deviation, recovering to less than 1% in 250µs for 25% step	
Transient response			±5.0	70	load change	
Start up delay		100		ms		
Output voltage rise time		100		ms		
Ripple & noise				mV pk-pk	See Models & ratings	
Overload protection	110	125	140	%	With nominal output voltage	
Short circuit protection	Continuous h	niccup mode, w	ith autorecover	у		
Maximum capacitive load	See Models	& Ratings table				
Temperature coefficient			±0.02	%/°C	Tc -40°C to +100°C	
Overvoltage protection	115	125	140	%	Of nominal output voltage	
Remote On/Off	Output is on if remote On/Off (pin 4) is open or high (3.5-160VDC), positive logic. Output turns off if remote On/Off (pin 4) is low (0-1.2VDC max.) Positive logic default, for negative logic option add -N suffix to part number. On/Off current typ. 0.3mA to max 1mA					





General

Characteristic	Minimum	Typical	Maximum	Units	Notes & conditions			
Efficiency		90		%	See Models & ratings table			
Isolation: Input to output	3000			VAC	60s, reinforced			
Isolation: Input to case	3000			VAC	60s, basic			
Isolation: Output to case	500			VAC	60s, basic			
Isolation resistance	100			MΩ				
Isolation capacitance		500		pF	Input to output			
Switching frequency	432	480	528	kHz	Fixed. Sync pin option (please ask for application support)			
Power density			185.4	W/cm ³				
Manage disease in advances of all times	450			Laborro	12/15V MIL-HDBK-217F, +25°C GB			
Mean time between failure	560			khrs	24/48V MIL-HDBK-217F, +25°C GB			
Weight		105.0 (0.23)		g (lb)				
Case material	Plastic DAP	UL94V-0 rated v	vith aluminum b	paseplate				
Potting material	Epoxy UL94	V-0						
Pin material	Copper with	nickel and matt	e tin plate					
Solder profile	Wave solder	260°C max 10s	max 1.5mm fro	m case. With 9	00W iron 420°C for 15s max.			
Fire and smoke	Meets EN45	Meets EN45545-2						
Water washing	Use de-ionis	ed water, dry the	oroughly					

Environmental

Characteristic	Minimum	Typical	Maximum	Units	Notes & conditions
Operating base plate	-40		+100	°C	At 72VDC input
Storage temperature	-55		+125	°C	
Thermal protection		+105		°C	Measured on baseplate. Non latching. Recover <95°C
Humidity			95	%RH	Non-condensing
Cooling	Baseplate co	ooled			
Altitude			5000	m	Operating. Storage 12000m

EMC: emissions

Phenomenon	Standard	Test level	Notes & conditions
Conducted	EN50121-3-2	A	See Application notes
Radiated	EN50121-3-2	A	See Application notes

EMC: immunity

Phenomenon	Standard	Test level	Criteria	Notes & conditions
Deile en	EN50121-3-2			See Application notes
Railway equipment	EN50155			Railway
ESD immunity	EN61000-4-2	±6kV/±8kV	А	Contact discharge/Air discharge
Radiated immunity	EN61000-4-3	20V/m	А	
EFT/Burst	EN61000-4-4	2kV	А	External capacitor required, see Application notes
Surge	EN61000-4-5	±4kV/±2kV	А	External capacitor required, see Application notes
Conducted immunity	EN61000-4-6	10Vrms	A	

Safety approvals

Safety agency	Standard	Test level	Notes & conditions					
UL	UL62368-1		ITE					
CE	Meets all applicable directive	Meets all applicable directives						
UKCA	Meets all applicable legislation							





Application notes

Input fusing and safety considerations

The RDF200 series converters have no internal fuse. In order to achieve maximum safety and system protection, always use an input line fuse. We recommended a 25A time delay fuse. It is recommended that the circuit has a transient voltage suppressor diode (TVS) across the input terminals to protect the unit against surge or spike voltages and input reverse voltage (as shown). A suitable part would be 1.5 KE180 A Littlefuse.

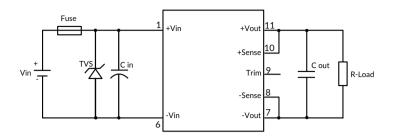
Output voltage adjustment

The Trim input permits the user to adjust the output voltage up by 20% or down by 15%. This is accomplished by connecting an external resistor between the Trim pin and either the +Sense pin or the -Sense pin.

To trim down

Connecting an external resistor (Rd) between the Trim pin and the +Sense pin decreases the output voltage. The following table can be used to determine the required external resistor value to obtain a percentage output voltage change of $\Delta\%$.

Trim down	12V	15V	24V	48V			
%	Rd (kΩ)						
1	389.67	573.80	929.51	1715.62			
2	186.43	276.35	447.25	821.86			
3	118.69	177.20	286.50	523.94			
4	84.82	127.62	206.13	374.98			
5	64.49	97.88	157.90	285.60			
6	50.94	78.05	125.75	226.02			
7	41.27	63.89	102.79	183.46			
8	34.01	53.26	85.56	151.54			
9	28.36	45.00	72.17	126.71			
10	23.85	38.39	61.45	106.85			
11	20.15	32.98	52.68	90.60			
12	17.07	28.47	45.38	77.06			
13	14.47	24.66	39.19	65.60			
14	12.23	21.39	33.89	55.78			
15	10.30	18.56	29.30	47.27			
16	8.60	16.08	25.28	39.82			
17	7.11	13.89	21.74	33.25			
18	5.78	11.95	18.58	27.41			
19	4.59	10.21	15.76	22.18			
20	3.52	8.64	13.23	17.48			



Output voltage sensing

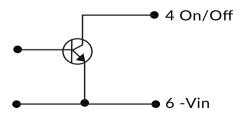
The module will automatically trim the output voltage via the sense pins to the default values either locally or at the load. If not required, the sense pins should be connected locally as indicated in the example circuit.

To trim up

Connecting an external resistor (Ru) between the Trim pin and the -Sense pin increases the output voltage. The following table can be used to determine the required external resistor value to obtain a percentage output voltage change of $\Delta\%$.

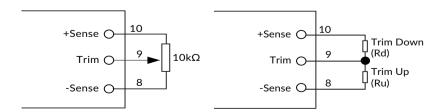
	40)/	45)/	0.07	40) (
Trim up	12V	15V	24V	48V					
%		Ru (kΩ)							
1	263.17	302.45	515.62	1040.52					
2	126.59	145.22	247.81	498.66					
3	81.06	92.82	158.54	318.04					
4	58.29	66.61	113.91	227.73					
5	44.63	50.89	87.12	173.54					
6	35.53	40.41	69.27	37.42					
7	29.02	32.92	56.52	111.62					
8	24.15	27.31	46.95	92.27					
9	20.35	22.94	39.51	77.21					
10	17.32	19.44	33.56	65.17					
11	14.83	16.59	28.69	55.32					
12	12.76	14.20	24.64	47.11					
13	11.01	12.19	21.20	40.16					
14	9.51	10.46	18.26	34.21					
15	8.21	8.96	15.71	29.05					

Remote On/Off control



Positive logic: "On" if pin 4 is high >3.5V to 160VDC "Off" if pin 4 is low <1.2V to 0VDC

External trim



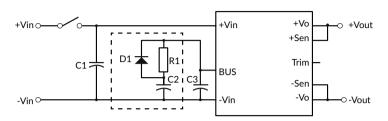




Application notes

Hold up

To enable hold up functionality the BUS pin can be used as outlined in the following typical circuit where C2 energy is used to maintain the module output.



C2	Nominal input voltages								
C2	24V	36V	48V	72V	96V	110V			
For 10ms	2400µF	2400µF	2400µF	2400µF	820µF	560µF			
For 30ms	7200µF	7200µF	7200µF	7200µF	2460µF	1680µF			

D1:200V10A

R1: 3mΩ 1W

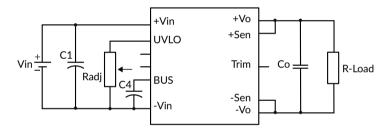
If the hold up function is not required use only C3

Suggested components:

C1 : $100\mu F$ 200V ESR <0.047 Ω C3 : $240\mu F$ (such as Vishay 118AHT)

Adjustable under voltage lockout

The module has default under voltage lockout feature. This can be adjusted by using the following typical circuit:



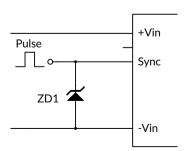
	Nominal input voltages						
	24V	110V					
Turn Off threshold (VDC)	11.0 ±0.5	20.0 ±1.0	27.3 ±1.0	41.6 ±1.0	53.0 ±1.0		
Turn On threshold (VDC)	13.0 ±0.5	22.0 ±1.0	29.6 ±1.0	44.6 ±1.0	58.0 ±1.0		
Radj resistor (KΩ) (UVLO to -Vin)	Open	62	34	18	10		

Suggested components:

C1 : $100\mu F$ 200V ESR <0.047 Ω C3 : $240\mu F$ (such as Vishay 118AHT)

Synchronized frequency

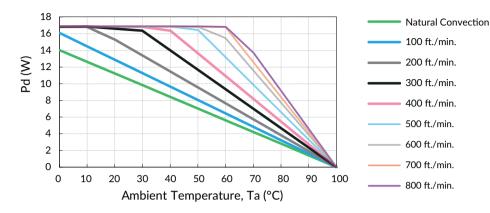
An extrenal clock can be used to synchronize the RDF200 by use of a narrrow pulse (75ns-120ns, 3.5-5VDC) applied to pin 3 "Sync". The applied signal should be between 530kHz and 630kHz and above the RDF200 switching frequency. Connect applied pulse in parallel with a 5.6V Zener diode as shown.





Application notes

Airflow derating graph



Air flow rate	Typical rca
7 III HOW Tate	Тургсагтса
Natural Convection 20ft/min (0.1m/s)	7.12°C/W
100ft/min (0.5m/s)	6.21°C/W
200ft/min (1.0m/s)	5.17°C/W
300ft/min (1.5m/s)	4.29°C/W
400ft/min (2.0m/s)	3.64°C/W
500ft/min (2.5m/s)	2.96°C/W
600ft/min (2.5m/s)	2.53°C/W
700ft/min (2.5m/s)	2.37°C/W
800ft/min (2.5m/s)	2.19°C/W

Example (Without heatsink)

To determine the minimum airflow necessary for a RDF20072WS24 operating at an input voltage of 72V, an output current of 6.25A, and a maximum ambient temperature of 20°C: Determine Power dissipation (Pd): Pd = Pi-Po = Po(1- η)/ η ,

Pd = 24V × 6.25A × (1-0.894)/0.894 = 17 Watts

Where Pi = Input power, Po = Output Power and η = Efficiency

Determine airflow from airflow derating graph using data points for Pd = 17W and Ta = $20^{\circ}C$

Minimum airflow= 400ft./min.

To check that the maximum case temp of 100°C is not exceeded:

Maximum temperature rise is

 $\Delta T = Pd \times Rca = 17 \times 3.64 = 62.$

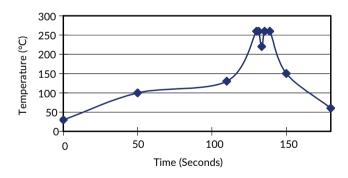
Maximum case temperature is

 $Tc = Ta + \Delta T = 82^{\circ}C < 100^{\circ}C.$

Where: Rca is the thermal resistance from case to ambient environment. Ta is ambient temperature and Tc is case temperature.

Solder profile

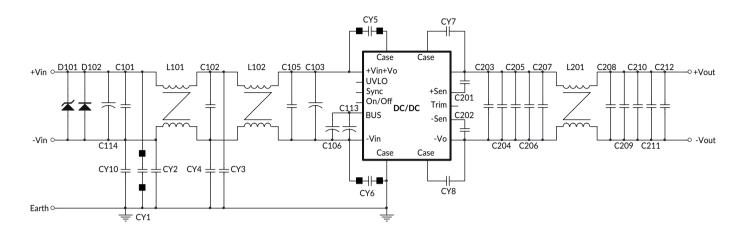
With iron 420 ±10°C for maximum 15s.





Application notes

EMC filter - Emissions and immunity



	RDF20072S12	RDF20072S15	RDF20072S24	RDF20072S48
C101, C102, C105	1μF/250V SMD			
C103, C113	220μF/200V aluminum cap. KXJ series			
C106	68μF/200V aluminum cap. CS series			
C114	120μF/220V aluminum cap. KXJ series			
C201, C202	0.1μF/100V SMD	0.068μF/50V SMD	0.1µF/100V SMD	
C203, 204	6.8µF/50V SMD		2.2µF/100V SMD	
C205-C207	10μF/50V SMD		2.2µF/100V SMD	
C208	0.1μF/100V SMD			
C209~C210	1μF/50V SMD			
C211, C212	6.8µF/50V SMD		2.2µF/100V SMD	
CY10	220pF/Y1	100pF/Y1		
CY1	100pF/Y1 220pF/Y1		220pF/Y1	
CY2	220pF/Y1	100pF/Y1		100pF/Y1
CY3, CY4	2200pF/Y1			
CY5, CY6	2200pF/Y1			
CY7, CY8	0.022μF/275Vac 10mm X2			
L101, L102	0.72mH 0.8mm*2/10T R-22/14/8B MA100-C ALWIN			
L201	0.12mH 0.7mm*8/2T FCN0179C WELL LIGHT 0.51mH 0.8mm*4		0.51mH 0.8mm*4/47	FCN0179C WELL LIGHT
BEAD CORE	CY5, CY6 BRI 4*1.5*2 CHILISIN (G4058651007), CY1 RDF20075S15 ONLY			

Notes:

C101, C102, C105: 1812 X7R ceramic.

C103, C113: NIPPON CHEMI-CON KXJ series aluminum capacitor or equivalent.

C106: Nichicon CS series aluminum capacitor or equivalent.

C201, C202: 0805 X7R ceramic.

C203, C204, C211, C212: 1812 X7R ceramic.

C205, C206, C207, C208, C209, C210: 1206 X7R ceramic. CY1, CY2M CY3, CY4, CY5, CY6, CY10: TDK Y1 capacitor or equivalent.

C114: NIPPON CHEMI-CON KXJ series aluminum capacitor or equivalent.

CY7, CY8: CARLI MPX Series X2 capacitor or equivalent.

L101, L102: 0.72mH 0.8mm*2/10T R-22/14/8B MA100-C ALWIN (G91CA125615)

L201: 0.12mH 0.7mm*8/2T, FCNO179C WELL LIGHT (G91C7425515)

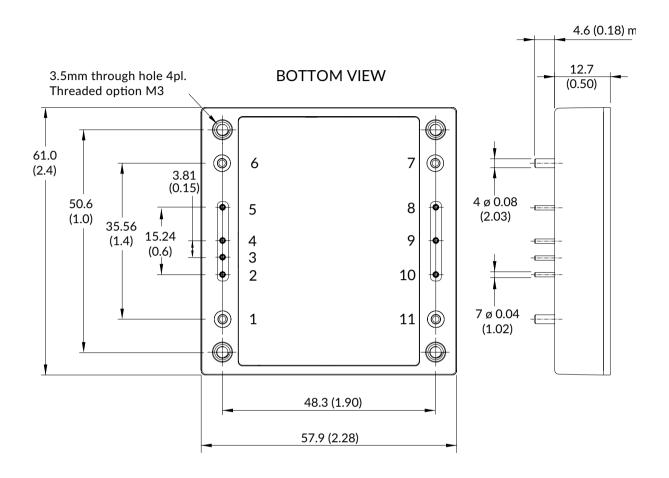
0.51mH 0.8mm*4/4T, FCNO179C WELL LIGHT (G91C7421915)

D101: SMCJ180A, LITTELFUSE D102: STTH8R03DJF-TR ST





Mechanical details



Pin connections			
Pin	Single		
1	+Vin		
2	UVLO		
3	Sync		
4	On/Off		
5	BUS		
6	-Vin		
7	-Vout		
8	-Sense		
9	Trim		
10	+Sense		
11	+Vout		

Notes:

- 1. All dimensions are in mm (inches)
- 2. Weight: 114g (0.25lbs) approx.
- 3. Tolerance: $x.xx = x.x = \pm 0.5$ (± 0.02), $x.xxx = x.xx = \pm 0.25$ (± 0.01)
- 4. Optional M3 x 0.5 threaded baseplate fixing add suffix -T
- 5. Mounting holes can be used as functional ground