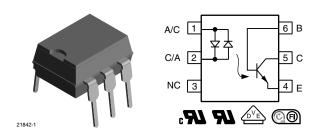


Optocoupler, Phototransistor Output, AC Input, With Base Connection



FEATURES

- · AC or polarity insensitive input
- Built-in reverse polarity input protection
- · I/O compatible with integrated circuits
- · Industry standard DIP package
- Isolation test voltage: 5300 V_{RMS}
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Telephone line detection
- AC line motor
- PLC
- Instrumentation

AGENCY APPROVALS

- <u>UL 1577</u>
- cUL
- DIN EN 60747-5-5 (VDE0884-5), available with option 1
- BSI
- FIMKO

LINKS TO ADDITIONAL RESOURCES





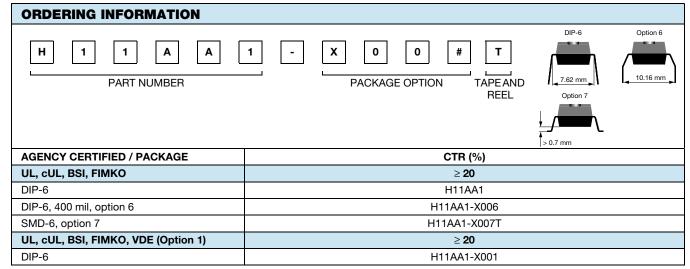






DESCRIPTION

The H11AA1 is a bi-directional input optically coupled isolator consisting of two inverse parallel gallium arsenide infrared LEDs coupled to a silicon NPN phototransistor in a 6 pin DIP package. The H11AA1 has a minimum CTR of 20 %, a CTR symmetry of 1:3 and is designed for applications requiring detection or monitoring of AC signals.



Notes

- · Additional options may be possible, please contact sales office
- (1) Also available in tubes; do not add T to end



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT							
Forward continuous current		I _F	± 60	mA			
Power dissipation		P _{diss}	100	mW			
Derate linearly from 25 °C			1.3	mW/°C			
OUTPUT	OUTPUT						
Power dissipation		P _{diss}	200	mW			
Derate linearly from 25 °C			2.6	mW/°C			
Collector emitter breakdown voltage		BV _{CEO}	30	V			
Emitter base breakdown voltage		BV _{EBO}	5	V			
Collector base breakdown voltage		BV _{CBO}	70	V			
COUPLER							
Storage temperature range		T _{stg}	-55 to +150	°C			
Operating temperature range		T _{amb}	-55 to +100	°C			
Lead soldering time at 260 °C		T _{sld}	10	s			

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
Forward voltage	$I_F = \pm 10 \text{ mA}$	V_{F}	-	1.2	1.5	V
OUTPUT						
Collector emitter breakdown voltage	$I_C = 1 \text{ mA}$	BV _{CEO}	30	-	-	V
Emitter base breakdown voltage	I _E = 100 μA	BV _{EBO}	5	-	-	V
Collector base breakdown voltage	$I_{C} = 100 \mu A$	BV _{CBO}	70	-	-	V
Collector emitter leakage current	V _{CE} = 10 V	I _{CEO}	-	5	100	nA
COUPLER						
Collector emitter saturation voltage	$I_F = \pm 10 \text{ mA}, I_C = 0.5 \text{ mA}$	V _{CEsat}	-	-	0.4	V

Note

Minimum and maximum values were tested requirements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
DC current transfer ratio	$I_F = \pm 10 \text{ mA}, V_{CE} = 10 \text{ V}$	CTR _{DC}	20	-	-	%
Symmetry (CTR at + 10 mA)/(CTR at - 10 mA)			0.33	1	3	

SAFETY AND INSULATION RATINGS					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Climatic classification	According to IEC 68 part 1		55 / 100 / 21		
Comparative tracking index		CTI	175		
Maximum rated withstanding isolation voltage	t = 1 min	V _{ISO}	4420	V_{RMS}	
Maximum transient isolation voltage		V _{IOTM}	10 000	V _{peak}	
Maximum repetitive peak isolation voltage		V _{IORM}	890	V _{peak}	
Isolation resistance	V _{IO} = 500 V, T _{amb} = 25 °C	R _{IO}	≥ 10 ¹²	Ω	
	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	≥ 10 ¹¹	Ω	
Output safety power		P _{SO}	400	mW	
Input safety current		I _{SI}	275	mA	
Safety temperature		T _S	175	°C	
Creepage distance			≥ 7	mm	
Clearance distance			≥ 7	mm	
Insulation thickness		DTI	≥ 0.4	mm	

Note

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

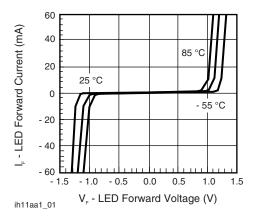


Fig. 1 - LED Forward Current vs.Forward Voltage

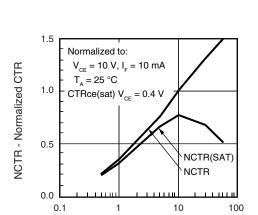


Fig. 2 - Normalized Non-Saturated and Saturated CTR vs. LED Current

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I_F - LED Current (mA)

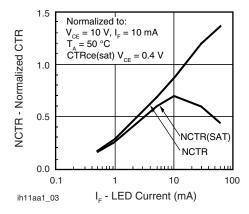


Fig. 3 - Normalized Non-Saturated and Saturated CTR vs. LED Current

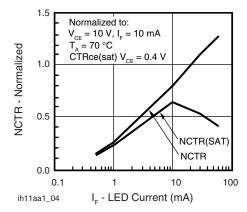


Fig. 4 - Normalized Non-Saturated and Saturated CTR vs. LED Current

As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits.





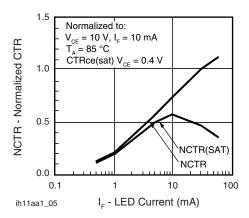


Fig. 5 - Normalized Non-Saturated and Saturated CTR vs. LED Current

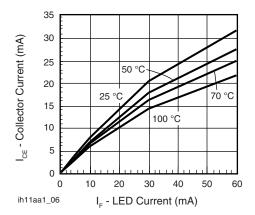


Fig. 6 - Collector Emitter Current vs. Temperature and LED Current

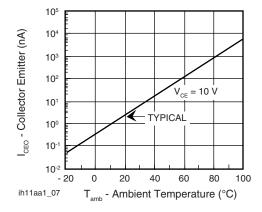


Fig. 7 - Collector Emitter Leakage Current vs. Temperature

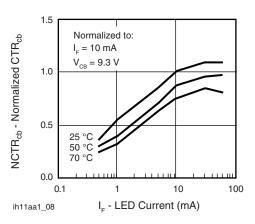


Fig. 8 - Normalized CTR_{cb} vs. LED Current and Temperature

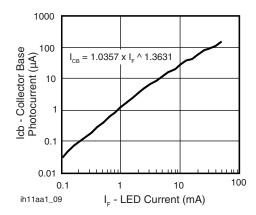


Fig. 9 - Collector Base Photocurrent vs. LED Current

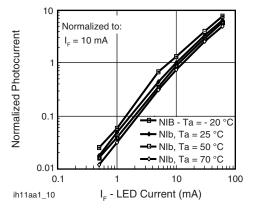


Fig. 10 - Normalized Photocurrent vs. LED Current



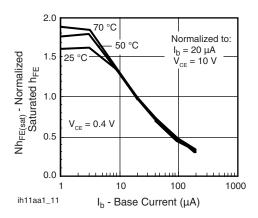


Fig. 11 - Normalized Saturated h_{FE} vs. Base Current and Temperature

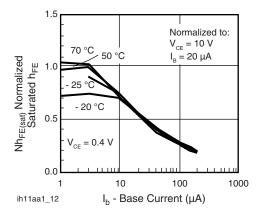


Fig. 12 - Normalized Saturated h_{FE} vs. Base Current and Temperature

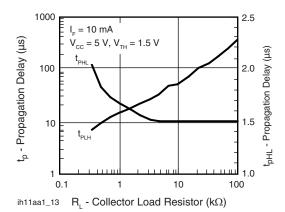
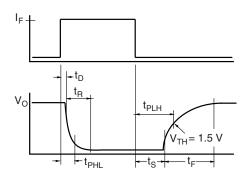
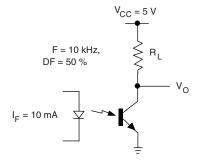


Fig. 13 - Propagation Delay vs. Collector Load Resistor



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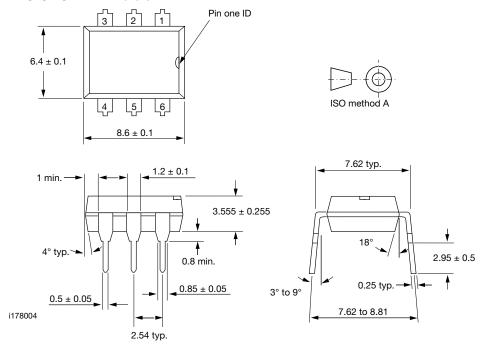
Fig. 14 - Switching Waveform

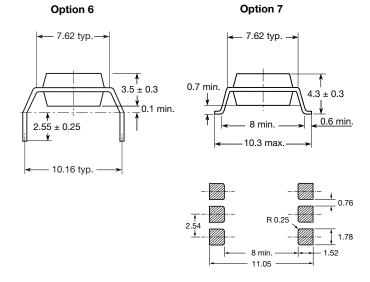


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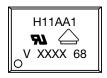
Fig. 15 - Switching Schematic

PACKAGE DIMENSIONS in millimeters





PACKAGE MARKING



Notes

- XXXX = LMC (lot marking code)
- Only options 1 and 7 are reflected in the package marking
- The VDE Logo is only marked on option1 parts
- Tape and reel suffix (T) is not part of the package marking



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