

General Description

The WSD3070DN is the highest performance trench N-ch MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The WSD3070DN meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

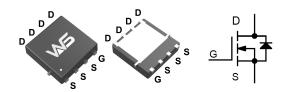
Product Summery

BVDSS	RDSON	ID
25V	2.5mΩ	70A

Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

DFN3.3X3.3 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter Rating		Units
V_{DS}	Drain-Source Voltage	25	V
V_{GS}	Gate-Source Voltage	±12	V
I _D @T _C =25℃	Continuous Drain Current, V _{GS} @ 10V ¹	70	Α
I _D @T _C =100℃	Continuous Drain Current, V _{GS} @ 10V ¹	55	А
I _D @T _A =25℃	Continuous Drain Current, V _{GS} @ 10V ¹	20	Α
I _D @T _A =70℃	Continuous Drain Current, V _{GS} @ 10V ¹	16	Α
I _{DM} @Tc=25℃	Pulsed Drain Current ²	200	А
EAS	Avalanche Energy ,Single Pulse (L=0.5mH) ³	100	mJ
I _{AS}	Avalanche Current ,Single pulse(L=0.5mH) ³	20	Α
P _D @T _C =25℃	Total Power Dissipation⁴	62.5	W
P _D @T _A =25℃	Total Power Dissipation⁴	1.78	W
T _{STG}	Storage Temperature Range -55 to 150		$^{\circ}$
TJ	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
$R_{ heta JA}$	Thermal Resistance Junction-Ambient ¹		70	°C/W
R _{eJC}	Thermal Resistance Junction-Case ¹		2.5	°C/W



Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	25			V
$\triangle BV_{DSS}/\triangle T_{J}$	BVDSS Temperature Coefficient	Reference to 25℃, I _D =1mA		0.028		V/°C
В	Static Drain-Source On-Resistance ²	V _{GS} =4.5V , I _D =20A		2.5	3.4	mΩ
$R_{DS(ON)}$		V _{GS} =2.5V , I _D =20A		3.0	4.0	
V _{GS(th)}	Gate Threshold Voltage	\\ -\\ -250\	0.5	0.8	1.1	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$V_{GS}=V_{DS}$, $I_D=250uA$		-6.16		mV/℃
	Drain-Source Leakage Current	V _{DS} =24V , V _{GS} =0V , T _J =25℃			1	
I _{DSS}		V _{DS} =24V , V _{GS} =0V , T _J =55℃			5	uA uA
I _{GSS}	Gate-Source Leakage Current	V_{GS} = $\pm 20 V$, V_{DS} = $0 V$			±100	nA
gfs	Forward Transconductance	V _{DS} =5V , I _D =40A		74		S
R _g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		0.85	1.1	Ω
Qg	Total Gate Charge (4.5V)			96	134	
Q_gs	Gate-Source Charge	VDS=15V, VGS=10V, IDS=20A		5.5	7.7	nC
Q_{gd}	Gate-Drain Charge			16	22	
$T_{d(on)}$	Turn-On Delay Time	VDD=15V, RL=15Ω ,		16.6	31	
T _r	Rise Time			12.2	24	
T _{d(off)}	Turn-Off Delay Time	IDS=1A, VGEN=10V,		135	244	ns
T _f	Fall Time	RG=6Ω		48	87	
C _{iss}	Input Capacitance	V _{DS} =15V , V _{GS} =0V , f=1MHz		4920		
C _{oss}	Output Capacitance			510		pF
C _{rss}	Reverse Transfer Capacitance			350		

Diode Characteristics

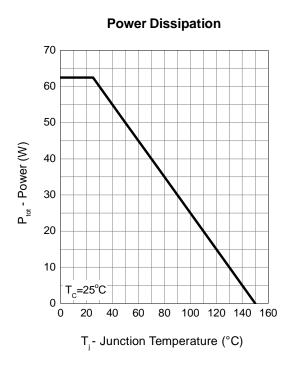
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I _S	Continuous Source Current ^{1,6}	V =V =0V Force Current			20	Α
I _{SM}	Pulsed Source Current ^{2,6}	$V_G=V_D=0V$, Force Current			70	Α
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =1A , T _J =25℃			1.1	V
t _{rr}	Reverse Recovery Time			14.8		nS
Qrr	Reverse Recovery Charge	lF=40A,dl/dt=100A/μs,T _J =25℃		3.9		nC

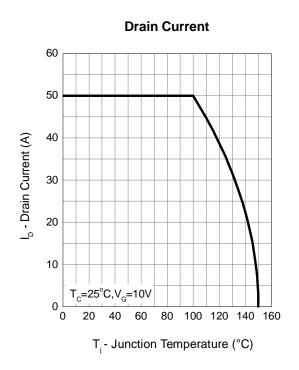
Note d : Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2\%.$

Note e: Guaranteed by design, not subject to production testing.

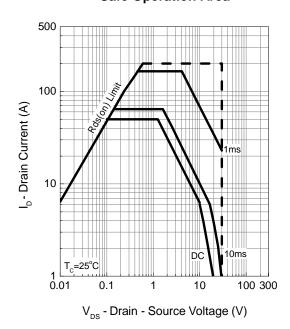


Typical Operating Characteristics

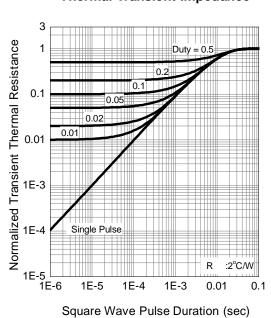




Safe Operation Area



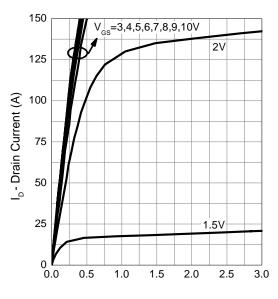
Thermal Transient Impedance





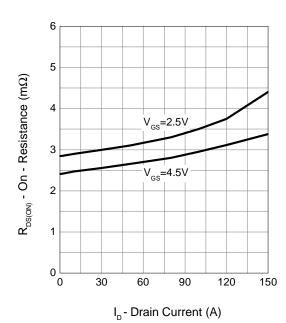
Typical Operating Characteristics (Cont.)

Output Characteristics

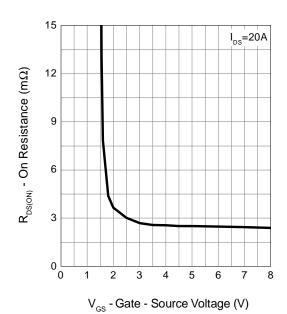


V_{DS} - Drain-Source Voltage (V)

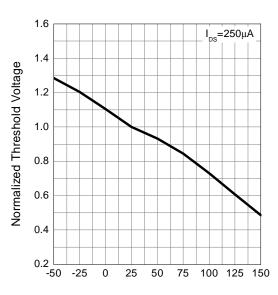
Drain-Source On Resistance



Gate-Source On Resistance



Gate Threshold Voltage

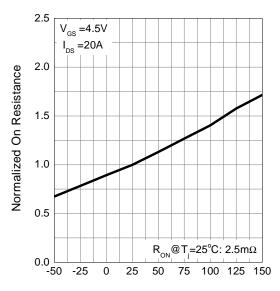


T_i - Junction Temperature (°C)



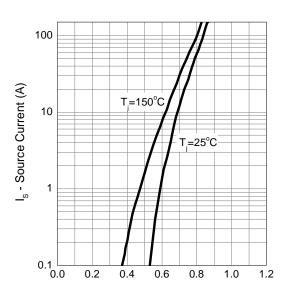
Typical Operating Characteristics (Cont.)

Drain-Source On Resistance



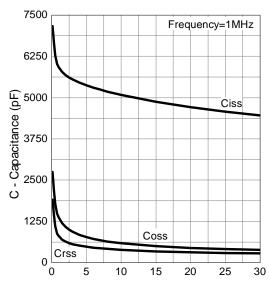
T_i - Junction Temperature (°C)

Source-Drain Diode Forward



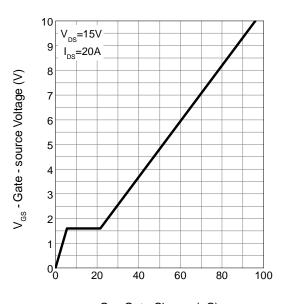
V_{SD} - Source - Drain Voltage (V)

Capacitance



 $V_{\scriptscriptstyle DS}$ - Drain - Source Voltage (V)

Gate Charge



Q_G - Gate Charge (nC)



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