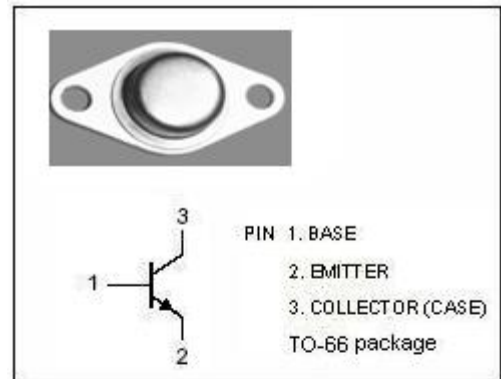


**isc Silicon NPN Power Transistor**
**2N4298**
**DESCRIPTION**

- Excellent Safe Operating Area
- Low Collector-Emitter Saturation Voltage
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.

**APPLICATIONS**

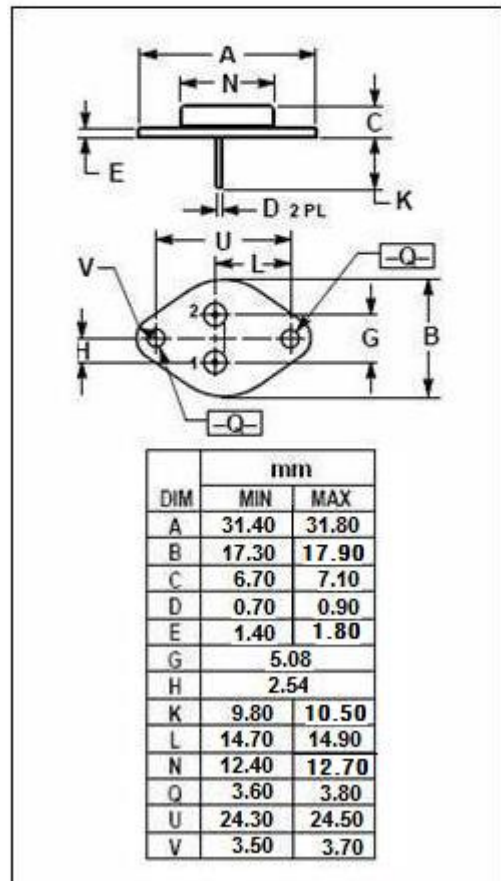
- Designed for switching regulator applications where high frequency and high voltage swings and required


**ABSOLUTE MAXIMUM RATINGS(T<sub>a</sub>=25°C)**

SYMBOL	PARAMETER	VALUE	UNIT
V <sub>CBO</sub>	Collector-Base Voltage	500	V
V <sub>CEO</sub>	Collector-Emitter Voltage	350	V
V <sub>EBO</sub>	Emitter-Base Voltage	4	V
I <sub>C</sub>	Collector Current-Continuous	1	A
P <sub>C</sub>	Collector Power Dissipation@T <sub>C</sub> =25°C	20	W
T <sub>J</sub>	Junction Temperature	-65~175	°C
T <sub>stg</sub>	Storage Temperature	-65~175	°C

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	MAX	UNIT
R <sub>th j-c</sub>	Thermal Resistance, Junction to Case	7.5	°C/W



**isc Silicon NPN Power Transistor****2N4298****ELECTRICAL CHARACTERISTICS** $T_C=25^{\circ}\text{C}$  unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
$V_{CEO(SUS)}^*$	Collector-Emitter Sustaining Voltage	$I_C=50\text{mA}; I_B=0$	350		V
$I_{CBO}$	Collector Cutoff Current	$V_{CE}=500\text{V}; I_B=0$		0.1	mA
$I_{EBO}$	Emitter Cutoff Current	$V_{EB}=4\text{V}; I_C=0$		0.1	mA
$V_{CE(sat)}^*$	Collector-Emitter Saturation Voltage	$I_C=50\text{mA}; I_B=5\text{mA}$		0.9	V
$V_{BE(sat)}^*$	Base-Emitter Saturation Voltage	$I_C=50\text{mA}; I_B=5\text{mA}$		1.5	V
$V_{BE(ON)}^*$	Base-Emitter On Voltage	$I_C=0.1\text{A}; V_{CE}=10\text{V}$		0.9	V
$h_{FE-1}^*$	DC Current Gain	$I_C=5\text{mA}; V_{CE}=10\text{V}$	20		
$h_{FE-2}^*$	DC Current Gain	$I_C=50\text{mA}; V_{CE}=10\text{V}$	25	75	
$h_{FE-3}^*$	DC Current Gain	$I_C=0.1\text{A}; V_{CE}=10\text{V}$	20		

\*:Pulse test:Pulse width=300us,duty cycle $\leq$ 2%**NOTICE:**

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