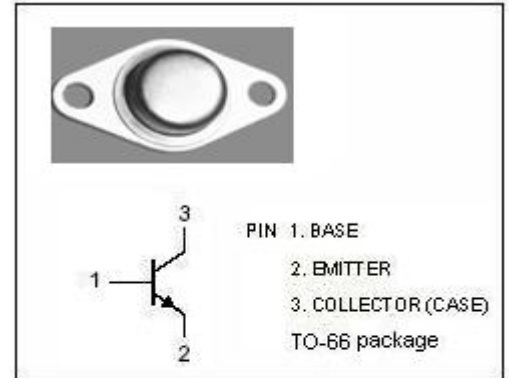


**isc Silicon NPN Power Transistor**
**2N6495**
**DESCRIPTION**

- Collector-Emitter Sustaining Voltage-  
:  $V_{CEO} = 80V(\text{Min.})$
- With TO-66 package
- Low collector saturation
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation


**APPLICATIONS**

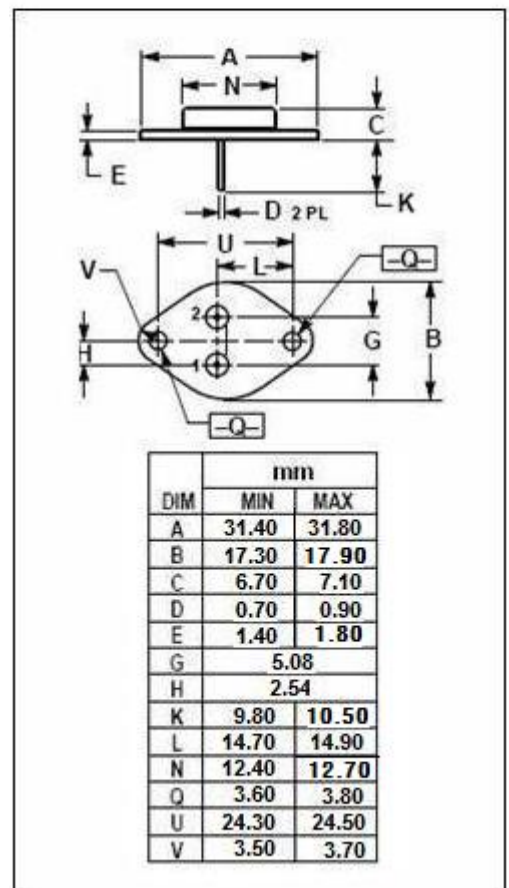
- Designed for switching and wide-band amplifier applications.

**ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ\text{C}$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	150	V
$V_{CEO}$	Collector-Emitter Voltage	80	V
$V_{EBO}$	Emitter-Base Voltage	7	V
$I_C$	Collector Current-Continuous	10	A
$P_C$	Collector Power Dissipation@ $T_C=25^\circ\text{C}$	70	W
$T_J$	Junction Temperature	150	$^\circ\text{C}$
$T_{stg}$	Storage Temperature	-65~200	$^\circ\text{C}$

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	MAX	UNIT
$R_{th\ j-c}$	Thermal Resistance, Junction to Case	4.37	$^\circ\text{C}/\text{W}$



**isc Silicon NPN Power Transistor****2N6495****ELECTRICAL CHARACTERISTICS****T<sub>C</sub>=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
V <sub>CEQ(SUS)</sub>	Collector-Emitter Sustaining Voltage	I <sub>C</sub> = 50mA ; I <sub>B</sub> = 0	80			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> =10A; I <sub>B</sub> = 1A			1.5	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> =10A; I <sub>B</sub> = 1A			2.0	V
V <sub>BE(on)</sub>	Base-Emitter On Voltage	I <sub>C</sub> = 10A ; V <sub>CE</sub> = 3V			2.8	V
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = 7V; I <sub>C</sub> = 0			0.1	mA
I <sub>CEO</sub>	Collector Cutoff Current	V <sub>CE</sub> = 40V; I <sub>B</sub> =0			0.1	mA
I <sub>CBO</sub>	Collector Base Cutoff Current	V <sub>CB</sub> =150V; I <sub>E</sub> = 0			0.1	mA
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = 10A; V <sub>CE</sub> = 3V	10		60	
f <sub>T</sub>	Current Gain-Bandwidth Product	I <sub>C</sub> = 1A; V <sub>CE</sub> = 10V		25		MHz

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