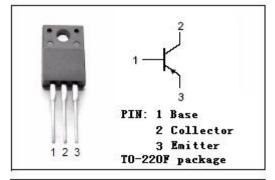


isc Silicon PNP Power Transistor

DESCRIPTION

- · Collector-Emitter Breakdown Voltage-
 - : V_{(BR)CEO}= -100V(Min)
- · Collector Power Dissipation-
 - : Pc= 30W@ Tc= 25℃
- · Low Collector Saturation Voltage-
 - : $V_{CE(sat)} = -2.0V(Max)@ (I_C = -4A, I_B = -0.4A)$
- Complement to Type 2SD2059
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

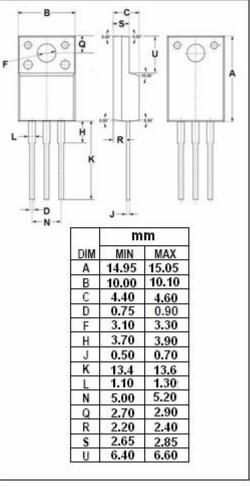


APPLICATIONS

· Designed for general purpose applications.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector-Base Voltage	-100	V	
V _{CEO}	Collector-Emitter Voltage	-100	V	
V _{EBO}	Emitter-Base Voltage	-5	V	
Ic	Collector Current-Continuous	-5	А	
I _B	Base Current-Continuous	-0.5	Α	
Pc	Collector Power Dissipation @T _C =25°C	30	W	
TJ	Junction Temperature	150	$^{\circ}$	
T _{stg}	Storage Temperature	-55~150	$^{\circ}$	





isc Silicon PNP Power Transistor

2SB1367

ELECTRICAL CHARACTERISTICS

Tj=25℃ unless otherwise specified

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SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -50mA; I _B = 0	-100			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -4A; I _B = -0.4A			-2.0	V
V _{BE} (on)	Base-Emitter On Voltage	I _C = -4A; V _{CE} = -5V			-1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -100V; I _E = 0			-100	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0			-1	mA
h _{FE-1}	DC Current Gain	I _C = -1A; V _{CE} = -5V	40		240	
h _{FE-2}	DC Current Gain	I _C = -4A; V _{CE} = -5V	20			

♦ h_{FE-1} Classifications

R	0	Y
40-80	70-140	120-240

NOTICE:

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