

isc Silicon NPN Power Transistor
2SC1722
DESCRIPTION

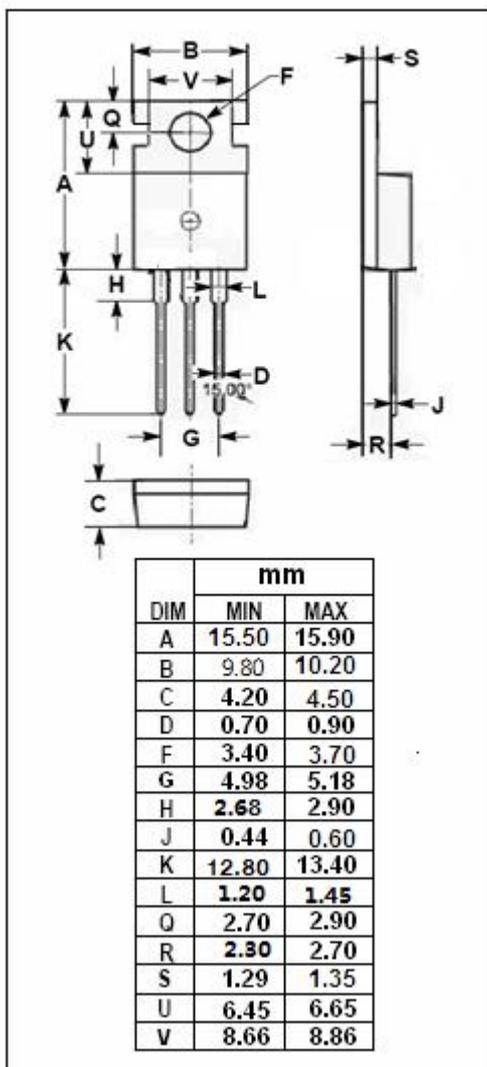
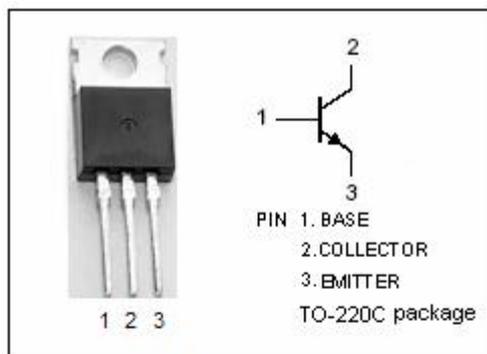
- Silicon NPN triple diffused mesa
- High breakdown voltage
- Large collector dissipation
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Low frequency power amplifier
- TV horizontal/vertical drivers

ABSOLUTE MAXIMUM RATINGS (T_a=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CB0}	Collector-Base Voltage	300	V
V _{CEO}	Collector-Emitter Voltage	300	V
V _{EBO}	Emitter-Base Voltage	5	V
I _c	Collector Current-Continuous	0.2	A
P _c	Collector Power Dissipation @ T _c =25°C	1.8	W
T _J	Junction Temperature	150	°C
T _{stg}	Storage Temperature Range	-45~150	°C



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ELECTRICAL CHARACTERISTICS

 T_c=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 5mA ; I _B = 0	300			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 50mA; I _B = 5mA			2.0	V
V _{BE(ON)}	Base-Emitter ON Voltage	I _C = 50mA ; V _{CE} = 10V			0.9	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 250V ; I _E = 0			0.1	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5V; I _C = 0			10	μ A
h _{FE}	DC Current Gain	I _C = 50mA ; V _{CE} = 10V	50		300	

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