

isc Silicon NPN Power Transistor

DESCRIPTION

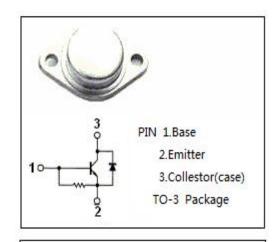
- · High Breakdown Voltage-
- : V_{CBO}= 1500V (Min)
- · Low Collector Saturation Voltage-
- : V_{CE(sat)}= 5.0V(Max.)@ I_C= 2.5A
- · Built-in Damper Diode
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

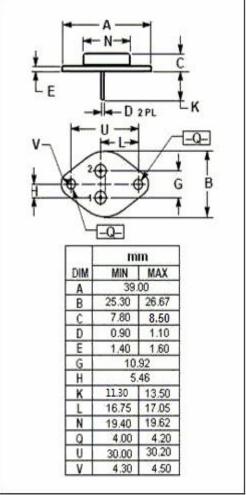


• Designed for line-operated horizontal deflection output applications.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector-Base Voltage	V		
V _{CES}	Collector-Emitter Voltage	1500	V	
V _{EBO}	Emitter-Base Voltage 5		V	
Ic	Collector Current- Continuous	3	А	
I _{CP}	Collector Current- Peak	5	А	
Pc	Collector Power Dissipation @ T _C = 25 °C	65	W	
TJ	Junction Temperature	130	$^{\circ}$	
T _{stg}	Storage Temperature Range	-65~130	$^{\circ}$	







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2SD951

ELECTRICAL CHARACTERISTICS

Tc=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{EBO}	Emitter-Base Breakdown Voltage	I _E = 200mA; I _C = 0	5			V
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 2.5A; I _B = 0.8A			5.0	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = 2.5A; I _B = 0.8A			1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 750V; I _E = 0			50	μA
		V _{CB} = 1500V; I _E = 0			1	mA
h _{FE}	DC Current Gain	I _C = 2.5A; V _{CE} = 10V	3		12	
V _{ECF}	C-E Diode Forward Voltage	I _F = 4A			1.7	V
t _{stg}	Storage Time	· I _C = 2.5A, I _{Bend} = 0.8A; L _B = 5 μ H		11		μ s
t _f	Fall Time	IC- Z.JA, IBend- U.OA, LB- 3 µ П			0.9	μ s

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