

Isc N-Channel MOSFET Transistor

IRFS41N15D

• FEATURES

- With To-262 package
- Low input capacitance and gate charge
- Low gate input resistance
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

• APPLICATIONS

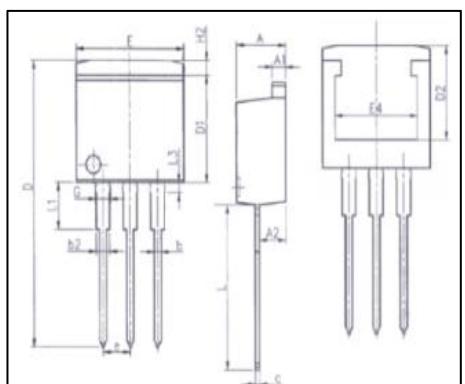
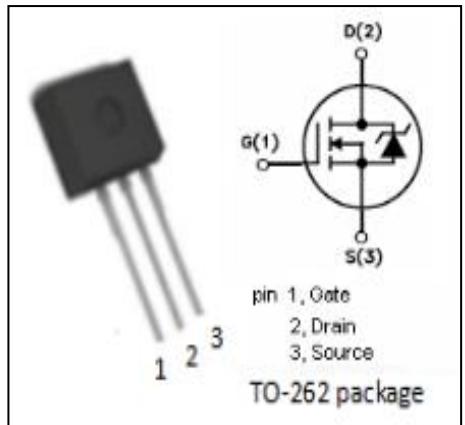
- Switching applications

• ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DSS}	Drain-Source Voltage	150	V
V_{GSS}	Gate-Source Voltage	± 30	V
I_D	Drain Current-Continuous $T_c=25^\circ\text{C}$ $T_c=100^\circ\text{C}$	41 29	A
I_{DM}	Drain Current-Single Pulsed	164	A
P_D	Total Dissipation @ $T_c=25^\circ\text{C}$	200	W
T_{ch}	Max. Operating Junction Temperature	175	°C
T_{stg}	Storage Temperature	-55~175	°C

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	0.75	°C/W



DIM	mm	
	MIN	MAX
A	4.37	4.77
A1	1.22	1.42
A2	2.47	2.87
b	0.70	0.97
b2	1.17	1.42
c	0.28	0.53
D	23.20	24.02
D1	8.38	8.90
D2	6.00	—
E	9.90	10.39
E4	7.30	—
e	2.54BSC	
G	1.25	1.50
H2	—	1.31
L	13.34	14.10
L1	3.30	4.06
L3	0.95	1.15

Isc N-Channel MOSFET Transistor**IRFS41N15D****ELECTRICAL CHARACTERISTICS****T_c=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V; I _D =0.25mA	150			V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} ; I _D =0.25mA	3.0		5.5	V
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} = 10V; I _D =25A			45	mΩ
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ± 20V; V _{DS} =0V			± 0.1	μA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =150V; V _{GS} = 0V; T _j =25°C V _{DS} =120V; V _{GS} = 0V; T _j =125°C			25 250	μA
V _{SDF}	Diode forward voltage	I _{SD} =25A, V _{GS} = 0 V			1.3	V

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