

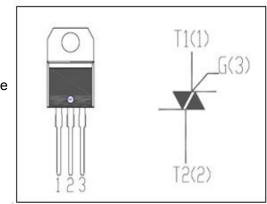
# isc Thyristors Q6025R5

#### **DESCRIPTION**

- With TO-220 packaging
- Operating in 3 quadrants
- · High commutation capability
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### **APPLICATIONS**

- · Solid state relays; heating and cooking appliances
- · Switching applications



## ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER			UNIT
$V_{DRM}$	Repetitive peak off-state voltage			V
V <sub>RRM</sub>	Repetitive peak reverse voltage	4	600	V
I <sub>T(RSM)</sub>	Average on-state current @Tc=110°C			Α
I <sub>TSM</sub>	Surge non-repetitive on-state current	50HZ 60HZ	167 200	Α
P <sub>G(AV)</sub>	Average gate power dissipation ( over any 20 ms period )			W
T <sub>j</sub>	Operating junction temperature			$^{\circ}\!\mathbb{C}$
T <sub>stg</sub>	Storage temperature			$^{\circ}\!\mathbb{C}$

## ELECTRICAL CHARACTERISTICS (T<sub>c</sub>=25°C unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS			MIN	MAX	UNIT
I <sub>RRM</sub>	Repetitive peak reverse current	V <sub>R</sub> =V <sub>RRM</sub> Rated;	Tj=25℃			0.1	
I <sub>DRM</sub>	Repetitive peak off-state current	V <sub>D</sub> =V <sub>DRM</sub> Rated;	Tj=100℃   Tj=125℃			1.0 3.0	mA
V <sub>TM</sub>	On-state voltage	I <sub>T</sub> =25A				1.8	V
I <sub>GT</sub>	Gate-trigger current	$V_D$ =12V;RG=330 $\Omega$ ; II III			50		
				II		50	mA
				III		50	
V <sub>GT</sub>	Gate-trigger voltage	$V_D$ =12V;RG=330 $\Omega$ ;RL=6 $\Omega$				2.5	V
Rth (j-c)	Junction to case	Half cycle				0.89	°C/W

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