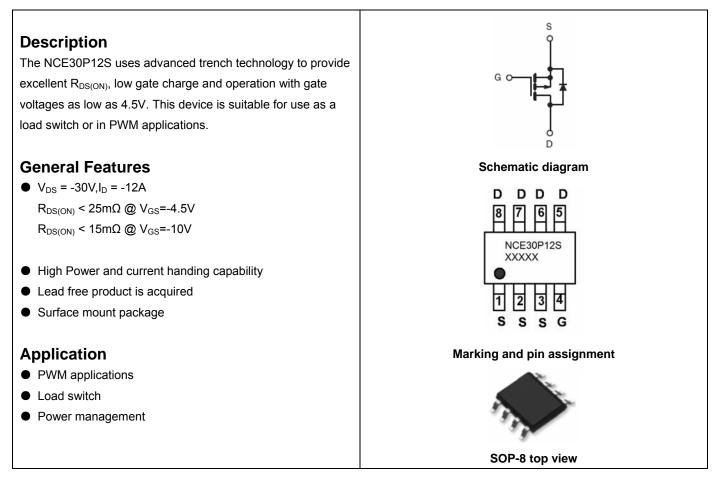


# NCE P-Channel Enhancement Mode Power MOSFET



## **Package Marking and Ordering Information**

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Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE30P12S	NCE30P12S	SOP-8	Ø330mm	12mm	4000 units

## Absolute Maximum Ratings (T<sub>A</sub>=25℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	Vds	-30	V	
Gate-Source Voltage	Vgs	±20	V	
Drain Current-Continuous (T $_{C}$ =25 $^{\circ}C$ )		-12	Δ	
Drain Current-Continuous (T <sub>C</sub> =100℃)	l <sub>D</sub>	-8.4	A	
Drain Current-Pulsed (Note 1)	I <sub>DM</sub>	-48	A	
Maximum Power Dissipation (T <sub>c</sub> =25 $^{\circ}$ C)	- P <sub>D</sub>	3	W	
Maximum Power Dissipation (T_c=100 $^\circ \! {\rm C}$ )	ΓD	1.3	vv	
Single pulse avalanche energy (Note 5)	E <sub>AS</sub>	231	mJ	
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 To 150	°C	

### **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ extsf{ heta}JA}$	41.67	°C <b>/W</b>	
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# Electrical Characteristics (T\_A=25 $^\circ\!\!\mathrm{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250µA	-30	-33	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-30V,V <sub>GS</sub> =0V	-	-	-1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250µA	-1	-1.5	-2.2	V
		V <sub>GS</sub> =-10V, I <sub>D</sub> =-10A	-	11.5	15	mΩ
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-7A	-	18	25	mΩ
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =-10V,I <sub>D</sub> =-10A	-	20	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>		-	2419	-	PF
Output Capacitance	Coss	- V <sub>DS</sub> =-15V,V <sub>GS</sub> =0V, F=1.0MHz	-	318	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	262	-	PF
Switching Characteristics (Note 4)			•			
Turn-on Delay Time	t <sub>d(on)</sub>		-	9	-	nS
Turn-on Rise Time	tr	V <sub>DD</sub> =-15V, ID=-10A,	-	8	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =-10V, $R_{GEN}$ =1 $\Omega$	-	28	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	10	-	nS
Total Gate Charge	Qg		-	44.4	-	nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =-15V,I <sub>D</sub> =-10A,V <sub>GS</sub> =-10V	-	4.6	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	10	-	nC
Drain-Source Diode Characteristics						
Diode Forward Current (Note 2)	I <sub>S</sub>		-	-	-12	А
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =-12A	-	-	-1.2	V

### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- **3.** Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production
- **5.** EAS condition: Tj=25 $^\circ C$  ,V\_DD=-15V,V\_G=10V,L=0.5mH,Rg=25\Omega, IAS=-34A



## **Typical Electrical and Thermal Characteristics**

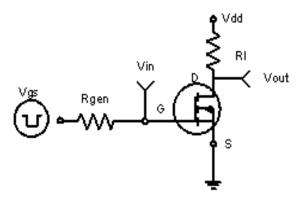
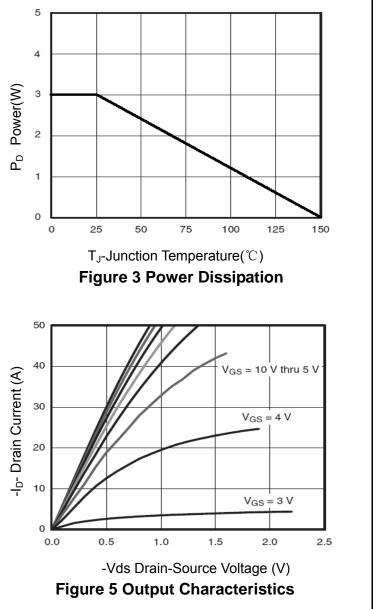
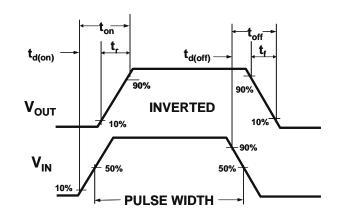


Figure 1:Switching Test Circuit







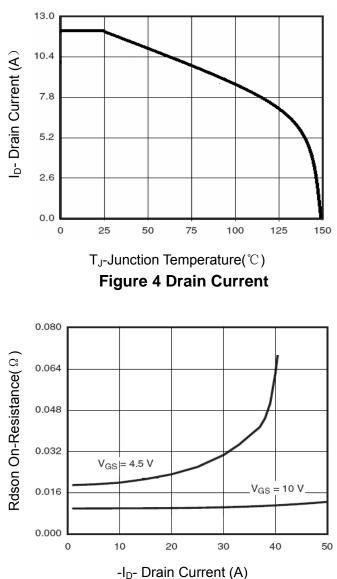
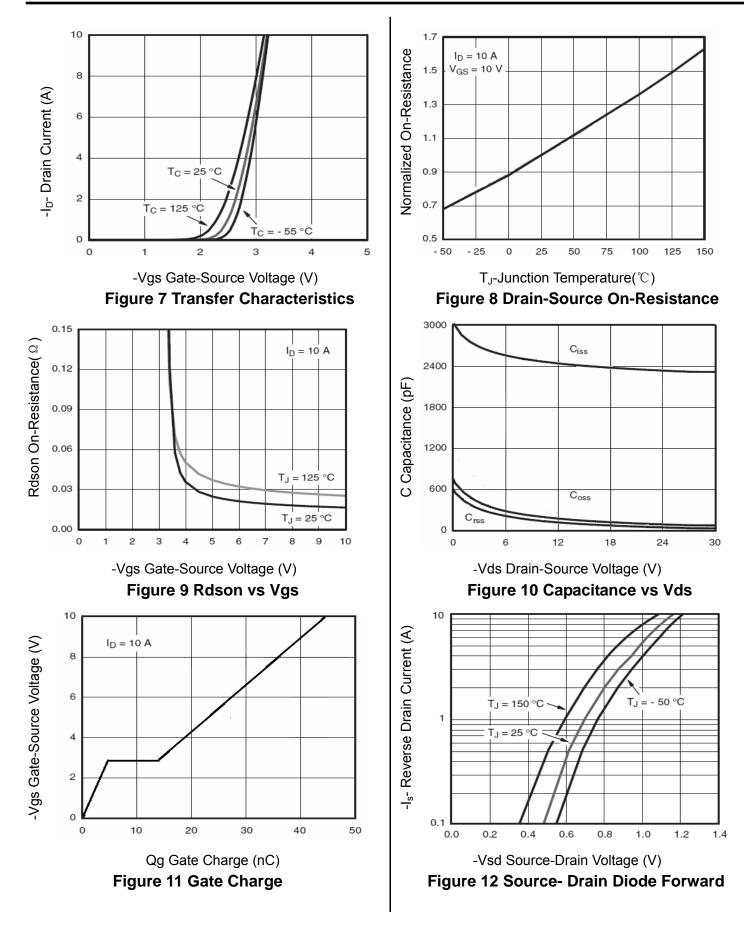


Figure 6 Drain-Source On-Resistance



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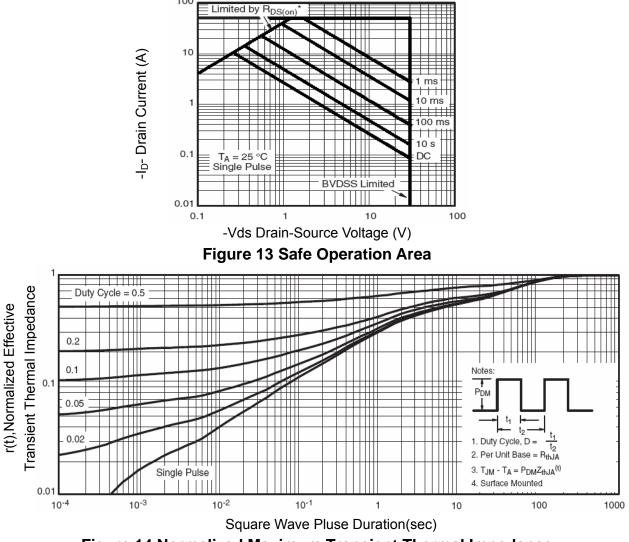
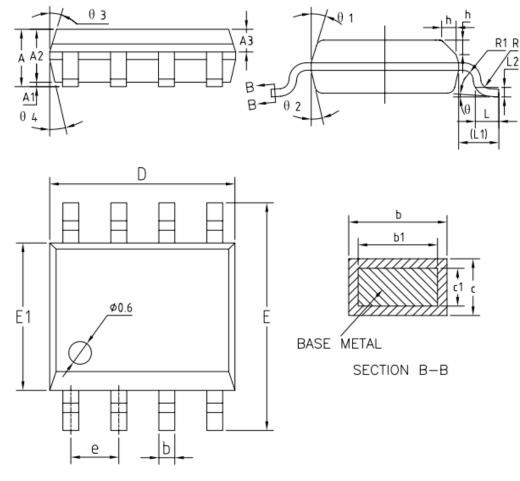


Figure 14 Normalized Maximum Transient Thermal Impedance



# **SOP-8 Package Information**



(UNITS OF MEASURE=MILLIMETER)				
SYMBOL	MIN	NOM	MAX	
A	1.35	1.55	1.75	
A1	0.10	0.15	0.25	
A2	1.25	1.40	1.65	
A3	0.50	0.60	0.70	
b	0.38	-	0.51	
Ь1	0.37	0.42	0.47	
с	0.18	—	0.25	
c1	0.17	0.20	0.23	
D	4.80	4.90	5.00	
E	5.80	6.00	6.20	
E1	3.80	3.90	4.00	
е	1.17	1.27	1.37	
L L1	0.45	0.60	0.80	
L1		1.04REF		
L2	0.25BSC			
R	0.07	-	-	
R1	0.07	—	-	
h	0.30	0.40	0.50	
θ	0•	-	8*	
θ1	15 <b>'</b>	17 <b>°</b>	19'	
θ2	11	13*	15 <b>°</b>	
θ3	15 <b>'</b>	17'	19 <b>'</b>	
θ4	11'	13°	15'	

### COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)



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